

MITSUBISHI RF POWER TRANSISTOR 2SC2904

NPN EPITAXIAL PLANAR TYPE

DISCRIPTION

2SC2904 is a silicon NPN epitaxial planar type transistor specifically designed for high power amplifiers in HF band.

FEATURES

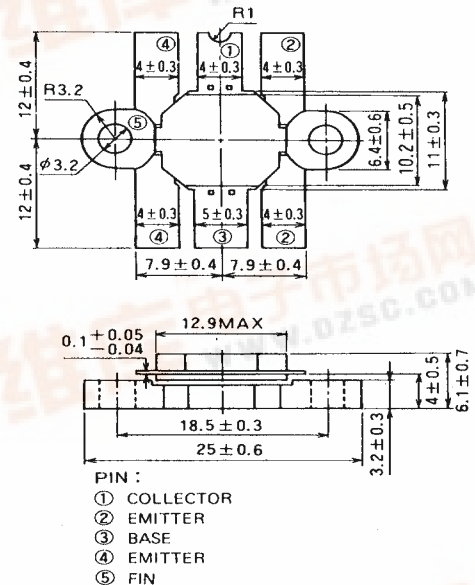
- High gain: $G_{pe} \geq 11.5\text{dB}$
@ $V_{CC} = 12.5\text{V}$, $P_o = 100\text{W}$, $f = 30\text{MHz}$
- High ruggedness: Ability to withstand 20:1 load VSWR when operated at $f = 30\text{MHz}$
 $P_o = 100\text{W}$, $V_{CC} = 15.2\text{V}$
- Emitter ballansted construction
- Low thermal resistance ceramic package with flange.

APPLICATION

Output stage of transmitter in HF band SSB mobile radio sets.

OUTLINE DRAWING

Dimensions in mm



T-40

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Ratings	Unit
V_{CBO}	Collector to base voltage		50	V
V_{EBO}	Emitter to base voltage		5	V
V_{CEO}	Collector to emitter voltage	$R_{BE} = \infty$	20	V
I_C	Collector current		22	A
P_C	Collector dissipation	$T_a = 25^\circ\text{C}$	7.8	W
		$T_C = 25^\circ\text{C}$	200	W
T_j	Junction temperature		175	$^\circ\text{C}$
T_{stg}	Storage temperature		-55 to 175	$^\circ\text{C}$
R_{th-c}	Thermal resistance		0.75	$^\circ\text{C/W}$

Note. Above parameters are guaranteed independently.

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
$V_{(BR)EBO}$	Emitter to base breakdown voltage	$I_E = 20\text{mA}$, $I_C = 0$	5			V
$V_{(BR)CBO}$	Collector to base breakdown voltage	$I_C = 20\text{mA}$, $I_E = 0$	50			V
$V_{(BR)CEO}$	Collector to emitter breakdown voltage	$I_C = 100\text{mA}$, $R_{BE} = \infty$	20			V
I_{CBO}	Collector cutoff current	$V_{CB} = 15\text{V}$, $I_E = 0$			5	mA
I_{EBO}	Emitter cutoff current	$V_{EB} = 3\text{V}$, $I_C = 0$			5	mA
h_{FE}	DC forward current gain *	$V_{CE} = 10\text{V}$, $I_C = 1\text{A}$	10	50	180	—
P_o	Output power	$f = 30\text{MHz}$, $V_{CC} = 12.5\text{V}$, $P_{in} = 7\text{W}$	100	110		W
η_C	Collector efficiency		55	60		%

Note. * Pulse test, $P_W = 150\mu\text{s}$, duty = 5%.

Above parameters, ratings, limits and conditions are subject to change.

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The schematic diagram shows a common-emitter amplifier circuit with input and output matching networks. The input matching network consists of a series capacitor C_1 (10D, 6T, 3P) and a series inductor (4T, 2T) connected to the base of the transistor. The base is biased with a 10 Ω resistor and a 160 pF capacitor. The output matching network consists of a series inductor (10D, 4T, 5P) connected to the collector, which is biased with a 10D, 17T, 2P capacitor and a 10 Ω resistor. The output is taken from the collector through a series capacitor (to 110 pF) and a series inductor (to 110 pF). The circuit is powered by a +V_{CC} supply and has a 30-MHz operating frequency.

A line graph showing the DC current gain (h_{FE}) of a 2N3055 transistor as a function of collector current (I_C). The y-axis represents h_{FE} and ranges from 30 to 130 in increments of 20. The x-axis represents I_C in Amperes (A) on a logarithmic scale, with major ticks at 0.1, 0.2, 0.5, 1, 5, 10, 20, 50, and 100. The graph is for $T_C = 25^\circ\text{C}$ and $V_{CE} = 10\text{V}$. The curve starts at approximately $h_{FE} = 40$ for $I_C = 0.1\text{A}$, rises steadily to a peak of about 80 at $I_C = 20\text{A}$, and then slightly decreases to approximately 78 at $I_C = 25\text{A}$.

Collector Current I_C (A)	DC Current Gain h_{FE}
0.1	40
0.2	42
0.5	48
1	52
5	65
10	72
20	80
25	78

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