

2SD2479

Silicon NPN epitaxial planar type Darlington

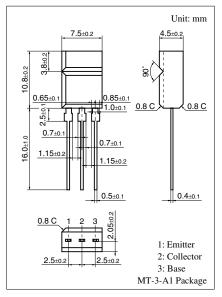
For low-frequency amplification

■ Features

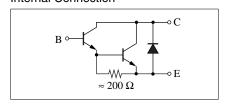
- High forward current transfer ratio h_{FE}
- Allowing automatic insertion with radial taping

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit
Collector to base voltage	V _{CBO}	120	V
Collector to emitter voltage	V _{CEO}	100	V
Emitter to base voltage	V _{EBO}	5	V
Peak collector current	I _{CP}	3	A
Collector current	I_{C}	2	A
Collector power dissipation	P _C	1.5	W
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-55 to +150	°C



Internal Connection



\blacksquare Electrical Characteristics $~T_a = 25^{\circ}C \pm 2^{\circ}C$

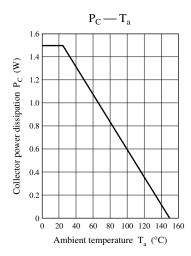
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector cutoff current	I_{CBO}	$V_{CB} = 25 \text{ V}, I_E = 0$			0.1	μΑ
Emitter cutoff current	I_{EBO}	$V_{EB} = 4 \text{ V}, I_{C} = 0$			1	μΑ
Collector to base voltage	V_{CBO}	$I_C = 100 \ \mu A, I_E = 0$	120			V
Collector to emitter voltage	V _{CEO}	$I_{\rm C} = 1 \text{mA}, I_{\rm B} = 0$	100			V
Emitter to base voltage	V_{EBO}	$I_E = 100 \ \mu A, I_C = 0$	5			V
DC current gain *1, 2	h _{FE}	$V_{CE} = 10 \text{ V}, I_{C} = 1 \text{ A}$	4000		40 000	
Collector to emitter saturation voltage *1	V _{CE(sat)}	$I_C = 1 A, I_B = 1 mA$			1.5	V
Base to emitter saturation voltage *1	V _{BE(sat)}	$I_C = 1 A, I_B = 1 mA$			2	V
Gain bandwidth product	f_T	$V_{CB} = 10 \text{ V}, I_E = -50 \text{ mA}, f = 200 \text{ MHz}$		150		MHz

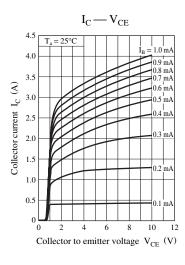
Note) *1: Pulse measurement

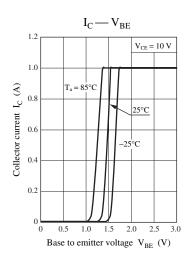
*2: Rank classification

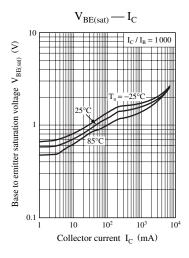
Rank	Q	R	S
h_{FE}	4000 to 10000	8000 to 20000	16000 to 40000

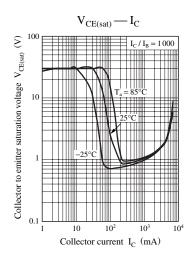
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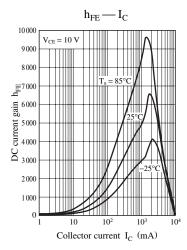


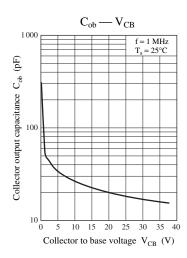












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