2SC4606

Silicon NPN epitaxial planar type

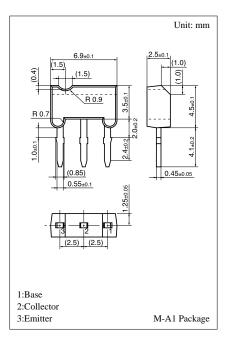
For low-frequency driver amplification Complementary to 2SA1762

Features

- High collector to emitter voltage V_{CEO}.
- Optimum for the driver stage of a low-frequency and 25 to 30W output amplifier.
- M type package allowing easy automatic and manual insertion as well as stand-alone fixing to the printed circuit board.

Parameter	Symbol	Ratings	Unit
Collector to base voltage	V _{CBO}	80	V
Collector to emitter voltage	V _{CEO}	80	V
Emitter to base voltage	V _{EBO}	5	V
Peak collector current	I _{CP}	1	А
Collector current	I _C	0.5	А
Collector power dissipation	P_{C}^{*}	1	W
Junction temperature	Tj	150	°C
Storage temperature	T _{stg}	-55 ~ +150	°C

Absolute Maximum Ratings (Ta=25°C)



* Printed circuit board: Copper foil area of 1cm² or more, and the board thickness of 1.7mm for the collector portion

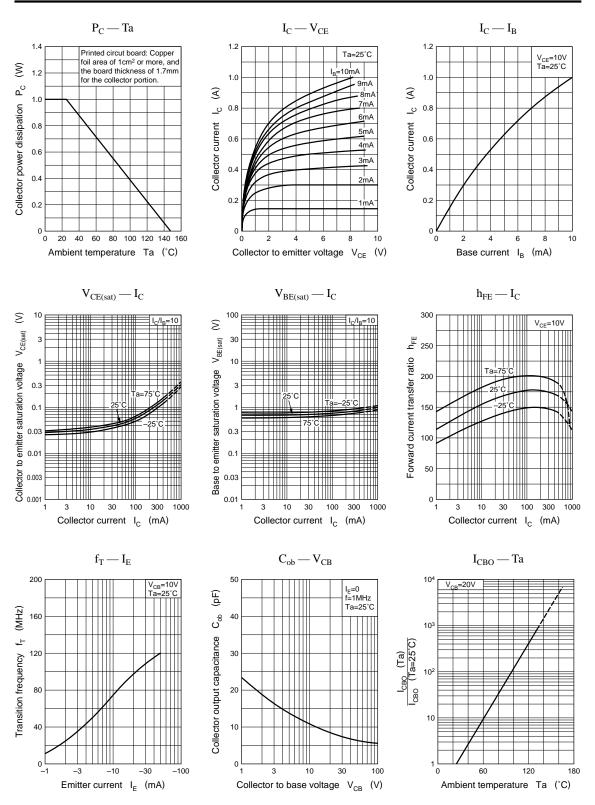
Electrical Characteristics (Ta=25°C)

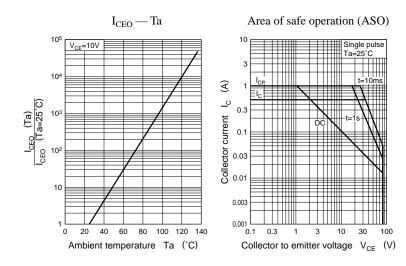
Parameter	Symbol	Conditions	min	typ	max	Unit
Collector cutoff current	I _{CBO}	$V_{CB} = 20V, I_E = 0$			0.1	μA
Collector to base voltage	V _{CBO}	$I_{\rm C} = 10 \mu A, I_{\rm E} = 0$	80			V
Collector to emitter voltage	V _{CEO}	$I_{\rm C} = 100 \mu A, I_{\rm B} = 0$	80			v
Emitter to base voltage	V _{EBO}	$I_{\rm E} = 10 \mu A, I_{\rm C} = 0$	5			v
	h _{FE1} *1	$V_{CE} = 10V, I_{C} = 150mA^{*2}$	130		330	
Forward current transfer ratio	h _{FE2}	$V_{CE} = 5V, I_C = 500mA^{*2}$	50	100		
Collector to emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 300 {\rm mA}, I_{\rm B} = 30 {\rm mA}^{*2}$		0.2	0.4	v
Base to emitter saturation voltage	V _{BE(sat)}	$I_{\rm C} = 300 {\rm mA}, I_{\rm B} = 30 {\rm mA}^{*2}$		0.85	1.2	v
Transition frequency	f _T	$V_{CB} = 10V, I_E = -50mA, f = 200MHz$		120		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10V, I_E = 0, f = 1MHz$		11	20	pF

*1hFE1 Rank classification

Rank	R	S
h _{FE1}	130 ~ 220	185 ~ 330

*2 Pulse measurement





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