2SB1322A

Silicon PNP epitaxial planer type

For low-frequency power amplification Complementary to 2SD1994A

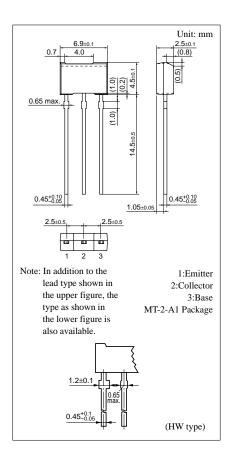
Features

• Allowing supply with the radial taping.

Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	V_{CBO}	-60	V
Collector to emitter voltage	V_{CEO}	-50	V
Emitter to base voltage	V _{EBO}	-5	V
Peak collector current	I_{CP}	-1.5	A
Collector current	I_{C}	-1	A
Collector power dissipation	P_{C}^{*}	1	W
Junction temperature	T _j	150	°C
Storage temperature	$T_{\rm stg}$	−55 ~ +150	°C

 $^{^{\}ast}$ Printed circuit board: Copper foil area of 1cm² or more, and the board thickness of 1.7mm for the collector portion



Electrical Characteristics (Ta=25°C)

Parameter	Symbol	Conditions	min	typ	max	Unit
Collector cutoff current	I_{CBO}	$V_{CB} = -20V, I_{E} = 0$			- 0.1	μΑ
Collector to base voltage	V _{CBO}	$I_{\rm C} = -10 \mu A, I_{\rm E} = 0$	-60			V
Collector to emitter voltage	V _{CEO}	$I_{C} = -2mA, I_{B} = 0$	-50			V
Emitter to base voltage	V _{EBO}	$I_E = -10\mu A, I_C = 0$	-5			V
Forward current transfer ratio	h _{FE1} *1	$V_{CE} = -10V, I_{C} = -500 \text{mA}^{*2}$	85		340	
	h _{FE2}	$V_{CE} = -5V, I_C = -1 \text{mA}^{*2}$	50			
Collector to emitter saturation voltage	V _{CE(sat)}	$I_C = -500 \text{mA}, I_B = -50 \text{mA}^{*2}$			- 0.4	V
Base to emitter saturation voltage	V _{BE(sat)}	$I_C = -500 \text{mA}, I_B = -50 \text{mA}^{*2}$			-1.2	V
Transition frequency	f_{T}	$V_{CB} = -10V$, $I_E = 50mA$, $f = 200MHz$		200		MHz
Collector output capacitance	C _{ob}	$V_{CB} = -10V, I_E = 0, f = 1MHz$		20	30	pF

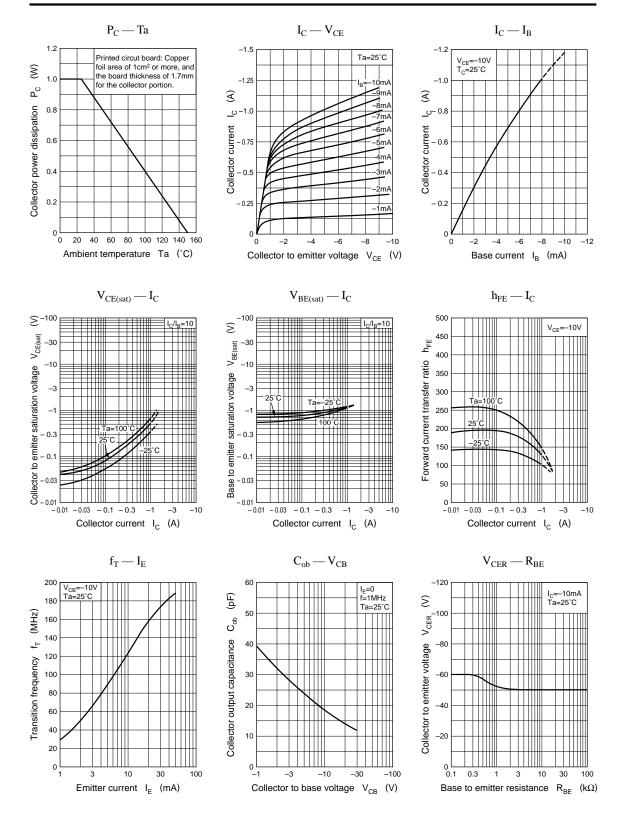
^{*2} Pulse measurement

^{*1}hFE1 Rank classification

Rank	Q	R	S
h_{FE1}	85 ~ 170	120 ~ 240	170 ~ 340

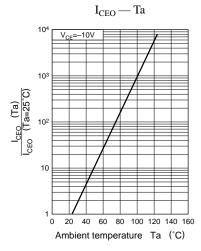
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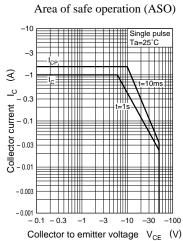
Transistor 2SB1322A



Panasonic 255

Transistor 2SB1322A





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