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02/05/99

Errata: CS4954/55 Rev. B

(Reference CS4954/55 Data Sheet revision DS278PP3 dated Jul '98)

A)   FIELD (pin 9) is always an output.
     Refer to page 4 CS4954 Datasheet

     DC CHARACTERISTICS
            Inputs
                    High Level Input Voltage

     SHOULD READ   HSYNC/VSYNC/CLKIN
     ORIGINAL TEXT   HSYNC/VSYNC/*FIELD*/CLKIN

     CLARIFICATION: The FIELD pin (pin 9) remains an output pin in SLAVE
     mode. However, the FIELD pin state does not toggle in SLAVE mode and
     its output state should be considered random

B)   CONTROL REGISTER 4 CS4954 Datasheet page 42

     Bit 6 CB_FLD_SEL FUNCTION description

     SHOULD READ: Composite Blank / FIELD output select (1=CB
                         select, 0=FIELD select)
     ORIGINAL TEXT: Composite Blank / HSYNC output select (1=CB
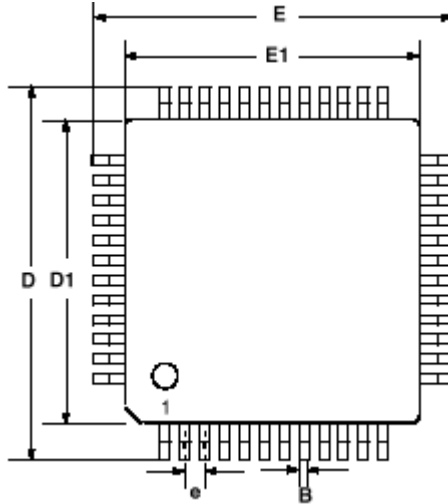   select, 0=HSYNC select)

C)   75 Ω output filter CS4954 Datasheet Page 69

     Fig.2
     330 pf should be replaced by 33 pf

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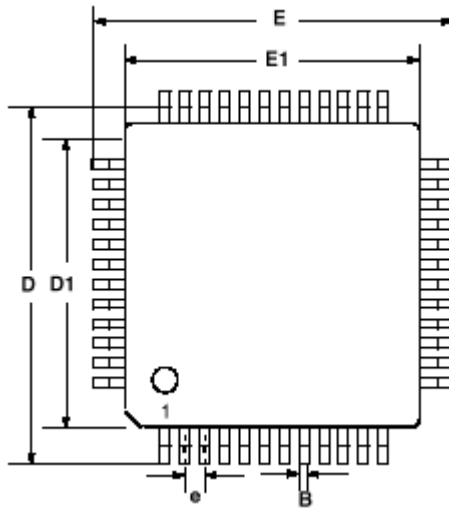*http://www.crystal.com*

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D) PACKAGE DRAWING for the 48L TQFP page 59

While the specification table for this package is correct, the drawing itself should be adjusted to accurately show measurement 'D' and 'D1'

SHOULD READ:

ORIGINAL DRAWING:

If there are any questions concerning this information,
Please contact: Jacques Patry (512)912-3908 jpatry@crystal.cirrus.com