

DATA SHEET

74LV241

Octal buffer/line driver (3-State)

Product specification
Supersedes data of 1997 Feb 19
IC24 Data Handbook

1998 May 20

Octal buffer/line driver (3-State)

74LV241

FEATURES

- Optimized for low voltage applications: 1.0 to 3.6 V
- Accepts TTL input levels between $V_{CC} = 2.7$ V and $V_{CC} = 3.6$ V
- Typical V_{OLP} (output ground bounce) < 0.8 V at $V_{CC} = 3.3$ V, $T_{amb} = 25^\circ\text{C}$
- Typical V_{OHV} (output V_{OH} undershoot) > 2 V at $V_{CC} = 3.3$ V, $T_{amb} = 25^\circ\text{C}$
- Output capability: bus driver
- I_{CC} category: MSI

QUICK REFERENCE DATA

 $GND = 0$ V; $T_{amb} = 25^\circ\text{C}$; $t_r = t_f \leq 2.5$ ns

SYMBOL	PARAMETER	CONDITIONS	TYPICAL	UNIT
t_{PHL}/t_{PLH}	Propagation delay 1A _n to 1Y _n ; 2A _n to 2Y _n	$C_L = 15$ pF; $V_{CC} = 3.3$ V	8.0	ns
C_I	Input capacitance		3.5	pF
C_{PD}	Power dissipation capacitance per buffer	$V_{CC} = 3.3$ V $V_I = GND$ to V_{CC} ¹	30	pF

NOTE:

1. C_{PD} is used to determine the dynamic power dissipation (P_D in μW)

$$P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o) \text{ where:}$$

f_i = input frequency in MHz; C_L = output load capacitance in pF;

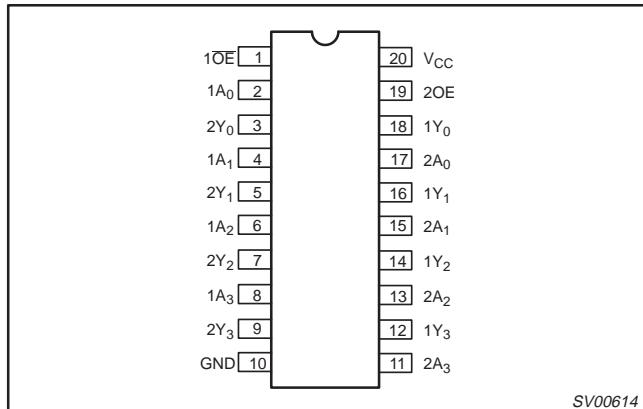
f_o = output frequency in MHz; V_{CC} = supply voltage in V;

$\sum (C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs.

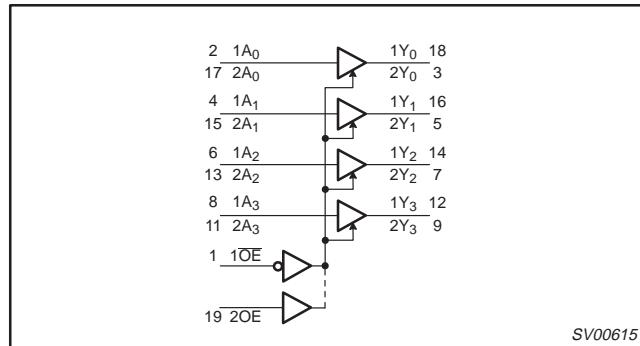
ORDERING INFORMATION

PACKAGES	TEMPERATURE RANGE	OUTSIDE NORTH AMERICA	NORTH AMERICA	PKG. DWG. #
20-Pin Plastic DIL	-40°C to +125°C	74LV241 N	74LV241 N	SOT146-1
20-Pin Plastic SO	-40°C to +125°C	74LV241 D	74LV241 D	SOT163-1
20-Pin Plastic SSOP Type II	-40°C to +125°C	74LV241 DB	74LV241 DB	SOT339-1
20-Pin Plastic TSSOP Type I	-40°C to +125°C	74LV241 PW	74LV241PW DH	SOT360-1

PIN CONFIGURATION



LOGIC SYMBOL



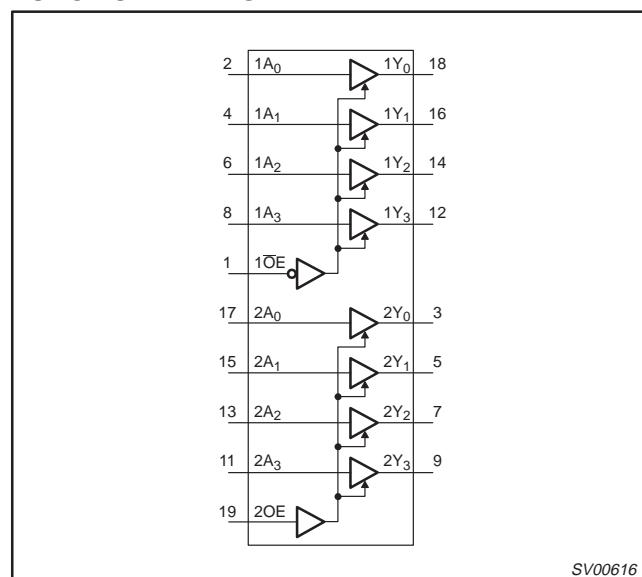
Octal buffer/line driver (3-State)

74LV241

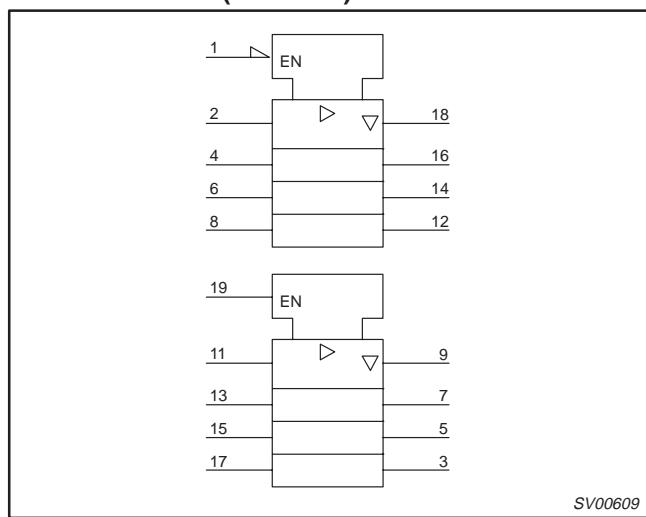
PIN DESCRIPTION

PIN NUMBER	SYMBOL	FUNCTION
1	$1\overline{OE}$	Output enable input (active LOW)
2, 4, 6, 8	$1A_0$ to $1A_3$	Data inputs
3, 5, 7, 9	$2Y_0$ to $2Y_3$	Bus outputs
10	GND	Ground (0 V)
17, 15, 13, 11	$2A_0$ to $2A_3$	Data inputs
18, 16, 14, 12	$1Y_0$ to $1Y_3$	Bus outputs
19	$2OE$	Output enable input (active HIGH)
20	V_{CC}	Positive supply voltage

FUNCTIONAL DIAGRAM



LOGIC SYMBOL (IEEE/IEC)



FUNCTION TABLE

INPUTS				OUTPUT	
$1\overline{OE}$	$1A_n$	$2OE$	$2A_n$	$1Y_n$	$2Y_n$
L	L	H	L	H	L
L	H	H	H	L	H
H	X	L	X	Z	Z

NOTES:

H = HIGH voltage level
 L = LOW voltage level
 X = don't care
 Z = high impedance OFF-state

Octal buffer/line driver (3-State)

74LV241

RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
V_{CC}	DC supply voltage	See Note 1	1.0	3.3	3.6	V
V_I	Input voltage		0	—	V_{CC}	V
V_O	Output voltage		0	—	V_{CC}	V
T_{amb}	Operating ambient temperature range in free air	See DC and AC characteristics	-40 -40		+85 +125	°C
t_r, t_f	Input rise and fall times	$V_{CC} = 1.0V$ to $2.0V$ $V_{CC} = 2.0V$ to $2.7V$ $V_{CC} = 2.7V$ to $3.6V$	— — —	— — —	500 200 100	ns/V

NOTE:

1. The LV is guaranteed to function down to $V_{CC} = 1.0V$ (input levels GND or V_{CC}); DC characteristics are guaranteed from $V_{CC} = 1.2V$ to $V_{CC} = 3.6V$.

ABSOLUTE MAXIMUM RATINGS^{1, 2}

In accordance with the Absolute Maximum Rating System (IEC 134).

Voltages are referenced to GND (ground = 0V).

SYMBOL	PARAMETER	CONDITIONS	RATING	UNIT
V_{CC}	DC supply voltage		-0.5 to +4.6	V
$\pm I_{IK}$	DC input diode current	$V_I < -0.5$ or $V_I > V_{CC} + 0.5V$	20	mA
$\pm I_{OK}$	DC output diode current	$V_O < -0.5$ or $V_O > V_{CC} + 0.5V$	50	mA
$\pm I_O$	DC output source or sink current — bus driver outputs	$-0.5V < V_O < V_{CC} + 0.5V$	35	mA
$\pm I_{GND},$ $\pm I_{CC}$	DC V_{CC} or GND current for types with — bus driver outputs		70	mA
T_{stg}	Storage temperature range		-65 to +150	°C
P_{tot}	Power dissipation per package — plastic DIL — plastic mini-pack (SO) — plastic shrink mini-pack (SSOP and TSSOP)	for temperature range: -40 to +125°C above +70°C derate linearly with 12mW/K above +70°C derate linearly with 8 mW/K above +60°C derate linearly with 5.5 mW/K	750 500 400	mW

NOTES:

1. Stresses beyond those listed may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
2. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

Octal buffer/line driver (3-State)

74LV241

DC CHARACTERISTICS FOR THE LV FAMILY

Over recommended operating conditions. Voltages are referenced to GND (ground = 0V).

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS					UNIT	
			-40°C to +85°C			-40°C to +125°C			
			MIN	TYP ¹	MAX	MIN	MAX		
V _{IH}	HIGH level Input voltage	V _{CC} = 1.2V	0.9			0.9		V	
		V _{CC} = 2.0V	1.4			1.4			
		V _{CC} = 2.7 to 3.6V	2.0			2.0			
V _{IL}	LOW level Input voltage	V _{CC} = 1.2V			0.3		0.3	V	
		V _{CC} = 2.0V			0.6		0.6		
		V _{CC} = 2.7 to 3.6V			0.8		0.8		
V _{OH}	HIGH level output voltage; all outputs	V _{CC} = 1.2V; V _I = V _{IH} or V _{IL} ; -I _O = 100µA		1.2				V	
		V _{CC} = 2.0V; V _I = V _{IH} or V _{IL} ; -I _O = 100µA	1.8	2.0		1.8			
		V _{CC} = 2.7V; V _I = V _{IH} or V _{IL} ; -I _O = 100µA	2.5	2.7		2.5			
		V _{CC} = 3.0V; V _I = V _{IH} or V _{IL} ; -I _O = 100µA	2.8	3.0		2.8			
V _{OH}	HIGH level output voltage; BUS driver outputs	V _{CC} = 3.0V; V _I = V _{IH} or V _{IL} ; -I _O = 8mA	2.40	2.82		2.20		V	
V _{OL}	LOW level output voltage; all outputs	V _{CC} = 1.2V; V _I = V _{IH} or V _{IL} ; I _O = 100µA		0				V	
		V _{CC} = 2.0V; V _I = V _{IH} or V _{IL} ; I _O = 100µA		0	0.2		0.2		
		V _{CC} = 2.7V; V _I = V _{IH} or V _{IL} ; I _O = 100µA		0	0.2		0.2		
		V _{CC} = 3.0V; V _I = V _{IH} or V _{IL} ; I _O = 100µA		0	0.2		0.2		
V _{OL}	LOW level output voltage; BUS driver outputs	V _{CC} = 3.0V; V _I = V _{IH} or V _{IL} ; I _O = 8mA		0.20	0.40		0.50	V	
I _I	Input leakage current	V _{CC} = 3.6V; V _I = V _{CC} or GND			1.0		1.0	µA	
I _{OZ}	3-State output OFF-state current	V _{CC} = 3.6V; V _I = V _{IH} or V _{IL} ; V _O = V _{CC} or GND			5		10	µA	
I _{CC}	Quiescent supply current; MSI	V _{CC} = 3.6V; V _I = V _{CC} or GND; I _O = 0			20.0		160	µA	
ΔI _{CC}	Additional quiescent supply current per input	V _{CC} = 2.7V to 3.6V; V _I = V _{CC} - 0.6V			500		850	µA	

NOTE:

1. All typical values are measured at T_{amb} = 25°C.

Octal buffer/line driver (3-State)

74LV241

AC CHARACTERISTICS

GND = 0V; $t_r = t_f \leq 2.5\text{ns}$; $C_L = 50\text{pF}$; $R_L = 1\text{k}\Omega$

SYMBOL	PARAMETER	WAVEFORM	CONDITION	LIMITS					UNIT
				-40 to +85 °C		-40 to +125 °C			
			$V_{CC}(\text{V})$	MIN	TYP ¹	MAX	MIN	MAX	
t_{PHL}/t_{PLH}	Propagation delay $1A_n$ to $1Y_n$; $2A_n$ to $2Y_n$	Figures 1	1.2		45				ns
			2.0		15	31		36	
			2.7		11	23		26	
			3.0 to 3.6		9 ²	18		21	
t_{PZH}/t_{PZL}	3-State output enable time $1\bar{O}E$ to $1Y_n$; $2OE$ to $2Y_n$	Figures 2, 3	1.2		55				ns
			2.0		19	36		44	
			2.7		14	26		33	
			3.0 to 3.6		10 ²	21		26	
t_{PHZ}/t_{PLZ}	3-State output disable time $1\bar{O}E$ to $1Y_n$; $2OE$ to $2Y_n$	Figures 2, 3	1.2		60				ns
			2.0		22	39		48	
			2.7		17	29		36	
			3.0 to 3.6		13 ²	24		29	

NOTES:

1. Unless otherwise stated, all typical values are measured at $T_{amb} = 25^\circ\text{C}$.
2. Typical values are measured at $V_{CC} = 3.3\text{ V}$.

AC WAVEFORMS

$V_M = 1.5\text{ V}$ at $V_{CC} \geq 2.7\text{ V}$; $V_M = 0.5\text{ V} \times V_{CC}$ at $V_{CC} < 2.7\text{ V}$
 $V_X = V_{OL} + 0.3\text{ V}$ at $V_{CC} \geq 2.7\text{ V}$; $V_X = V_{OL} + 0.1\text{ V} \times V_{CC}$ at $V_{CC} < 2.7\text{ V}$
 $V_Y = V_{OH} - 0.3\text{ V}$ at $V_{CC} \geq 2.7\text{ V}$; $V_Y = V_{OH} - 0.1 \times V_{CC}$ at $V_{CC} < 2.7\text{ V}$
 V_{OL} and V_{OH} are the typical output voltage drop that occur with the output load.

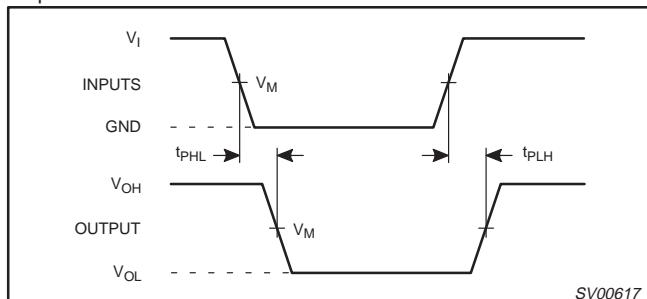


Figure 1. Input ($1A_n$, $2A_n$) to output ($1Y_n$, $2Y_n$) propagation delays.

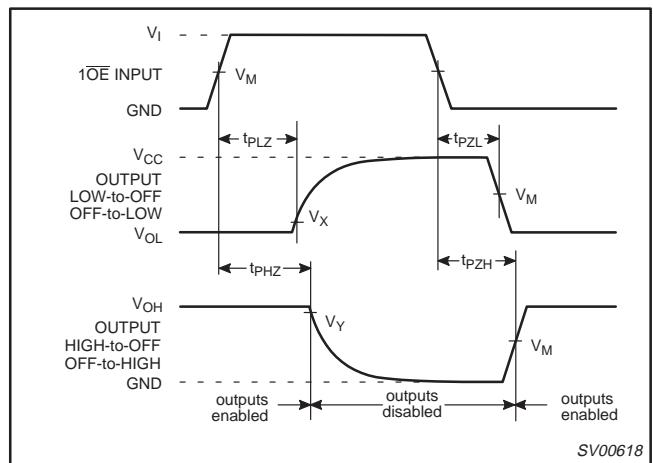


Figure 2. 3-State enable and disable times.

Octal buffer/line driver (3-State)

74LV241

AC WAVEFORMS (Continued)

$V_M = 1.5 \text{ V at } V_{CC} \geq 2.7 \text{ V}; V_M = 0.5 \text{ V} \times V_{CC} \text{ at } V_{CC} < 2.7 \text{ V}$
 $V_X = V_{OL} + 0.3 \text{ V at } V_{CC} \geq 2.7 \text{ V}; V_X = V_{OL} + 0.1 \text{ V} \times V_{CC} \text{ at } V_{CC} < 2.7 \text{ V}$
 $V_Y = V_{OH} - 0.3 \text{ V at } V_{CC} \geq 2.7 \text{ V}; V_Y = V_{OH} - 0.1 \times V_{CC} \text{ at } V_{CC} < 2.7 \text{ V}$
 V_{OL} and V_{OH} are the typical output voltage drop that occur with the output load.

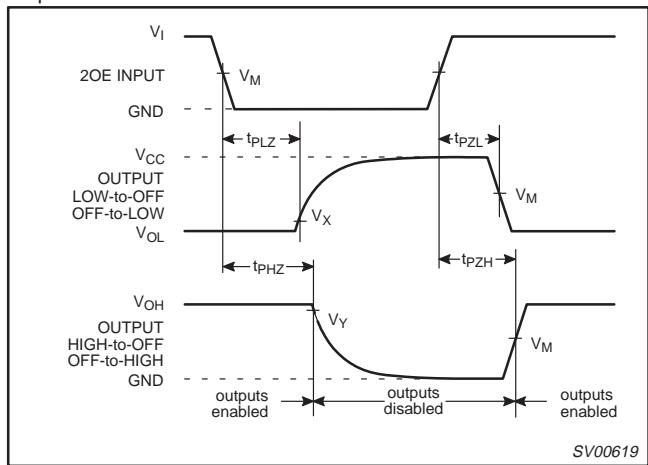
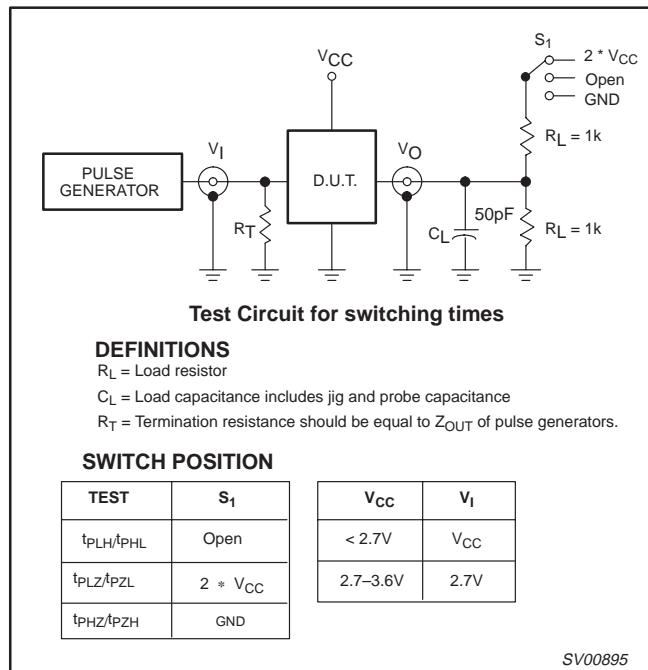


Figure 3. 3-State enable and disable times for input 2OE.

TEST CIRCUIT



Test Circuit for switching times

DEFINITIONS

R_L = Load resistor
 C_L = Load capacitance includes jig and probe capacitance
 R_T = Termination resistance should be equal to Z_{OUT} of pulse generators.

SWITCH POSITION

TEST	S ₁
t_{PLH}/t_{PHL}	Open
t_{PLZ}/t_{PZL}	$2 * V_{CC}$
t_{PHZ}/t_{PZH}	GND

V _{CC}	V _I
< 2.7V	V _{CC}
2.7–3.6V	2.7V

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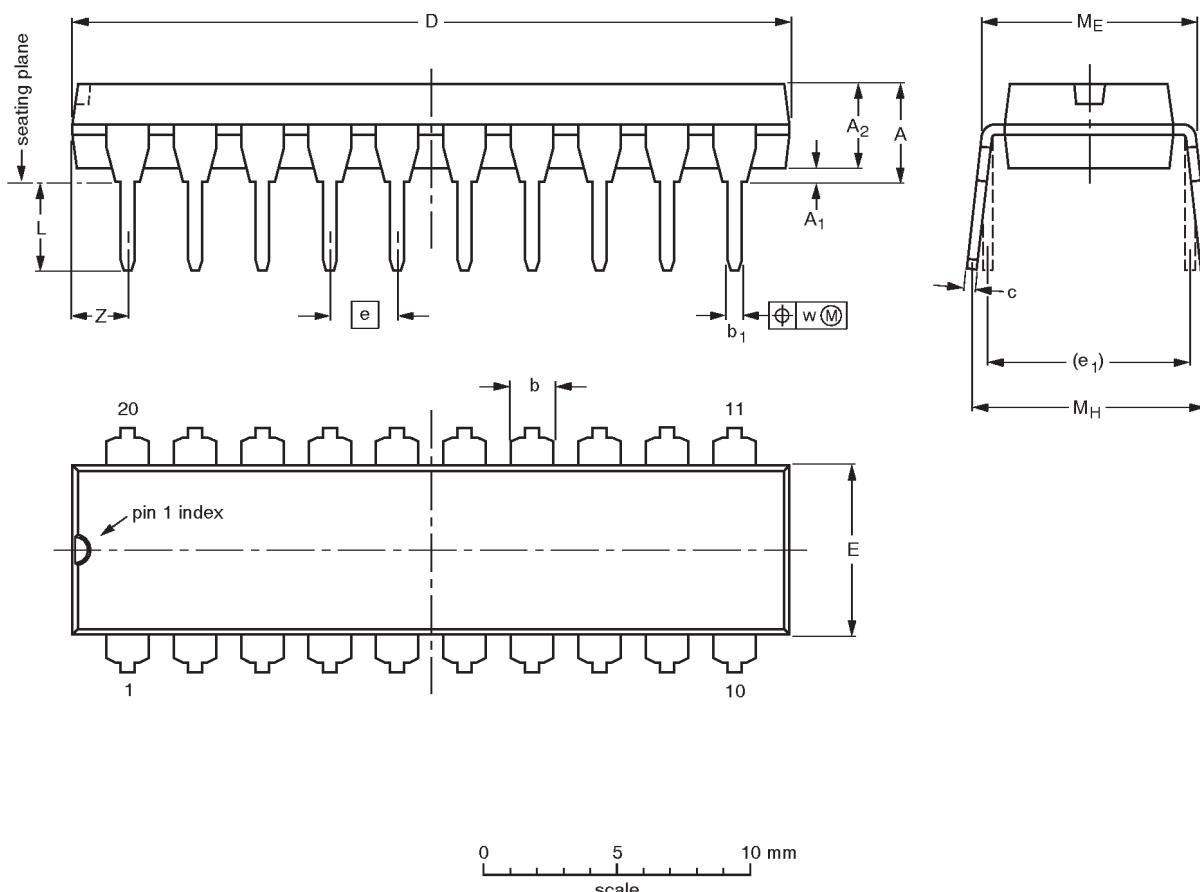
Figure 4. Load circuitry for switching times.

Octal buffer/line driver (3-State)

74LV241

DIP20: plastic dual in-line package; 20 leads (300 mil)

SOT146-1



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A ₁ min.	A ₂ max.	b	b ₁	c	D ⁽¹⁾	E ⁽¹⁾	e	e ₁	L	M _E	M _H	w	Z ⁽¹⁾ max.
mm	4.2	0.51	3.2	1.73 1.30	0.53 0.38	0.36 0.23	26.92 26.54	6.40 6.22	2.54	7.62	3.60 3.05	8.25 7.80	10.0 8.3	0.254	2.0
inches	0.17	0.020	0.13	0.068 0.051	0.021 0.015	0.014 0.009	1.060 1.045	0.25 0.24	0.10	0.30	0.14 0.12	0.32 0.31	0.39 0.33	0.01	0.078

Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

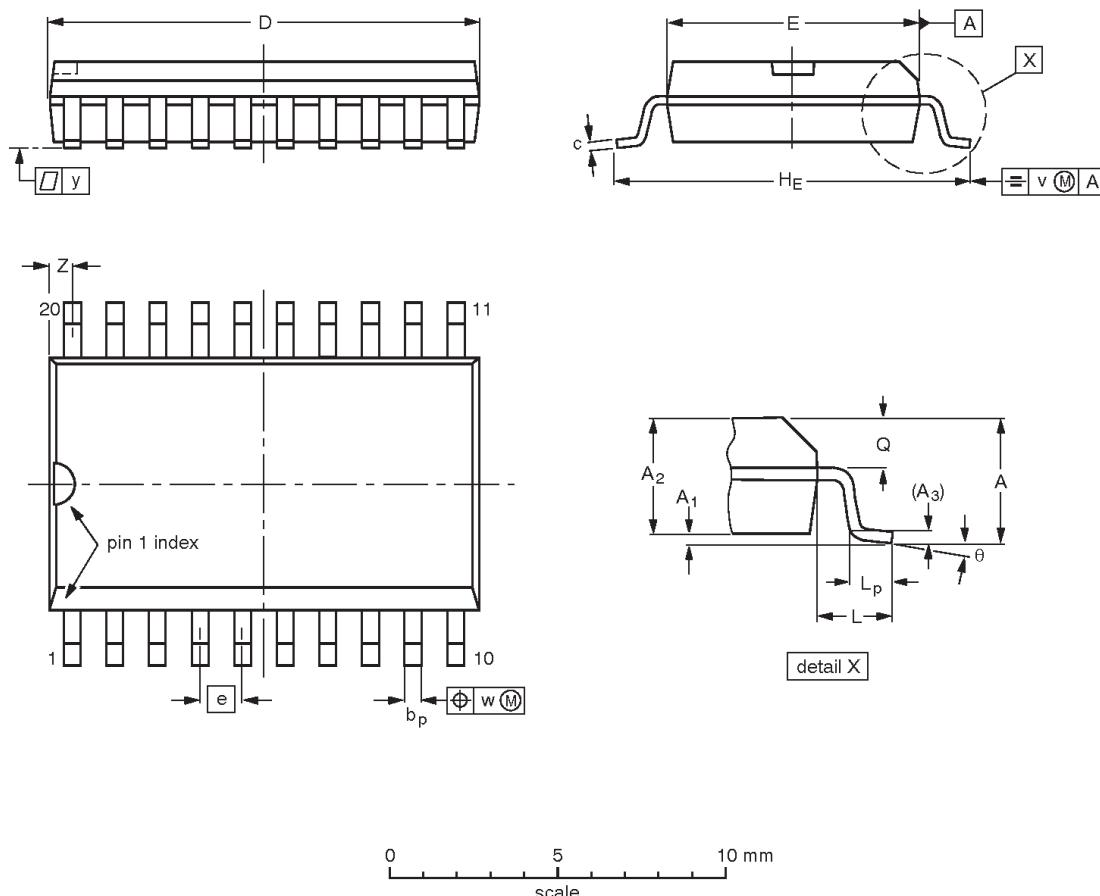
OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT146-1			SC603			92-11-17 95-05-24

Octal buffer/line driver (3-State)

74LV241

SO20: plastic small outline package; 20 leads; body width 7.5 mm

SOT163-1



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A ₁	A ₂	A ₃	b _p	c	D ⁽¹⁾	E ⁽¹⁾	e	H _E	L	L _p	Q	v	w	y	Z ⁽¹⁾	θ
mm	2.65 0.10	0.30 2.25	2.45	0.25	0.49 0.36	0.32 0.23	13.0 12.6	7.6 7.4	1.27	10.65 10.00	1.4	1.1 0.4	1.1 1.0	0.25	0.25	0.1	0.9 0.4	8° 0°
inches	0.10 0.004	0.012 0.089	0.096	0.01	0.019 0.014	0.013 0.009	0.51 0.49	0.30 0.29	0.050	0.42 0.39	0.055	0.043 0.016	0.043 0.039	0.01	0.01	0.004	0.035 0.016	0° 0°

Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

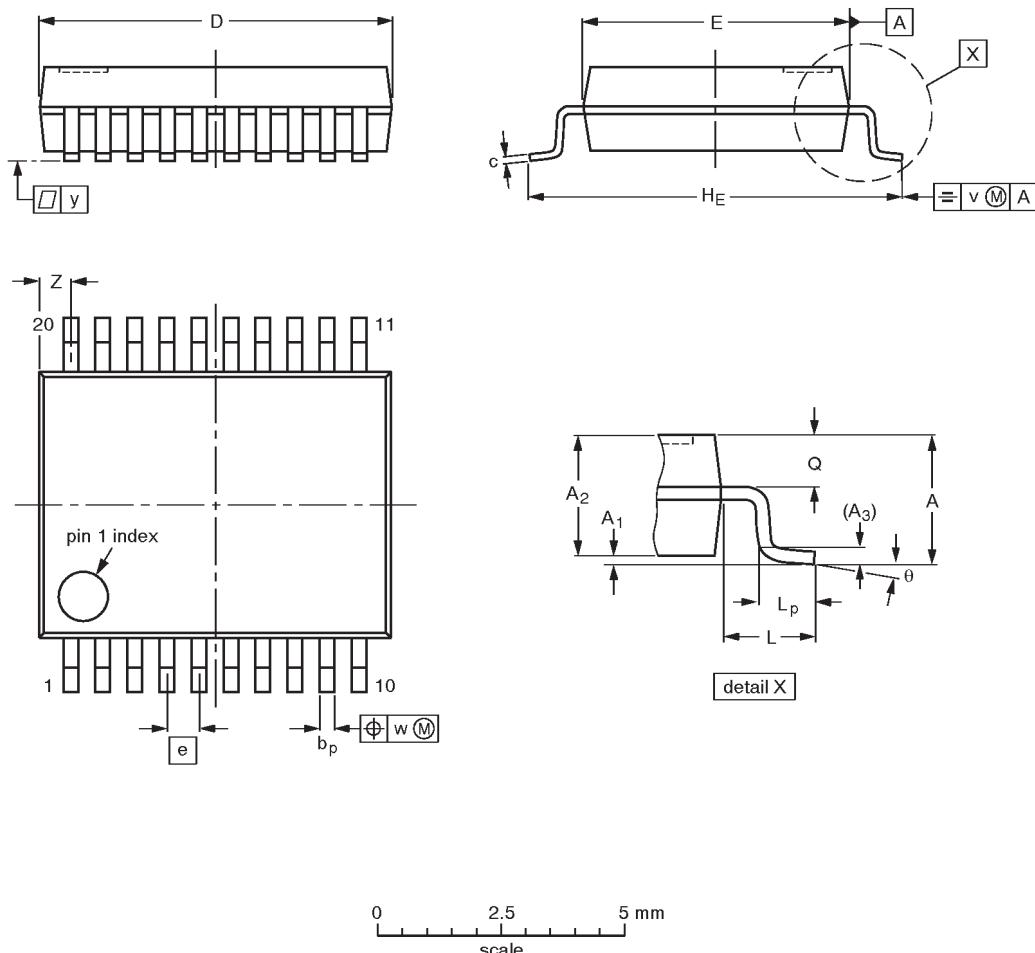
OUTLINE VERSION	REFERENCES			EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ		
SOT163-1	075E04	MS-013AC			92-11-17 95-01-24

Octal buffer/line driver (3-State)

74LV241

SSOP20: plastic shrink small outline package; 20 leads; body width 5.3 mm

SOT339-1



DIMENSIONS (mm are the original dimensions)

UNIT	A max.	A ₁	A ₂	A ₃	b _p	c	D ⁽¹⁾	E ⁽¹⁾	e	H _E	L	L _p	Q	v	w	y	Z ⁽¹⁾	θ
mm	2.0 0.05	0.21 1.65	1.80	0.25	0.38 0.25	0.20 0.09	7.4 7.0	5.4 5.2	0.65	7.9 7.6	1.25	1.03 0.63	0.9 0.7	0.2	0.13	0.1	0.9 0.5	8° 0°

Note

1. Plastic or metal protrusions of 0.20 mm maximum per side are not included.

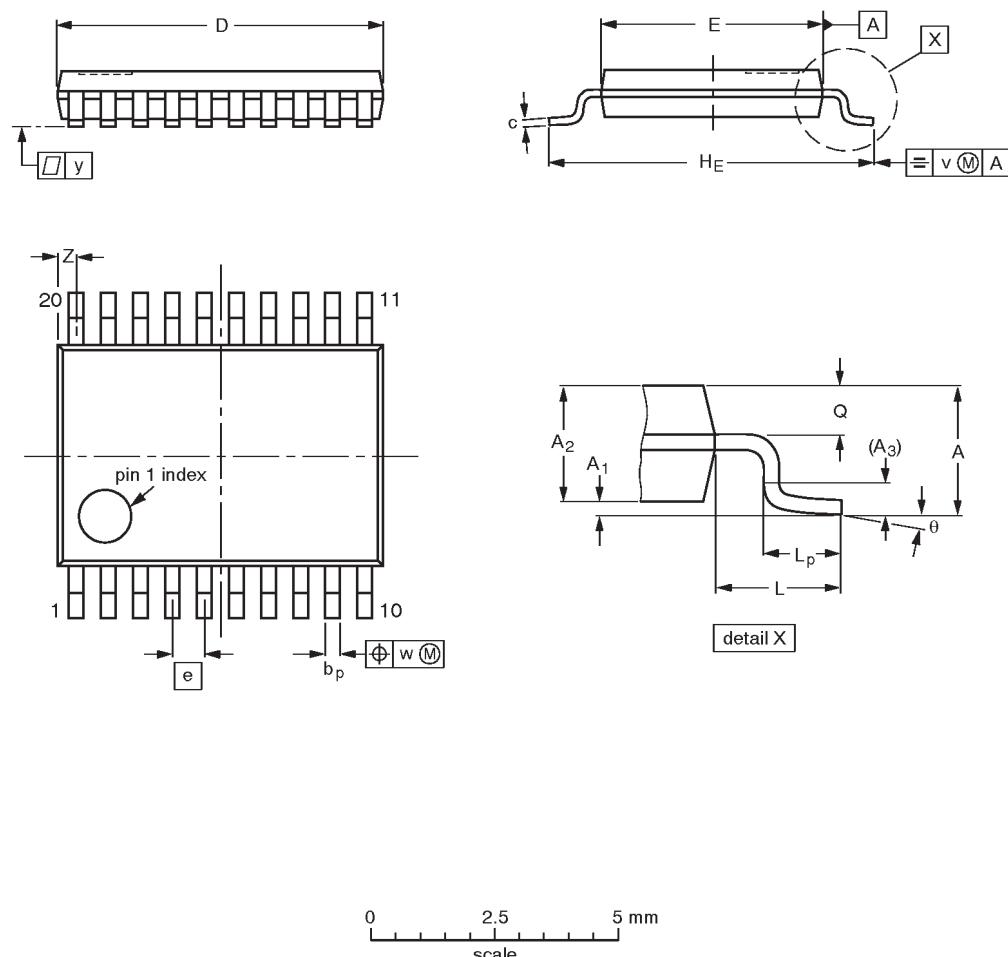
OUTLINE VERSION	REFERENCES			EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ		
SOT339-1		MO-150AE			93-09-08 95-02-04

Octal buffer/line driver (3-State)

74LV241

TSSOP20: plastic thin shrink small outline package; 20 leads; body width 4.4 mm

SOT360-1



DIMENSIONS (mm are the original dimensions)

UNIT	A max.	A ₁	A ₂	A ₃	b _p	c	D ⁽¹⁾	E ⁽²⁾	e	H _E	L	L _p	Q	v	w	y	z ⁽¹⁾	θ
mm	1.10 0.05	0.15 0.080	0.95	0.25	0.30 0.19	0.2 0.1	6.6 6.4	4.5 4.3	0.65	6.6 6.2	1.0	0.75 0.50	0.4 0.3	0.2	0.13	0.1	0.5 0.2	8° 0°

Notes

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
2. Plastic interlead protrusions of 0.25 mm maximum per side are not included.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT360-1		MO-153AC				-93-06-16 95-02-04

Octal buffer/line driver (3-State)

74LV241

DEFINITIONS

Data Sheet Identification	Product Status	Definition
<i>Objective Specification</i>	Formative or in Design	This data sheet contains the design target or goal specifications for product development. Specifications may change in any manner without notice.
<i>Preliminary Specification</i>	Preproduction Product	This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.
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811 East Arques Avenue
P.O. Box 3409
Sunnyvale, California 94088-3409
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