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Transistor

# 2SC5019

## Silicon NPN epitaxial planer type

For UHF band low-noise amplification

#### Features

- Low noise figure NF.
- High gain.
- High transition frequency f<sub>T</sub>.
- Mini Power type package, allowing downsizing of the equipment and automatic insertion through the tape packing and the magazine packing.

Symbol	Ratings	Unit				
V <sub>CBO</sub>	15	V				
V <sub>CEO</sub>	10	V				
$V_{EBO}$	2	V				
I <sub>C</sub>	80	mA				
$P_{C}^{*}$	1	W				
Tj	150	°C				
T <sub>stg</sub>	-55 ~ +150	°C				
	Symbol       V <sub>CBO</sub> V <sub>CEO</sub> V <sub>EBO</sub> I <sub>C</sub> P <sub>C</sub> *       T <sub>j</sub>	$\begin{tabular}{ c c c c } \hline Symbol & Ratings \\ \hline V_{CBO} & 15 \\ \hline V_{CEO} & 10 \\ \hline V_{EBO} & 2 \\ \hline I_C & 80 \\ \hline P_C^* & 1 \\ \hline T_j & 150 \\ \hline \end{tabular}$				

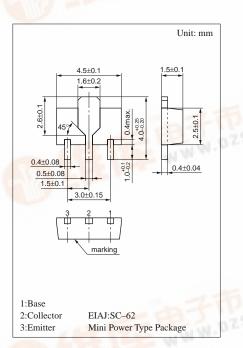
## Absolute Maximum Ratings (Ta=25°C)

Printed circuit board: Copper foil area of 1cm<sup>2</sup> or more, and the board thickness of 1.7mm for the collector portion

## Electrical Characteristics (Ta=25°C)

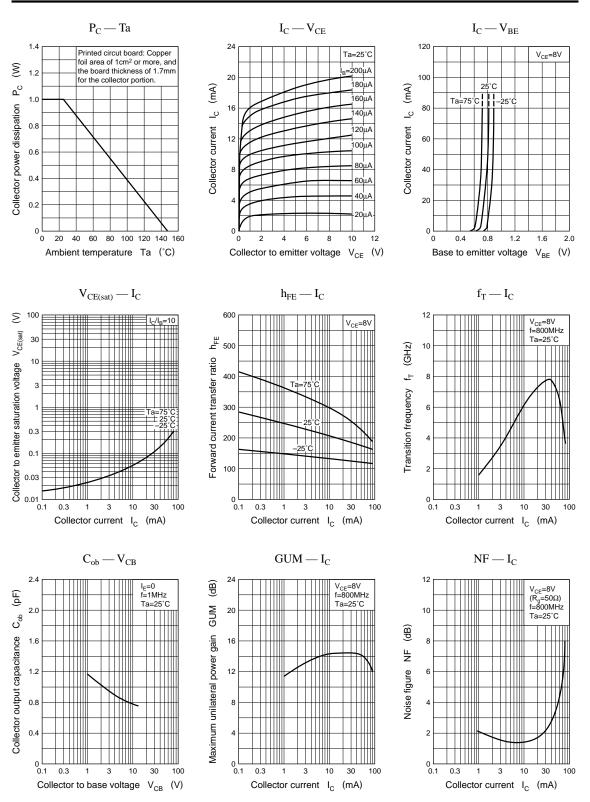
	-5 (1a=25 C)					
Parameter	Symbol	Conditions	min	typ	max	Unit
Collector cutoff current	I <sub>CBO</sub>	$V_{CB} = 10V, I_E = 0$		-	1	μA
Emitter cutoff current	I <sub>EBO</sub>	$V_{EB} = 2V, I_C = 0$			1	μΑ
Collector to base voltage	V <sub>CBO</sub>	$I_{\rm C} = 10\mu A, I_{\rm E} = 0$	15			V
Collector to emitter voltage	V <sub>CEO</sub>	$I_{\rm C} = 100 \mu A, I_{\rm B} = 0$	10			V
Forward current transfer ratio	h <sub>FE</sub>	$V_{CE} = 8V, I_{C} = 20mA$	80		250	
Transition frequency	f <sub>T</sub>	$V_{CE} = 8V, I_{C} = 20mA, f = 800MHz$	5	6		GHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = 10V, I_E = 0, f = 1MHz$		0.9	1.2	pF
Foward transfer gain	$ S_{21e} ^2$	$V_{CE} = 8V, I_{C} = 20mA, f = 800MHz$	7.5	10		dB
Maximum unilateral power gain	GUM	$V_{CE} = 8V, I_{C} = 20mA, f = 800MHz$		11.5		dB
Noise figure	NF	$V_{CE} = 8V, I_C = 20mA, f = 800MHz$		1.7		dB





Marking symbol : W

## Transistor



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