

MN39243FT

6 mm (type-1/3) high-sensitivity CCD area image sensor

Overview

The MN39243FT is a 6 mm (type-1/3) interline transfer CCD (IT-CCD) solid state image sensor device.

This device uses photodiodes in the optoelectric conversion section and CCDs for signal readout. The electronic shutter function has made an exposure time of 1/10 000 seconds possible. Further, this device has the features of high sensitivity, low noise, broad dynamic range, and super-low smear.

This device has a total of 466032 pixels (798 horizontal × 584 vertical) and provides stable and clear images with a resolution of 480 horizontal TV-lines and 420 vertical TV-lines.

Part Number	Size	System	Color or B/W
MN39243FT	6 mm (type-1/3)	PAL	Color

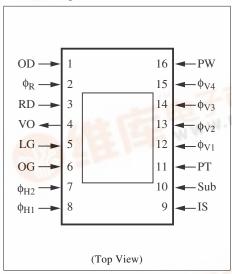
■ Features

- Total number of pixels: 798 (horizontal) × 584 (vertical)
- High sensitivity
- Broad dynamic range (compared to our conventional CCD ×1.2)
- Low smear
- Electronic shutter
- No image distortion
- Small size enables design of compact equipment
- High reliability

Applications

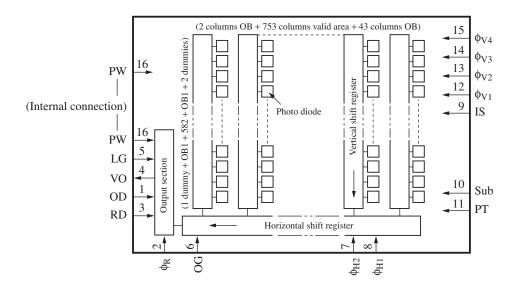
• Camcorders, surveillance cameras, door cameras

■ Pin Assignment





■ Block Diagram



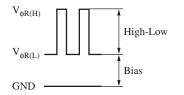
■ Pin Descriptions

Pin No.	Symbol	Descriptions	Pin No.	Symbol	Descriptions
1	OD	Output drain	9	IS	Horizontal CCD input source
2	φ _R	Reset pulse	10	Sub	Substrate
3	RD	Reset drain	11	PT	P-well for protection circuit
4	VO	Video output	12	$\phi_{\mathrm{V}1}$	Vertical shift register clock pulse 1
5	LG	Output load transistor gate	13	ϕ_{V2}	Vertical shift register clock pulse 2
6	OG	Output gate	14	ф _{V3}	Vertical shift register clock pulse 3
7	ф _{H2}	Horizontal register clock pulse 2	15	ϕ_{V4}	Vertical shift register clock pulse 4
8	фн1	Horizontal register clock pulse 1	16	PW	P-well

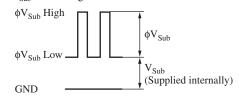
■ Absolute Maximum Ratings and Operating Conditions

Parameter			Rating		Operating condition			
		Symbol	min	max	min	typ	max	Unit
Reset drain voltage	e	V_{RD}	- 0.2	18.0	14.5	15.0	15.5	V
Output drain volta	ge	V _{OD}	- 0.2	18.0	14.5	15.0	15.5	V
Output load transis gate voltage	stor	V_{LG}		V				
Output gate voltag	Output gate voltage V _{OG}			(Internal bias)				
Horizontal CCD input	source voltage	V_{IS}	- 0.2	18.0	14.5	15.0	15.5	V
Protection P-well	voltage	V _{PT} *3, 4	-9.0	0.2	-7.3	-7.0	-6.7	V
P-well voltage		V_{PW}	Reference	e voltage	_	0	_	V
Reset	High-Low	$V_{\phi R(H-L)}^{*1}$	_	5.0	3.0	3.3	3.6	V
pulse voltage	Bias	$V_{\phi R(Bias)}^{*1}$	- 0.2	_	Sup	plied interr	V	
Horizontal register	Horizontal register		_	5.0	3.0	3.3	3.6	V
clock pulse voltage	clock pulse voltage 1		- 0.2	_	- 0.1	0	0.1	
Horizontal register		$V_{\phi H2(H)}$	_	5.0	3.0	3.3	3.6	V
clock pulse voltage 2		V _{ϕH2(L)}	- 0.2	_	- 0.1	0	0.1	
Vertical shift register		$V_{\phi V1(H)} *3, 4$	_	18.0	14.5	15.0	15.5	V
clock pulse voltage 1		V _{\phiV1(M)} *3, 4	_	_	- 0.2	0	0.2	
		V _{\phiV1(L)} *3, 4	-9.0	_	-7.3	-7.0	-6.7	
Vertical shift register		$V_{\phi V2(M)}$ *3, 4	_	15.0	- 0.2	0	0.2	V
clock pulse voltage	e 2	V _{\phiV2(L)} *3, 4	-9.0	_	-7.3	-7.0	-6.7	
Vertical shift register		V _{\$\phi V3(H)\$} *3, 4	_	18.0	14.5	15.0	15.5	V
clock pulse voltage 3		$V_{\phi V3(M)}$ *3, 4	_	_	- 0.2	0	0.2	
		V _{\phiV3(L)} *3, 4	-9.0	_	-7.3	-7.0	-6.7	
Vertical shift register		$V_{\phi V4(M)}^{*3, 4}$	_	15.0	- 0.2	0	0.2	V
clock pulse voltage 4		V _{\phiV4(L)} *3, 4	-9.0	_	-7.3	-7.0	-6.7	
Substrate voltage		V _{Sub} *2	- 0.2	45.0	Supplied internally		V	
		φV _{Sub} *2			21.0	22.0	23.0	
Operating tempera	ture	T_{opr}	-10	70		25	_	°C
Storage temperatur	re	$T_{\rm stg}$	-30	80		_	_	°C





*2: V_{Sub} when using electronic shutter function



- *3: Absolute maximum rating $-0.2 < V_{\phi V} V_{PT} < 24.5 \; (V)$
- *4: Relation between V_{PT} and $V_{\varphi V(L)}$

Set V_{PT} that is to meet the following conditions for VL voltage of the vertical shift clock waveform.

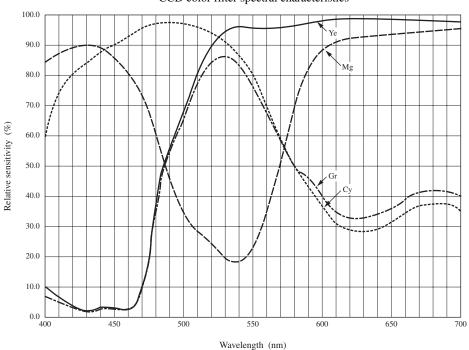
 $V_{PT} \le VL \ (V_{\phi V1(L)} \ to \ V_{\phi V4(L)})$

■ Optical Characteristics

	Color	Effective Sa		Saturation	Sensitivity	Vertical smear	Horizontal	Vertical
Part Number	or	pixels		output	F8	Sm	resolution	resolution
	B/W	Н	V	typ (mV)	typ (mV)	typ (dB)	typ (TV-lines)	typ (TV-lines)
MN39243FT	Color	737	575	750	450	-100	480	420

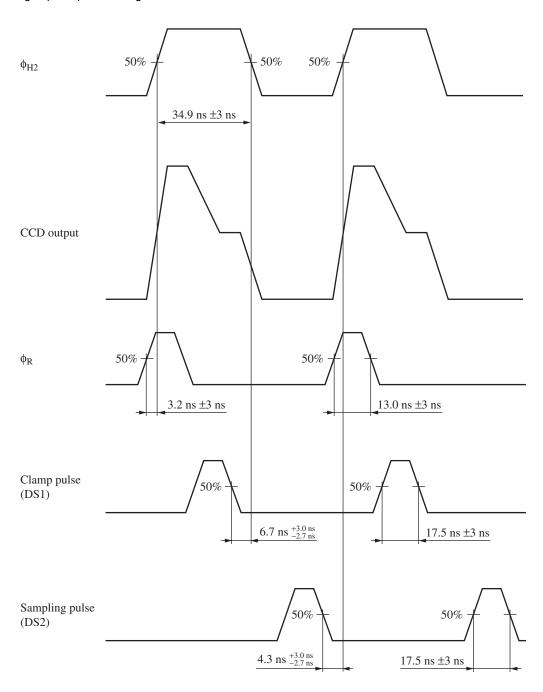
■ Graph of Characteristics

CCD color filter spectral characteristics



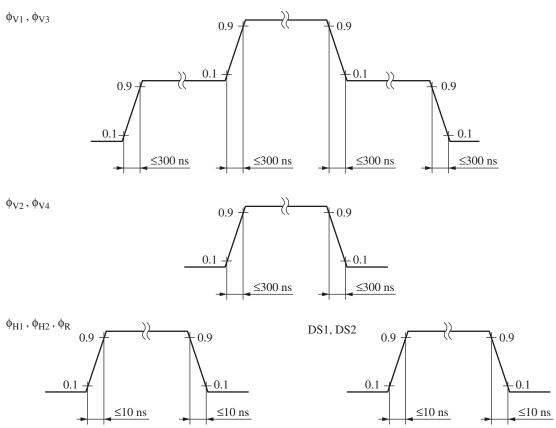
■ Timing Diagram

• High speed pulse timing



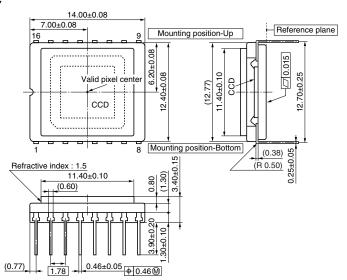
■ Timing Diagram (continued)

• Rise time and fall time of each pulse



■ Package Dimensions (unit: mm)

• WDIP016-P-0500C



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