查询2SC3979供应商 Power Transistors ₩₩₽₽₽₽ 捷多邦,专业PCB打样工厂,24小时加急

2SC3979, 2SC3979A

Silicon NPN triple diffusion planar type

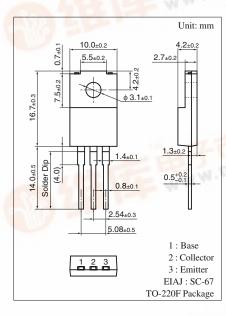
For high breakdown voltage high-speed switching

Features

- High-speed switching
- High collector to base voltage V_{CBO}
- Wide area of safe operation (ASO)
- \bullet Satisfactory linearity of forward current transfer ratio h_{FE}
- Full-pack package which can be installed to the heat sink with one screw

Absolute Maximum Ratings $T_c = 23 C$								
Paramet	er	Symbol	Rating	Unit				
Collector to base	2SC3979	V _{CBO}	900	V				
voltage	2SC3979A		1 000					
Collector to	2SC3979	V _{CES}	900	V				
emitter voltage	2SC3979A		1 000					
Collector to emitter voltage		V _{CEO}	800	V				
Emitter to base voltage		V _{EBO}	7	V				
Peak collector current		I _{CP}	5	А				
Collector current		I _C	3	А				
Base current		I _B	- 1	А				
Collector power	$T_C = 25^{\circ}C$	P _C	40	W				
dissipation	$T_a = 25^{\circ}C$		2					
Junction temperature		Tj	150	°C				
Storage temperature		T _{stg}	-55 to +150	°C				

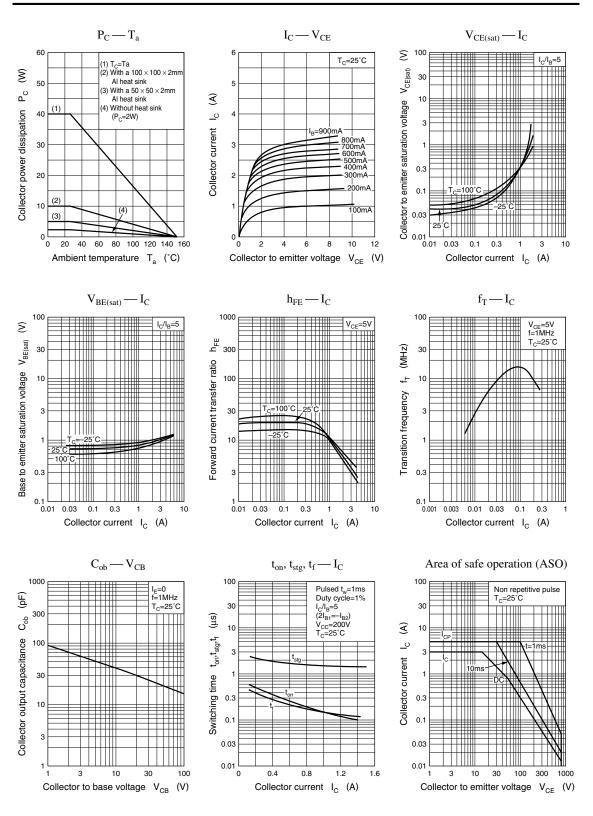
■ Absolute Maximum Ratings T_C = 25°C

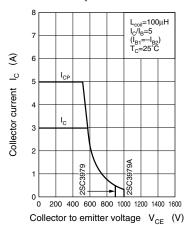


Electrical Characteristics $T_C = 25^{\circ}C$

_			t				1.14		
	Parameter		Symbol Conditions		Min	Тур	Max	Unit	
_	Collector cutoff	2SC3979	I _{CBO}	$V_{CB} = 900 \text{ V}, \text{ I}_{\text{E}} = 0$			50	μΑ	
	current	2SC3979A		$V_{CB} = 1\ 000\ V, I_E = 0$			50		
_	Emitter cutoff current		I _{EBO}	$V_{EB} = 7 V, I_C = 0$			50	μΑ	
_	Collector to emitter voltage		V _{CEO}	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 0$	800			V	
			h _{FE1}	$V_{CE} = 5 \text{ V}, \text{ I}_{C} = 0.1 \text{ A}$	8				
			h _{FE2}	$V_{CE} = 5 \text{ V}, \text{ I}_{C} = 0.8 \text{ A}$	6				
			V _{CE(sat)}	$I_{\rm C} = 0.8 \text{ A}, I_{\rm B} = 0.16 \text{ A}$			1.5	V	
			V _{BE(sat)}	$I_{\rm C} = 0.8 \text{ A}, I_{\rm B} = 0.16 \text{ A}$			1.5	V	
4			f_{T}	$V_{CE} = 5 \text{ V}, I_C = 0.15 \text{ A}, f = 1 \text{ MHz}$		10		MHz	
			t _{on}	$I_{C} = 0.8 \text{ A}, I_{B1} = 0.16 \text{ A}, I_{B2} = -0.32 \text{ A},$			0.7	μs	
5			t _{stg}	$V_{CC} = 250 \text{ V}$			2.5	μs	
Zþ	Fall time		t _f				0.3	μs	

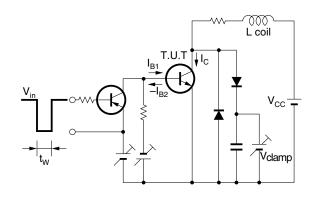
Power Transistors



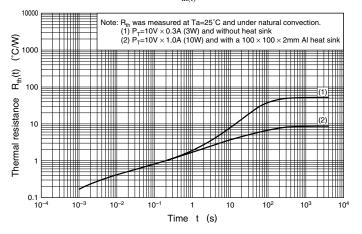


Area of safe operation, reverse bias ASO Reverse

Reverse bias ASO measuring circuit



 $R_{th(t)} - t$



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