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推多邦,专业PCB打样工厂,24小时加急 **Pamasonic**

Power Transistors

2SD1964

Silicon NPN epitaxial planar type

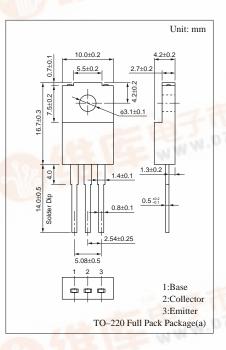
For power switching

Features

- Low collector to emitter saturation voltage V_{CE(sat)}
- Satisfactory linearity of foward current transfer ratio h_{FE}
- Large collector current I_C
- Full-pack package which can be installed to the heat sink with one screw

Parameter	Symbol	Ratings	Unit
Collector to base voltage	e V _{CBO}	130	V
Collector to emitter volta	ige V _{CEO}	80	v
Emitter to base voltage	V _{EBO}	7	V
Peak collector current	I _{CP}	25	А
Collector current	I _C	15	А
Collector power T _C =25		50	
dissipation Ta=25	PC PC	2	W
Junction temperature	Tj	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

Absolute Maximum Ratings $(T_C=25^{\circ}C)$



Electrical Characteristics (T_c=25°C)

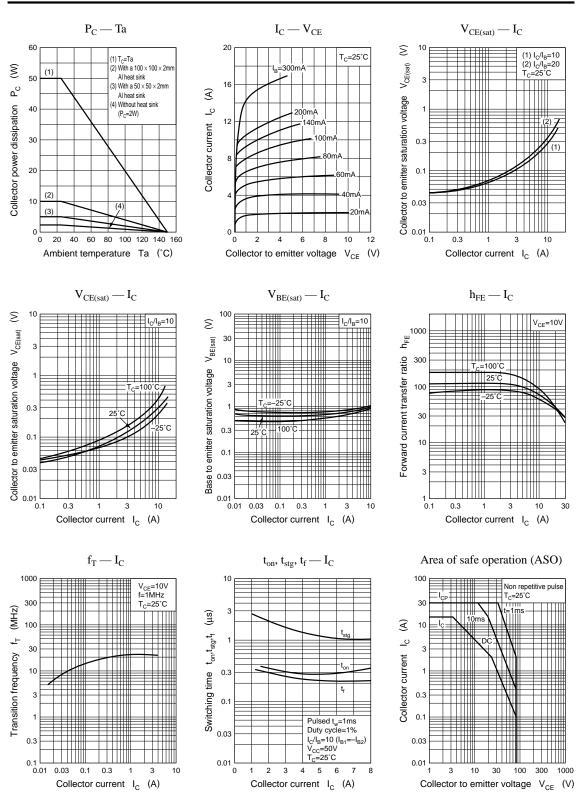
Parameter	Symbol	Conditions	min	typ	max	Unit
Collector cutoff current	I _{CBO}	$V_{CB} = 100V, I_E = 0$			10	μΑ
Emitter cutoff current	I _{EBO}	$V_{EB} = 5V, I_C = 0$			50	μA
Collector to emitter voltage	V _{CEO}	$I_{C} = 10 \text{mA}, I_{B} = 0$	80		1.44	V
Forward current transfer ratio	h _{FE1}	$V_{CE} = 2V, I_C = 0.1A$	45	-		3
	h _{FE2} *	$V_{CE} = 2V, I_C = 3A$	90		260	AL A
	h _{FE3}	$V_{CE} = 2V, I_C = 8A$	30			
Collector to emitter saturation voltage	V _{CE(sat)1}	$I_{\rm C} = 7A, I_{\rm B} = 0.35A$			0.5	V
	V _{CE(sat)2}	$I_{\rm C} = 15 {\rm A}, I_{\rm B} = 1.5 {\rm A}$			1.5	V
Base to emitter saturation voltage	V _{BE(sat)1}	$I_{C} = 7A, I_{B} = 0.35A$			1.5	V
	V _{BE(sat)2}	$I_{\rm C} = 15 {\rm A}, I_{\rm B} = 1.5 {\rm A}$			2.5	V
Transition frequency	f _T	$V_{CE} = 10V, I_C = 0.5A, f = 1MHz$		20		MH
Turn-on time	ton			0.5		μs
Storage time	t _{stg}	$I_{\rm C} = 7A, I_{\rm B1} = 0.7A, I_{\rm B2} = -0.7A,$		2.0		μs
Fall time	t _f	$V_{\rm CC} = 50 V$		0.2		μs

*h_{FE2} Rank classification

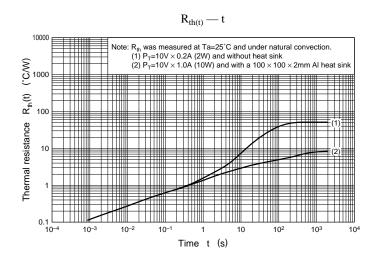


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