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2SB1434

Silicon PNP epitaxial planer type

For low-frequency output amplification Complementary to 2SD2177

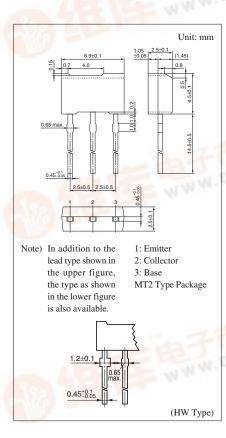
Features

- Low collector to emitter saturation voltage $V_{CE(sat)}$
- Allowing supply with the radial taping

Absolute Maximum Hatings $T_a = 25$ C							
Parameter	Symbol	Rating	Unit				
Collector to base voltage	V _{CBO}	-50	V				
Collector to emitter voltage	V _{CEO}	-50	V				
Emitter to base voltage	V _{EBO}	-5	V				
Peak collector current	I _{CP}	-3	А				
Collector current	I _C	-2	А				
Collector power dissipation *	P _C	1	W				
Junction temperature	Tj	150	°C				
Storage temperature	T _{stg}	-55 to +150	°C				

Absolute Maximum Ratings $T_a = 25^{\circ}C$

Note) *: Printed circuit board: Copper foil area of 1 cm² or more, and the board thickness of 1.7 mm for the collector portion



Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector cutoff current	I _{CBO}	$V_{CB} = -20 \text{ V}, I_E = 0$			- 0.1	μΑ
Collector to base voltage	V _{CBO}	$I_{C} = -10 \ \mu A, \ I_{E} = 0$	-50		1.00	V
Collector to emitter voltage	V _{CEO}	$I_{\rm C} = -1 \text{mA}, I_{\rm B} = 0$	-50			V
Emitter to base voltage	V _{EBO}	$I_E = -10 \ \mu A, \ I_C = 0$	-5	150	-	V
Forward current transfer ratio *1	h _{FE1} *2	$V_{CE} = -2 V, I_C = -200 mA$	120	1	340	
	h _{FE2}	$V_{CE} = -2 V, I_C = -1 A$	60			
Collector to emitter saturation voltage *1	V _{CE(sat)}	$I_{\rm C} = -1$ A, $I_{\rm B} = -50$ mA		- 0.2	- 0.3	V
Base to emitter saturation voltage *1	V _{BE(sat)}	$I_{\rm C} = -1$ A, $I_{\rm B} = -50$ mA		- 0.85	-1.2	V
Transition frequency	f _T	$V_{CB} = -10 \text{ V}, I_E = 50 \text{ mA}, f = 200 \text{ MHz}$		110		MHz
Collector output capacitance	C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		40	60	pF

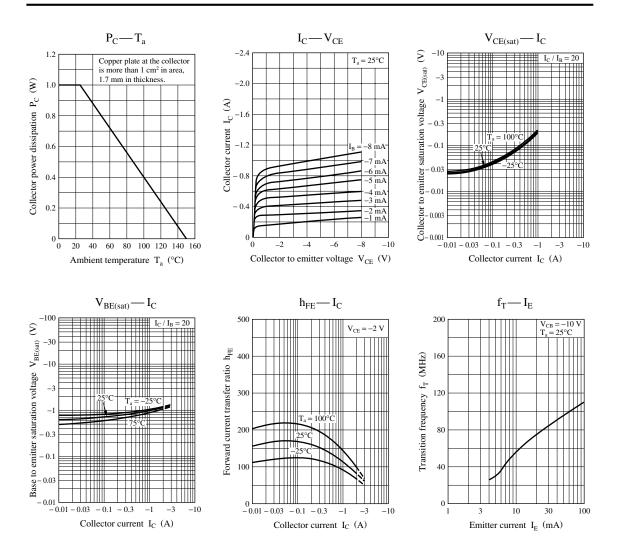
Note) *1: Pulse measurement

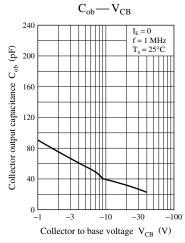
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*2: Rank classification

Rank	R	S	No-rank
h _{FE1}	120 to 240	170 to 340	120 to 340

roduct of no-rank is not classified and have no indication for rank.





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