查询2SD1424供应商

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Transistor

2SD1424

Silicon NPN epitaxial planer type

For low-frequency amplification

Features

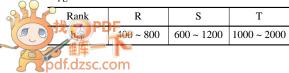
- Optimum for high-density mounting.
- Allowing supply with the radial taping.
- High foward current transfer ratio h_{FE}. •

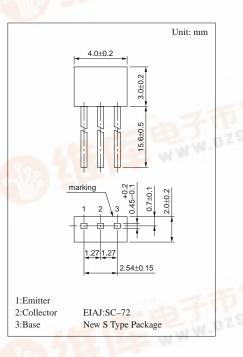
m Ratings	(Ta=25°C)	
Symbol	Ratings	Unit
V _{CBO}	50	V
V _{CEO}	40	V
V_{EBO}	15	V
I _{CP}	100	mA
I _C	50	mA
P _C	300	mW
Tj	150	°C
T _{stg}	-55 ~ +150	°C
	Symbol V _{CBO} V _{CEO} V _{EBO} I _{CP} I _C P _C T _j	$\begin{tabular}{ c c c c } \hline V_{CBO} & 50 \\ \hline V_{CEO} & 40 \\ \hline V_{EBO} & 15 \\ \hline I_{CP} & 100 \\ \hline I_C & 50 \\ \hline P_C & 300 \\ \hline T_j & 150 \\ \hline \end{tabular}$

Parameter	Symbol	Conditions	min	typ	max	Unit
Collector cutoff current	I _{CBO}	$V_{CB} = 10V, I_E = 0$			0.1	μΑ
	I _{CEO}	$V_{CE} = 20V, I_B = 0$			1	μΑ
Collector to base voltage	V _{CBO}	$I_C = 10 \mu A, \ I_E = 0$	50	. 1.0		v
Collector to emitter voltage	V _{CEO}	$I_{\rm C} = 1 {\rm mA}, I_{\rm B} = 0$	40		A.	v
Emitter to base voltage	V _{EBO}	$I_{\rm E} = 10\mu A, I_{\rm C} = 0$	15	1 m		V
Forward current transfer ratio	h _{FE} *	$V_{CE} = 10V, I_C = 2mA$	400		2000	
Collector to emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 10 {\rm mA}, I_{\rm B} = 1 {\rm mA}$		0.05	0.2	V
Transition frequency	f _T	$V_{CB} = 10V, I_E = -2mA, f = 200MHz$		200		MHz
Noise voltage	NV	$V_{CE} = 10V, I_C = 1mA, G_V = 80dB$ $R_g = 100k\Omega$, Function = FLAT		80		mV

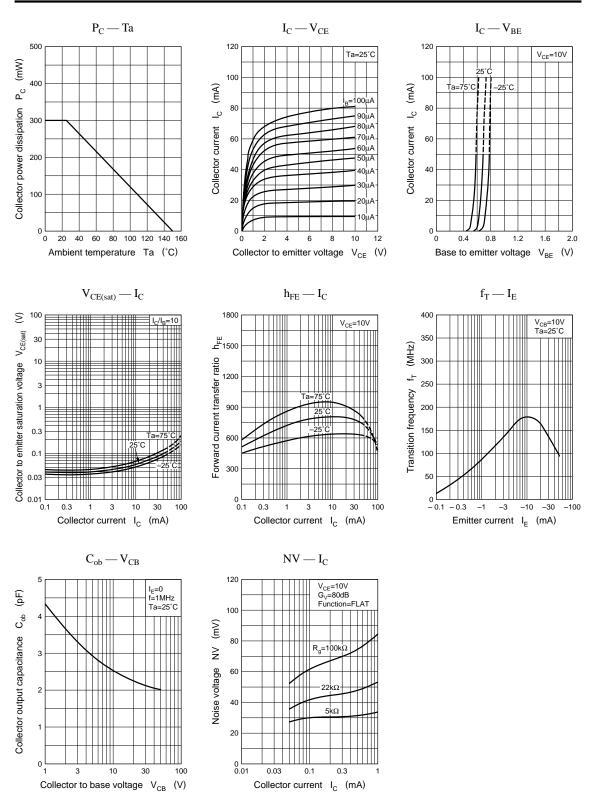
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^{*}h_{FE} Rank classification





Transistor



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