

# =AIRCHIL

SEMICONDUCTOR

April 1988 Revised September 2000 '4F38 Quad Two-Input NAND Buffer (Open Collector)

# 74F38 Quad Two-Input NAND Buffer (Open Collector)

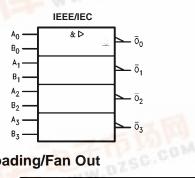
#### **General Description**

This device contains four independent gates, each of which performs the logic NAND function. The open-collector outputs require external pull-up resistors for proper logical operation.

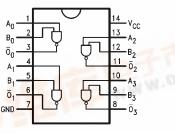
### **Ordering Code:**

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Order Number	er Number Package Number Package Description						
74F38SC	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow					
74F38SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide					
74F38PC	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide					
Devices also available	in Tape and Reel. Specify	by appending the suffix letter "X" to the ordering code.					

#### Logic Symbol



#### **Connection Diagram**



### Unit Loading/Fan Out

	Dia Nama	Description	U.L.	Input I <sub>IH</sub> /I <sub>IL</sub>		
	Pin Names	Description	HIGH/LOW	Output I <sub>OH</sub> /I <sub>OL</sub>		
	A <sub>n</sub> , B <sub>n</sub>	Inputs	1.0/2.0	20 μA/–1.2 mA		
	Ōn	Outputs	OC (Note 1) /106.6	OC (Note 1) /64 mA		

Note 1: OC = Open Collector

## **Function Table**

		1. 1. 1. 1. 1
Inp	outs	Output
А	В	ō
SO.V	L	Н
L	Н	Н
н	L	н
н	Н	L

H = HIGH Voltage Level L = LOW Voltage Level



### Absolute Maximum Ratings(Note 2)

-65°C to +150°C				
-55°C to +125°C				
-55°C to +150°C				
-0.5V to +7.0V				
-0.5V to +7.0V				
-30 mA to +5.0 mA				
-0.5V to V <sub>CC</sub>				
-0.5V to +5.5V				

twice the rated  $I_{OL} \mbox{(mA)}$ 

# Recommended Operating Conditions

Free Air Ambient Temperature
Supply Voltage

 $0^\circ C$  to +70°C +4.5V to +5.5V

**Note 2:** Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 3: Either voltage limit or current limit is sufficient to protect inputs.

#### **DC Electrical Characteristics**

Current Applied to Output in LOW State (Max)

Symbol	Parameter	Min	Тур	Max	Units	V <sub>cc</sub>	Conditions
VIH	Input HIGH Voltage	2.0			V		Recognized as a HIGH Signal
V <sub>IL</sub>	Input LOW Voltage			0.8	V		Recognized as a LOW Signal
V <sub>CD</sub>	Input Clamp Diode Voltage			-1.2	V	Min	I <sub>IN</sub> = -18 mA
V <sub>OL</sub>	Output LOW 10% V <sub>CC</sub> Voltage			0.55	V	Min	I <sub>OL</sub> = 64 mA
IIH	Input HIGH Current			5.0	μΑ	Max	V <sub>IN</sub> = 2.7V
I <sub>BVI</sub>	Input HIGH Current Breakdown Test			7.0	μΑ	Max	V <sub>IN</sub> = 7.0V
V <sub>ID</sub>	Input Leakage Test	4.75			V	0.0	I <sub>ID</sub> = 1.9 μA All Other Pins Grounded
I <sub>OD</sub>	Output Leakage Circuit Current			3.75	μΑ	0.0	V <sub>IOD</sub> = 150 mV All Other Pins Grounded
IIL	Input LOW Current			-1.2	mA	Max	V <sub>IN</sub> = 0.5V
I <sub>OHC</sub>	Open Collector, Output OFF Leakage Test			250	μΑ	Min	V <sub>OUT</sub> = V <sub>CC</sub>
I <sub>CCH</sub>	Power Supply Current		2.1	7.0	mA	Max	V <sub>O</sub> = HIGH
I <sub>CCL</sub>	Power Supply Current		26.0	30.0	mA	Max	$V_{O} = LOW$

## **AC Electrical Characteristics**

	Parameter		$\textbf{T}_{\textbf{A}}=+25^{\circ}\textbf{C}$		$T_A = 0^\circ C$		
Symbol		$V_{CC} = +5.0V$ $C_L = 50 \text{ pF}$			V <sub>CC</sub> = +5.0V C <sub>L</sub> = 50 pF		Units
		Min	Тур	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay	6.5	9.7	12.5	6.5	13.0	
t <sub>PHL</sub>	$A_n, B_n \text{ to } \overline{O}_n$	1.5	2.1	5.0	1.5	5.5	ns

