



# SP8830

Data Sheet

May 2002



## Features

- High speed operation 1.5GHz
- Silicon techology for low phase noise (typically better than -140dBc/Hz at 10kHz)
- Very low power dissipation: 150mW (Typ.)
- Single 5V supply operation
- High input sensitivity
- Very wide operating frequency range
- Available as DESC SMD 5962 9157201MPA

## Description

1CMF.019

f.dzsc.com

The SP8830 is one of a range of very high speed low power prescalers for professional and military applications. The device features a complementary output stage with on chip current sources for the emitter follower outputs.

### **Ordering Information**

SP8830 A DG SP8830 B DG DES9157201/AC/DGAZ (SMD)

1.5GHZ 10 Prescaler

Temperature Range: -55°C to +125°C (A Grade)
-40 °C to +85°C (B Grade)

## Absolute Maximum Ratings

DS3690

| Supply voltage, V <sub>CC</sub> | 6.5V            |
|---------------------------------|-----------------|
| Clock input voltage             | 2.5V p-p        |
| Storage temperature range       | -65°C to +150°C |
| Junction temperature            | + 175°C         |



#### Figure 1 - SP8830 Block Diagram



Figure 2 - Pin Connections

**Electrical Characteristics -** Unless otherwise stated, the Electrical Characteristics are guaranteed over specified supply, frequency and temperature range.

Supply voltage, V<sub>CC</sub> -475V to +525V. Temperature, T<sub>AMB</sub> = -55°C to +125°C (A Grade), -40°C to +85°C (B Grade)

| Charactorictic  | Din  |            | Value                 | ł   | Unite          | Conditions  |
|---|--|------------|-----------------------|-----|----------------|---|
| Characteristic  | 4050sitivity, 100MHz to2, 3100mVRMS sinewave, measured in<br>50Ω system. See Figs. 3 and | Conditions |                       |     |                |   |
| Supply current, I <sub>CC</sub>   | 1  |            |                       |     | mA             |   |
|   |  |            | 40                    | 50  |                |   |
| Input sensitivity, 100MHz to 500MHz   | 2, 3   |            |                       | 100 | mV             | RMS sinewave, measured in $50\Omega$ system. See Figs. 3 and 4. |
| Input impedance (series equivalent)   | 2, 3   |            | 50<br>2               |     | Ω<br>pF        | See Fig. 5  |
| Output voltage with $f_{IN}$ = 100MHz<br>Output voltage with $f_{IN}$ = 1500MHz | 6, 7<br>6, 7   | 0.7        | 1<br>0 <sup>.</sup> 4 |     | V p-p<br>V p-p |   |



Figure 3 - Typical Input Sensitivity

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Figure 4 - Test Circuit



Figure 5 - Typical Input Impedance, Normalised to 50W

| APPRD.   | DATE 20Nov96 26Mar02 | ACN 201728 212450 CFMICONDUCTOR DG |                            | © Zarlink Semiconductor 2002 All rights reserved. | This dra                    | b<br>e<br>e<br>Nominal Centres |             |     |   | -√ A-b2 "N" Leads E                  |               |      |   | Pin 1 Ref. |             |                                   | Pin 1 |  |
|----------|----------------------|------------------------------------|----------------------------|---|-----------------------------|--------------------------------|-------------|-----|---|--------------------------------------|---------------|------|---|------------|-------------|-----------------------------------|-------|--|
|          |                      | DG / C                             | Previous package codes     |   | his drawing supersedes      |                                |             |     | 2 | b2 1,<br>0 0,                        |               | 0    |   |            |             | Symbol AI                         |       |  |
| GPD00270 |                      | eal Ceramic)                       | Package Outline for 8 lead | Package Code                                      | des 418/ED/39501/001 (Swind |                                | RECTANGULAR | 040 |   | 1.65 0.045<br>0.58 0.014<br>15 0.014 | 54 BSC. 0.100 | 7.62 | .51     0.020       59     7.87     0.220     0 | 5,08       | Nominal MAX | n. Dimensions Cont<br>millimetres |       |  |



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