

# International IRF Rectifier

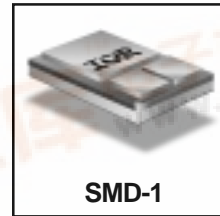
PD - 91553D

## POWER MOSFET SURFACE MOUNT(SMD-1)

**IRFN9140**  
**JANTX2N7236U**  
**JANTXV2N7236U**  
**REF:MIL-PRF-19500/595**  
**100V, P-CHANNEL**  
**HEXFET<sup>®</sup> MOSFET TECHNOLOGY**

### Product Summary

Part Number	RDS(on)	Id
IRFN9140	0.20Ω	-18A



HEXFET<sup>®</sup> MOSFET technology is the key to International Rectifier's advanced line of power MOSFET transistors. The efficient geometry design achieves very low on-state resistance combined with high transconductance. HEXFET transistors also feature all of the well-established advantages of MOSFETs, such as voltage control, very fast switching, ease of paralleling and electrical parameter temperature stability. They are well-suited for applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers, high energy pulse circuits, and virtually any application where high reliability is required. The HEXFET transistor's totally isolated package eliminates the need for additional isolating material between the device and the heatsink. This improves thermal efficiency and reduces drain capacitance.

### Features:

- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Electrically Isolated
- Surface Mount
- Dynamic dv/dt Rating
- Light-weight

### Absolute Maximum Ratings

	Parameter		Units
$I_D$ @ $V_{GS} = -10V, T_C = 25^\circ C$	Continuous Drain Current	-18	A
$I_D$ @ $V_{GS} = -10V, T_C = 100^\circ C$	Continuous Drain Current	-11	
$I_{DM}$	Pulsed Drain Current ①	-72	
$P_D$ @ $T_C = 25^\circ C$	Max. Power Dissipation	125	W
	Linear Derating Factor	1.0	W/°C
$V_{GS}$	Gate-to-Source Voltage	±20	V
EAS	Single Pulse Avalanche Energy ②	500	mJ
$I_{AR}$	Avalanche Current ①	-18	A
EAR	Repetitive Avalanche Energy ①	12.5	mJ
dv/dt	Peak Diode Recovery dv/dt ③	-5.0	V/ns
$T_J$	Operating Junction	-55 to 150	°C
$T_{STG}$	Storage Temperature Range		
	Package Mounting Surface Temperature	300 (for 5 S)	
	Weight	2.6(typical)	g

For footnotes refer to the last page

**Electrical Characteristics @ T<sub>j</sub> = 25°C (Unless Otherwise Specified)**

	Parameter	Min	Typ	Max	Units	Test Conditions
B <sub>V</sub> DSS	Drain-to-Source Breakdown Voltage	-100	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -1.0mA
ΔB <sub>V</sub> DSS/ΔT <sub>J</sub>	Temperature Coefficient of Breakdown Voltage	—	-0.087	—	V/°C	Reference to 25°C, I <sub>D</sub> = -1.0mA
R <sub>DS(on)</sub>	Static Drain-to-Source On-State Resistance	—	—	0.20	Ω	V <sub>GS</sub> = -10V, I <sub>D</sub> = -11A④
		—	—	0.22		V <sub>GS</sub> = -10V, I <sub>D</sub> = -18A ④
V <sub>GS(th)</sub>	Gate Threshold Voltage	-2.0	—	-4.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA
g <sub>fs</sub>	Forward Transconductance	6.2	—	—	S (r <sub>S</sub> )	V <sub>DS</sub> > -15V, I <sub>DS</sub> = -11A④
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	—	—	-25	μA	V <sub>DS</sub> = -80V, V <sub>GS</sub> = 0V
		—	—	-250		V <sub>DS</sub> = -80V V <sub>GS</sub> = 0V, T <sub>J</sub> = 125°C
I <sub>GSS</sub>	Gate-to-Source Leakage Forward	—	—	-100	nA	V <sub>GS</sub> = -20V
I <sub>GSS</sub>	Gate-to-Source Leakage Reverse	—	—	100	nA	V <sub>GS</sub> = 20V
Q <sub>g</sub>	Total Gate Charge	—	—	60	nC	V <sub>GS</sub> = -10V, I <sub>D</sub> = -18A V <sub>DS</sub> = -50V
Q <sub>gs</sub>	Gate-to-Source Charge	—	—	13		
Q <sub>gd</sub>	Gate-to-Drain ('Miller') Charge	—	—	35.2		
t <sub>d(on)</sub>	Turn-On Delay Time	—	—	35	ns	V <sub>DD</sub> = -50V, I <sub>D</sub> = -18A R <sub>G</sub> = 9.1Ω, V <sub>GS</sub> = -10V
t <sub>r</sub>	Rise Time	—	—	85		
t <sub>d(off)</sub>	Turn-Off Delay Time	—	—	85		
t <sub>f</sub>	Fall Time	—	—	65		
L <sub>S</sub> + L <sub>D</sub>	Total Inductance	—	4.0	—	nH	Measured from the center of drain pad to center of source pad
C <sub>iss</sub>	Input Capacitance	—	1400	—	pF	V <sub>GS</sub> = 0V, V <sub>DS</sub> = -25V f = 1.0MHz
C <sub>oss</sub>	Output Capacitance	—	600	—		
C <sub>rss</sub>	Reverse Transfer Capacitance	—	200	—		

**Source-Drain Diode Ratings and Characteristics**

	Parameter	Min	Typ	Max	Units	Test Conditions
I <sub>S</sub>	Continuous Source Current (Body Diode)	—	—	-18	A	
I <sub>SM</sub>	Pulse Source Current (Body Diode) ①	—	—	-72		
V <sub>SD</sub>	Diode Forward Voltage	—	—	-4.2	V	T <sub>j</sub> = 25°C, I <sub>S</sub> = -18A, V <sub>GS</sub> = 0V ④
t <sub>rr</sub>	Reverse Recovery Time	—	—	280	nS	T <sub>j</sub> = 25°C, I <sub>F</sub> = -18A, di/dt ≤ 100A/μs
Q <sub>RR</sub>	Reverse Recovery Charge	—	—	3.6	μC	V <sub>DD</sub> ≤ -30V ④
t <sub>on</sub>	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by L <sub>S</sub> + L <sub>D</sub> .				

**Thermal Resistance**

	Parameter	Min	Typ	Max	Units	Test Conditions
R <sub>thJC</sub>	Junction to Case	—	—	1.0	°C/W	Soldered to a copper-clad PC board
R <sub>thJ-PCB</sub>	Junction to PC Board	—	4.0	—		

For footnotes refer to the last page

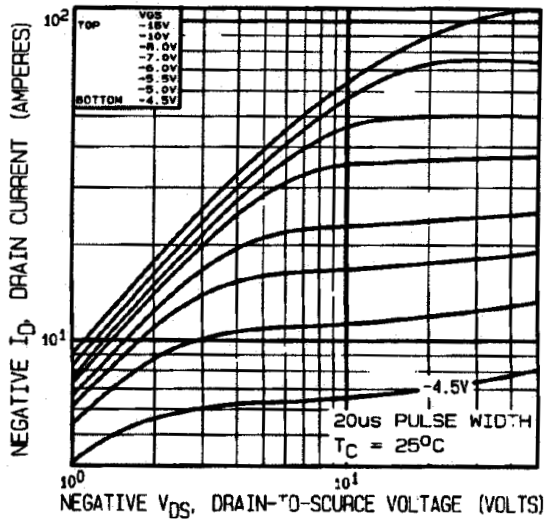


Fig 1. Typical Output Characteristics

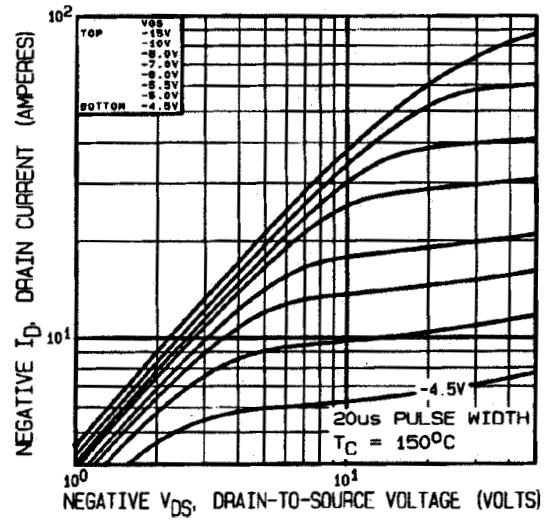


Fig 2. Typical Output Characteristics

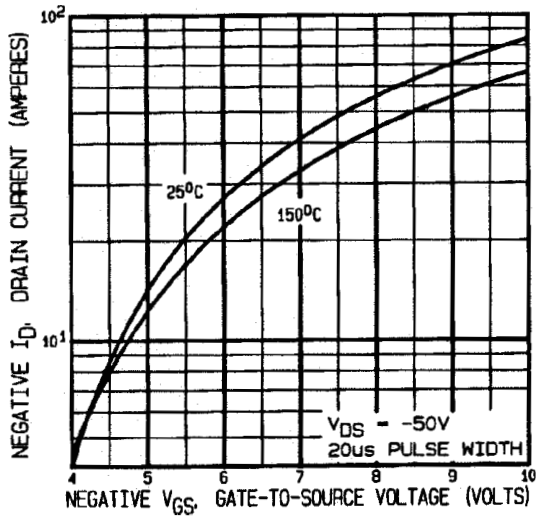


Fig 3. Typical Transfer Characteristics

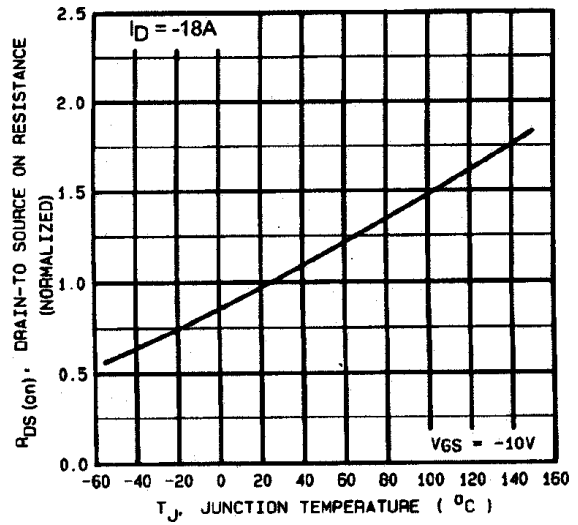


Fig 4. Normalized On-Resistance Vs. Temperature

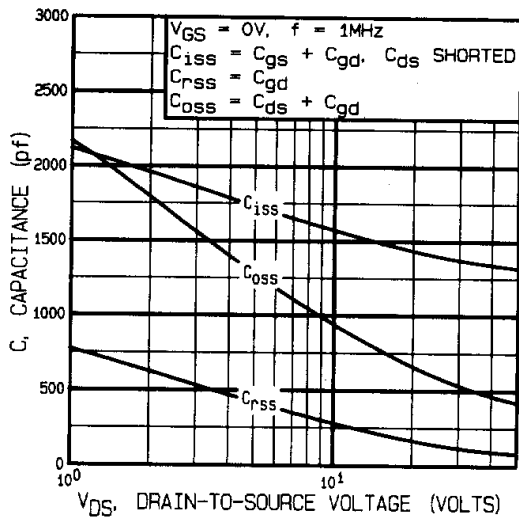


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

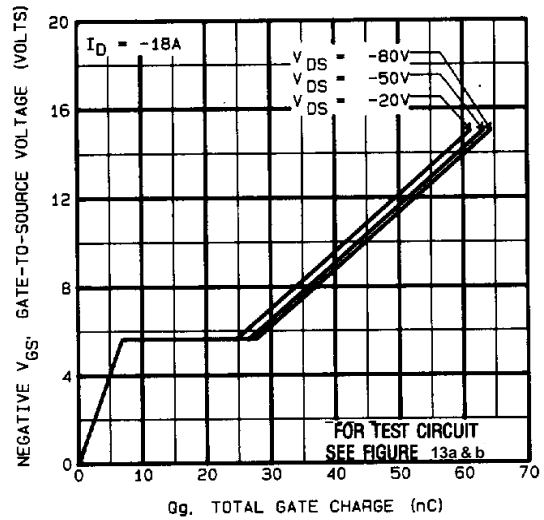


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

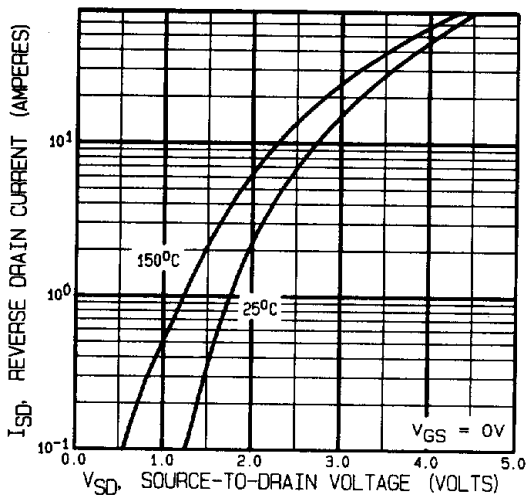


Fig 7. Typical Source-Drain Diode Forward Voltage

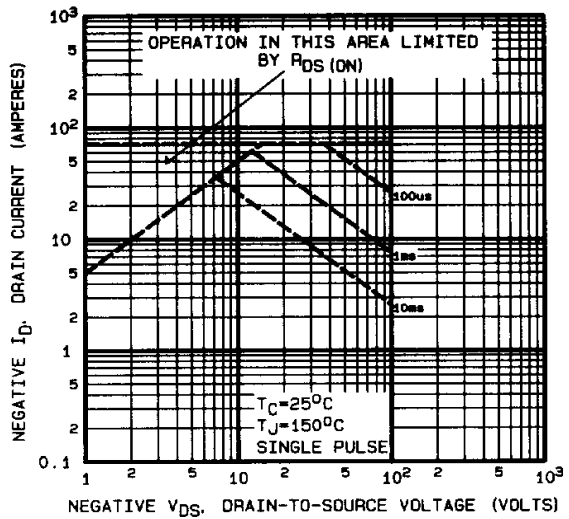
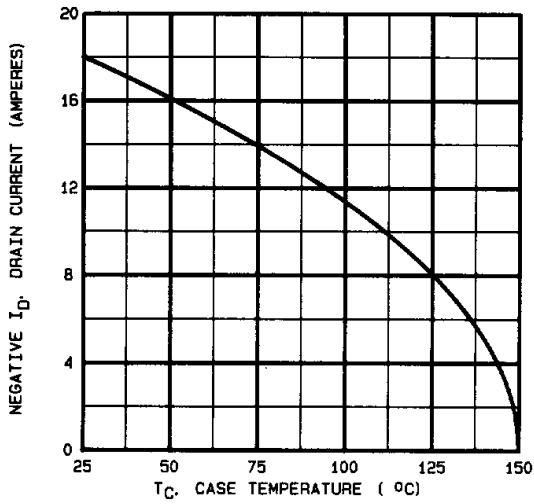
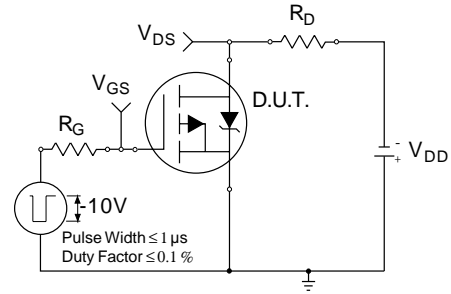


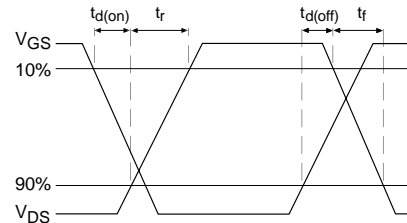
Fig 8. Maximum Safe Operating Area



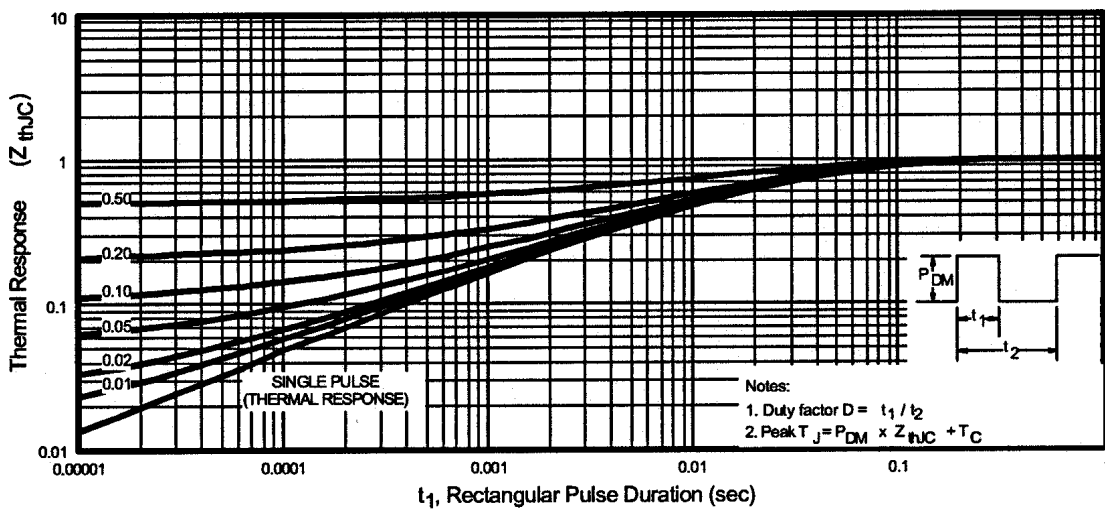
**Fig 9.** Maximum Drain Current Vs. Case Temperature



**Fig 10a.** Switching Time Test Circuit



**Fig 10b.** Switching Time Waveforms



**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Case

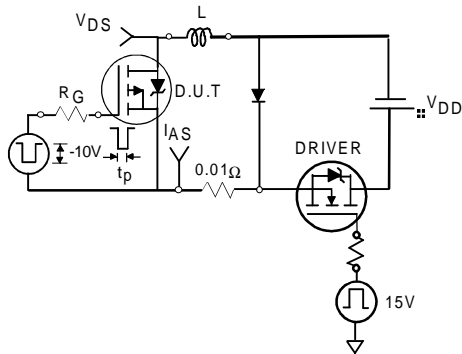


Fig 12a. Unclamped Inductive Test Circuit

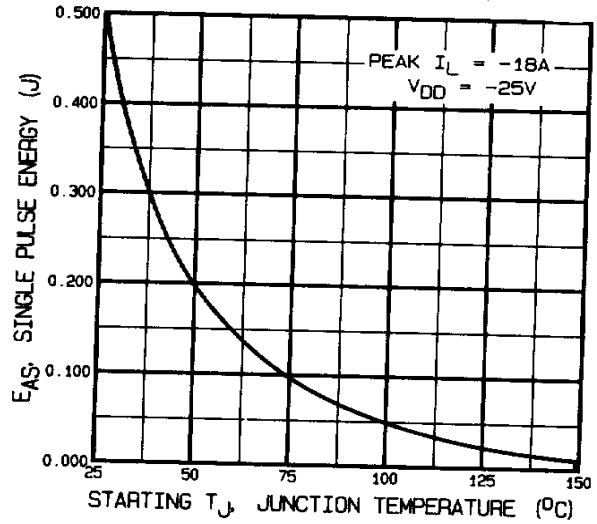


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

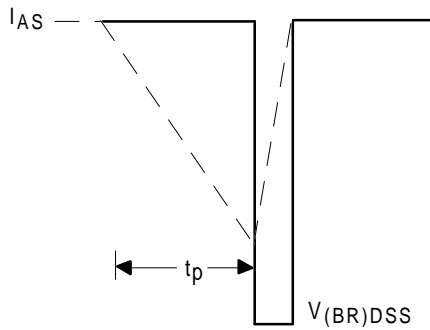


Fig 12b. Unclamped Inductive Waveforms

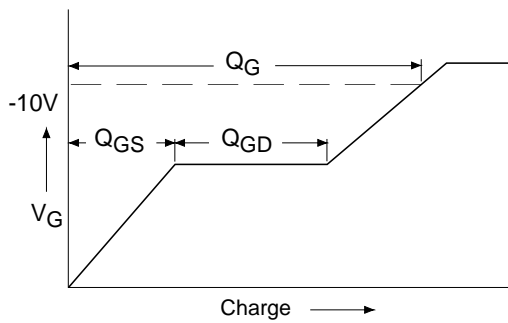


Fig 13a. Basic Gate Charge Waveform

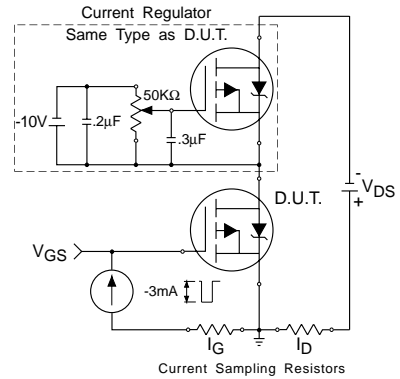
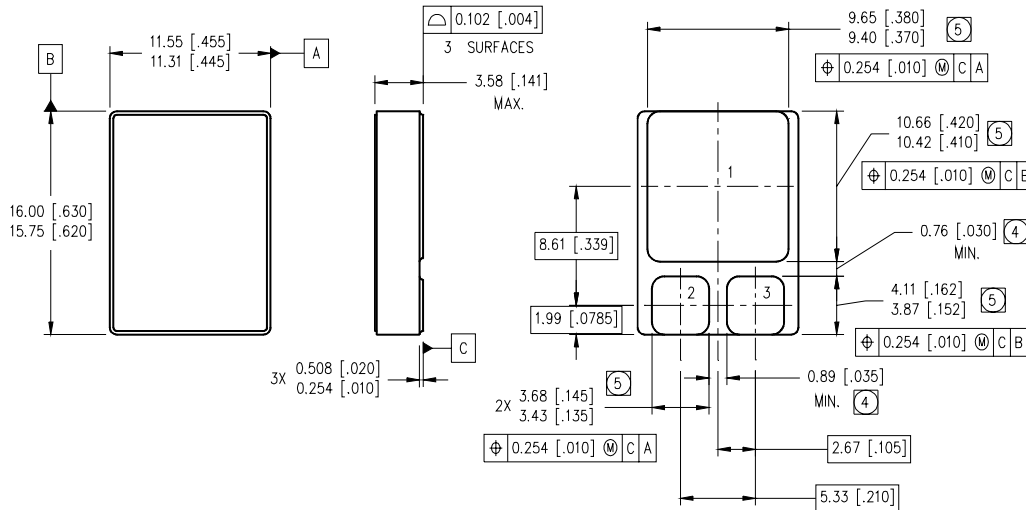


Fig 13b. Gate Charge Test Circuit

**Foot Notes:**

- ① Repetitive Rating; Pulse width limited by maximum junction temperature.
- ②  $V_{DD} = -25V$ , starting  $T_J = 25^\circ C$ ,  $L = 3.1mH$   
 Peak  $I_L = -18A$ ,  $V_{GS} = -10V$
- ③  $I_{SD} \leq -18A$ ,  $di/dt \leq -100A/\mu s$ ,  
 $V_{DD} \leq -100V$ ,  $T_J \leq 150^\circ C$
- ④ Pulse width  $\leq 300 \mu s$ ; Duty Cycle  $\leq 2\%$

**Case Outline and Dimensions — SMD-1**



NOTES:

- 1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
- 2. CONTROLLING DIMENSION: INCH.
- 3. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
- ④ DIMENSION INCLUDES METALLIZATION FLASH.
- ⑤ DIMENSION DOES NOT INCLUDE METALLIZATION FLASH.

PAD ASSIGNMENTS

- 1- DRAIN
- 2- GATE
- 3- SOURCE