PNP/NPN Epitaxial Planar Silicon Transistors



2SA1526/2SC3920

Switching Applications (with Bias Resistance)

Applications

· Switching circuits, inverter circuits, interface circuits, driver circuits.

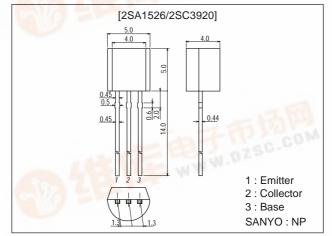
Features

- · On-chip bias resistance : R1=10k Ω , R2=10k Ω .
- · Large current capacity: I_C=500mA.

Package Dimensions

unit:mm

2003B



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Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		(-)50	V
Collector-to-Emitter Voltage	V _{CEO}		(–)50	V
Emitter-to-Base Voltage	V _{EBO}	- 1- (1)	(–)10	V
Collector Current	IC	/ CE / V4	(–)500	mA
Collector Current (Pulse)	I _{CP}		(–)800	mA
Collector Dissipation	PC		600	mW
Junction Temperature	Tj Tj		150	°C
Storage Temperature	Tstg	3.8%	-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions		Ratings			Unit
	Symbol		r	min	typ	max	Offic
Collector Cutoff Current	I _{CBO}	V _{CB} =(-)40V, I _E =0				(-)0.1	μΑ
	ICEO	V _{CE} =(-)40V, I _B =0	177		-11	(-)0.5	μΑ
Emitter Cutoff Current	I _{EBO}	V _{EB} =(-)5V, I _C =0	(-	-)195	(-)250	(-)360	μA
DC Current Gain	h _{FE}	V _{CE} =(-)5V, I _C =(-)10mA	1000	50	41.0		
Gain-Bandwidth Product	-	V _{CE} =(-)10V, I _C =(-)5mA			250		MHz
	l tT				(200)		MHz

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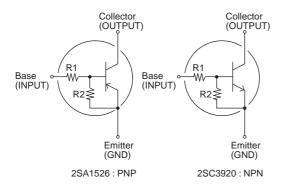
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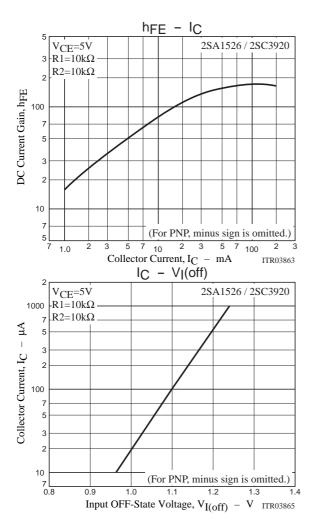
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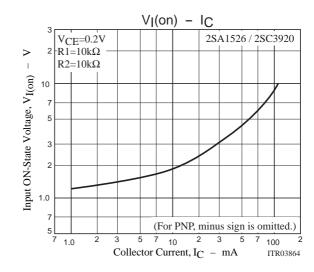
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Uill
Output Capacitance	C _{ob}	V _{CB} =(-)10V, f=1MHz		3.7		pF
				(5.5)		pF
Collector-to-Emitter Saturation Voltage	VCE(sat)	I _C =(-)20mA, I _B =(-)1mA		(-)0.1	(–)0.3	V
Collector-to-Base Breakdown Voltage	V _(BR) CBO	I _C =(-)10μA, I _E =0	(-)50			V
Collector-to-Emitter Breakdown Voltage	V _(BR) CEO	I _C =(-)100μA, R _{BE} =∞	(-)50			V
Input OFF-State Voltage	V _{I(off)}	$V_{CE}=(-)5V, I_{C}=(-)100\mu A$	(-)0.8	(-)1.1	(–)1.5	V
Input ON-State Voltage	V _{I(on)}	V _{CE} =(-)0.2V, I _C =(-)10mA	(-)1.0	(-)2.0	(-)4.0	V
Input Resistance	R1		7	10	13	kΩ
Resistance Ratio	R1/R2		0.9	1.0	1.1	

Electrical Connection







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