Ordering number: EN3133

PNP Epitaxial Planar Silicon Transistor



2SA1729

High-Speed Switching Applications

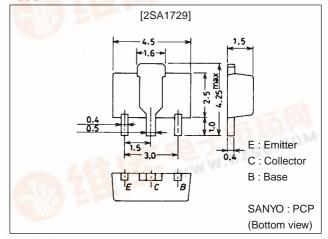
Features

- · Adoption of FBET, MBIT processes.
- · Large current capacity.
- · Low collector-to-emitter saturation voltage.
- · Fast switching speed.
- · Small-sized package.

Package Dimensions

unit:mm

2038



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit	
Collector-to-Base Voltage	V _{CBO}		-50	V	
Collector-to-Emitter Voltage	V _{CEO}		-40	V	
Emitter-to-Base Voltage	V _{EBO}		-5	V	
Collector Current	I _C		-1.5	Α	
Collector Current (Pulse)	I _{CP}		-3	Α	
Collector Dissipation	PC	Mounted on ceramic board (250mm ² ×0.8mm)	1.3	W	
Junction Temperature	Tj	80 14 05	150	°C	
Storage Temperature	Tstg	- File File	-55 to +150	°C	

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions		Ratings			
Faranietei	Syllibol	Conditions	min	typ	max	Unit	
Collector Cutoff Current	I _{CBO}	V _{CB} =-40V, I _E =0			-1	μΑ	
Emitter Cutoff Current	I _{EBO}	V _{EB} =-3V, I _C =0				μΑ	
DC Current Gain	h _{FE} 1	V _{CE} =-2V, I _C =-100mA	70*		280*	1111	
	h _{FE} 2	V _{CE} =-2V, I _C =-1.5A	25	- 11		1011	
Gain-Bandwidth Product	fT	V _{CE} =-2V, I _C =-100mA		300		MHz	
Output Capacitance	C _{ob}	V _{CB} =-10V, f=1MHz	W.A.	18		pF	
Collector-to-Emitter Saturatin Voltage	V _{CE(sat)}	I _C =-800mA, I _B =-40mA		-0.3	-0.8	V	
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =-800mA, I _B =-40mA		-0.9	-1.3	V	

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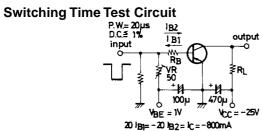
SANYO Electric Co.,Ltd. Semiconductor Bussiness Headquaters
TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

Parameter	Symbol	Conditions		Unit		
raidiletei	Syllibol	Conditions		typ	max	Offic
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =-10μA, I _E =0	-50			V
Collector-to-Emitter Saturation Voltage	V _(BR) CEO	I _C =-1mA, R _{BE} =∞	-40			V
Emitter-to-Base Breakdown Voltage V _{(B}		I _E =-10μA, I _C =0	-5			V
Turn-ON Time	ton	See specified Test Circuit		50	100	ns
Storage Time	t _{stg}	See specified Test Circuit		120	220	ns
Turn-OFF Time	toff	See specified Test Circuit		150	300	ns

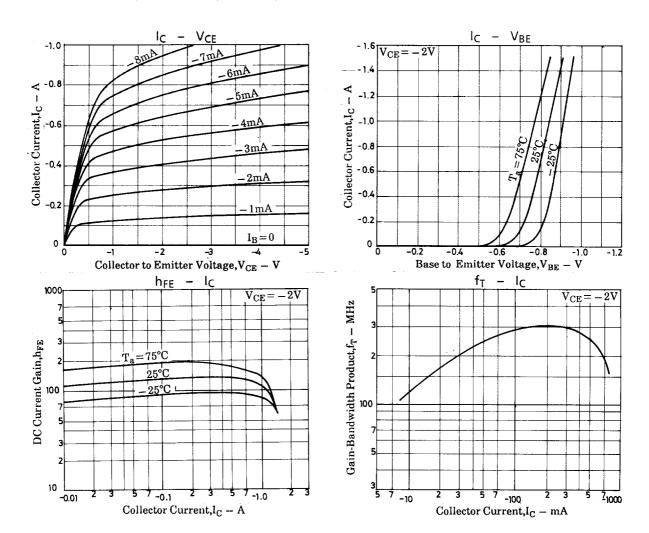
 $[\]ast$: The 2SA1729 is classified by 100mA h_{FE} as follows :

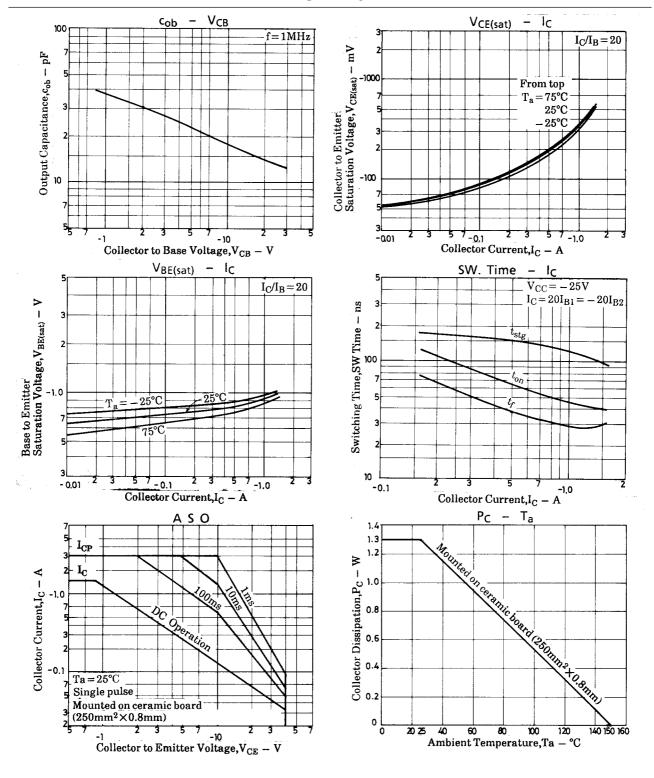
70	Q	140	100	R	200	140	S	280	
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Marking: AG h_{FE} rank : Q, R, S



Unit (resistance : Ω , capacitance : F)





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