

Ordering number:EN3366

PNP/NPN Epitaxial Planar Silicon Transistors



# 2SB1472/2SD2224

## Driver Applications

### Applications

- Motor drivers, printer hammer drivers, relay drivers, voltage regulator control.

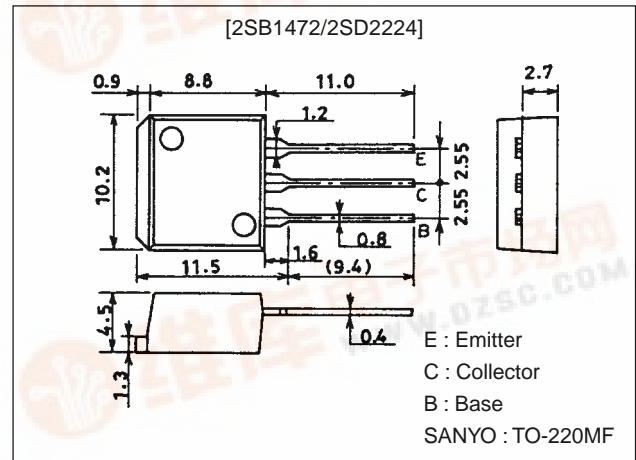
### Features

- Suitable for sets whose height is restricted.
- High DC current gain.
- Large current capacity and wide ASO.

### Package Dimensions

unit:mm

2049B



( ) : 2SB1472

### Specifications

#### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	$V_{CB0}$		(-70)	V
Collector-to-Emitter Voltage	$V_{CEO}$		(-60)	V
Emitter-to-Base Voltage	$V_{EBO}$		(-6)	V
Collector Current	$I_C$		(-7)	A
Collector Current (Pulse)	$I_{CP}$		(-10)	A
Collector Dissipation	$P_C$		1.65	W
		$T_c=25^\circ C$	35	W
Junction Temperature	$T_j$		150	°C
Storage Temperature	$T_{stg}$		-55 to +150	°C

#### Electrical Characteristics at Ta = 25°C

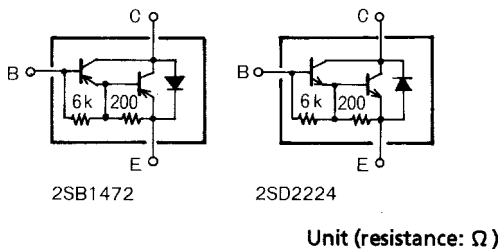
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	$I_{CBO}$	$V_{CB} = (-)40V, I_E = 0$			(-0.1)	mA
Emitter Cutoff Current	$I_{EBO}$	$V_{EB} = (-)5V, I_C = 0$			(-3.0)	mA
DC Current Gain	$h_{FE}$	$V_{CE} = (-)2V, I_C = (-)3.5A$	2000	5000		
Gain-Bandwidth Product	$f_T$	$V_{CE} = (-)5V, I_C = (-)3.5A$		20		MHz
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C = (-)3.5A, I_B = (-)7mA$		0.9	(-1.5)	V
				(-1.0)		V
Base-to-Emitter Saturation Voltage	$V_{BE(sat)}$	$I_C = (-)3.5V, I_B = (-)7mA$			(-2.0)	V

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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector-to-Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C = (-)5\text{mA}, I_E = 0$	(-)70			V
Collector-to-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C = (-)50\text{mA}, R_{BE} = \infty$	(-)60			V
Turn-ON Time	$t_{on}$	See specified test circuit.		(0.5)		$\mu\text{s}$
				0.6		$\mu\text{s}$
Storage Time	$t_{stg}$	See specified test circuit.		(1.5)		$\mu\text{s}$
				3.0		$\mu\text{s}$
Fall Time	$t_f$	See specified test circuit.		(1.4)		$\mu\text{s}$
				1.7		$\mu\text{s}$

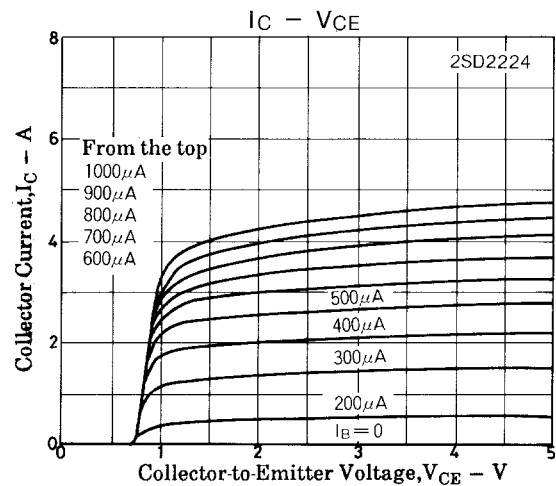
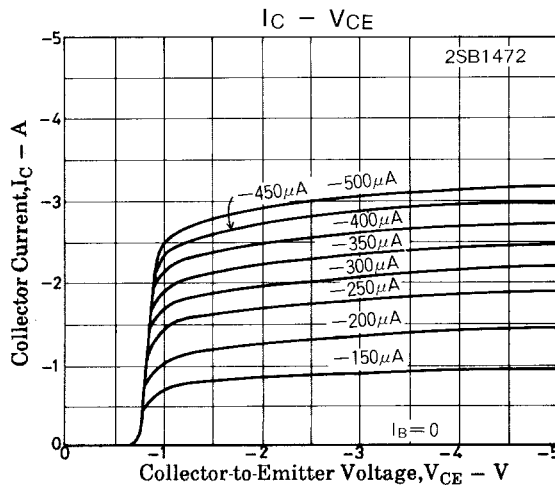
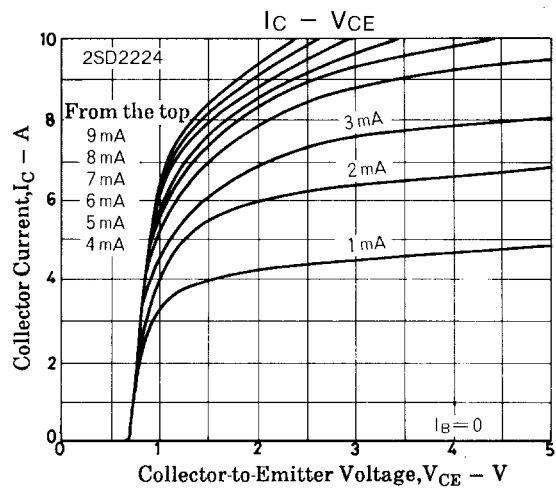
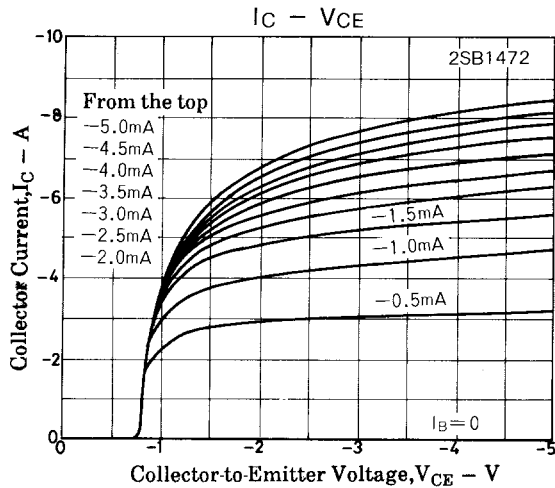
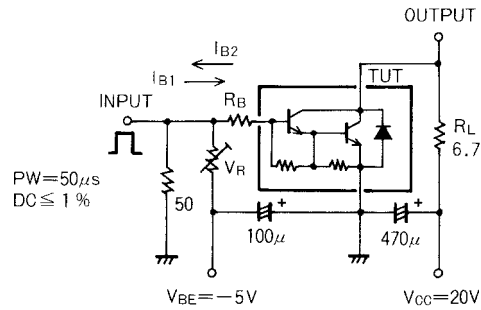
### Electrical Connection



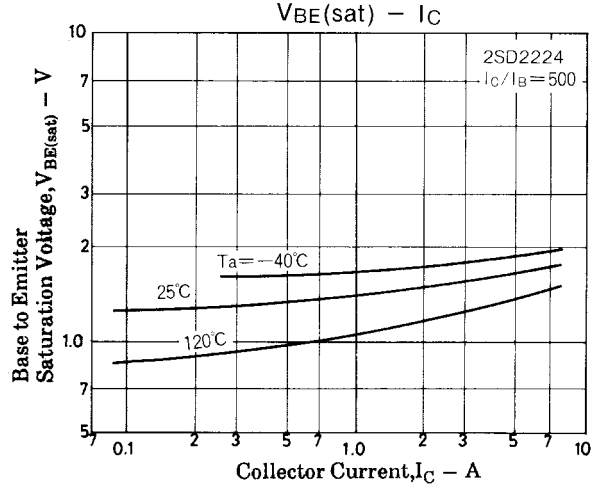
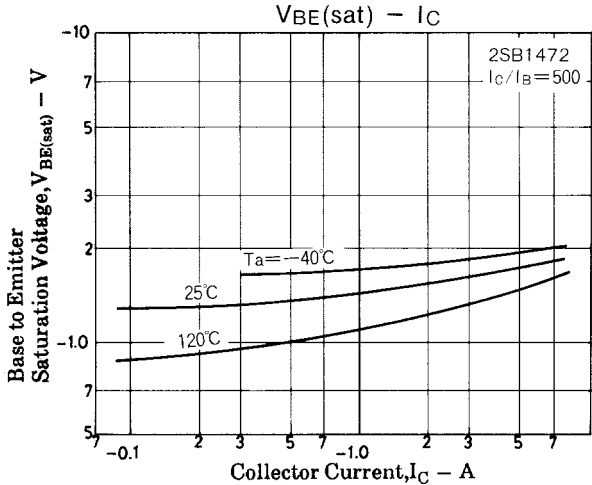
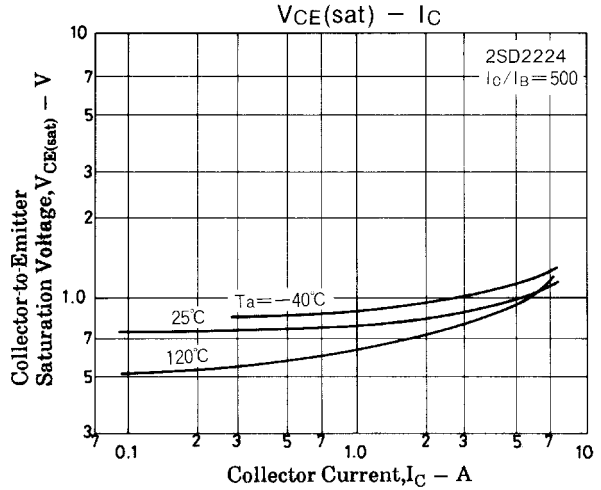
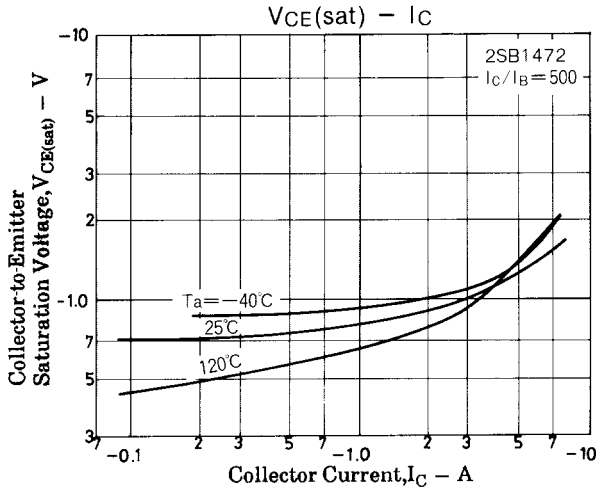
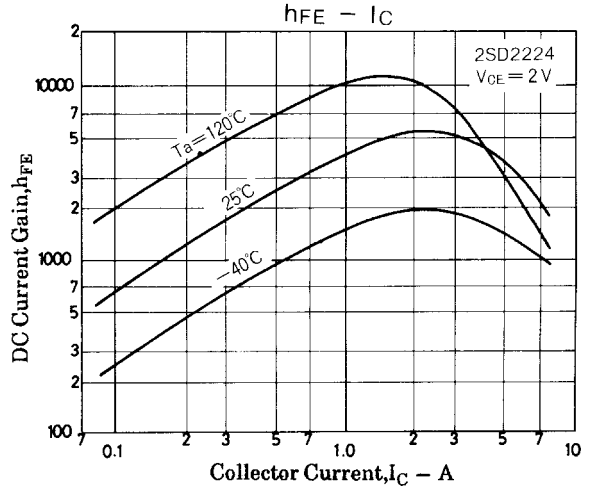
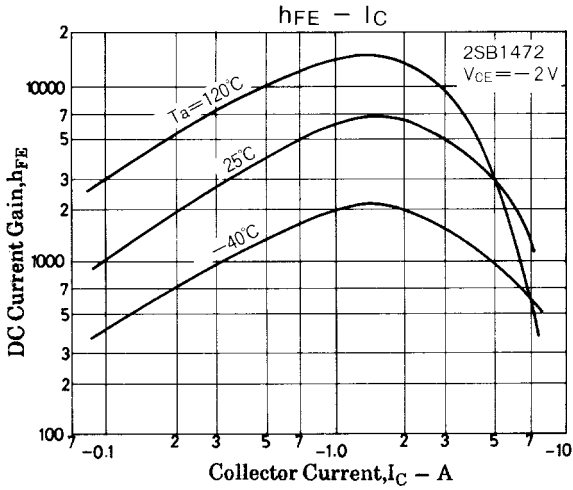
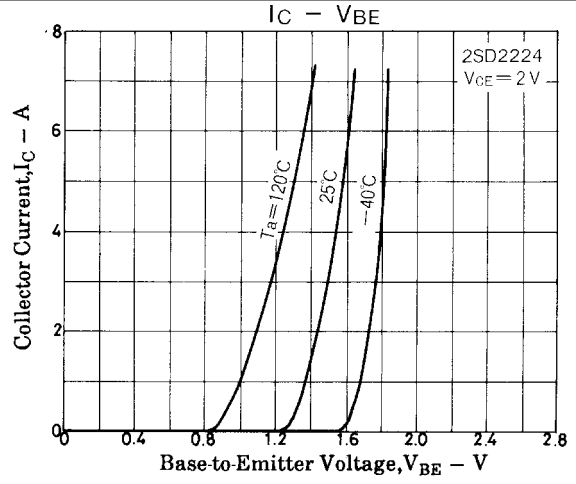
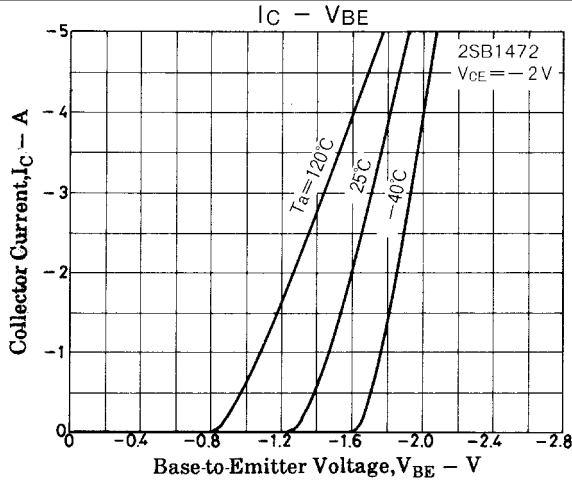
### Switching Time Test Circuit

(For PNP, the polarity is reversed.)

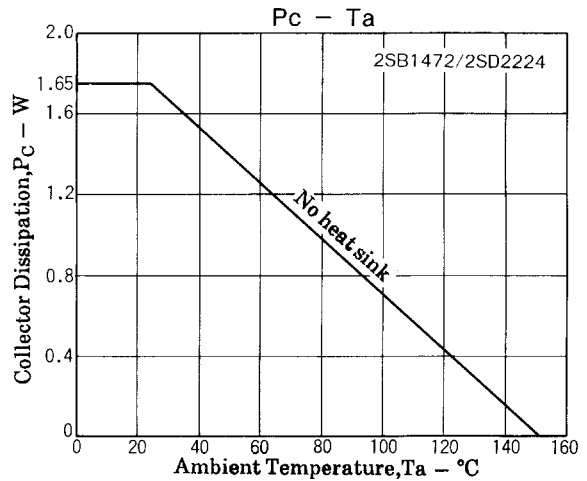
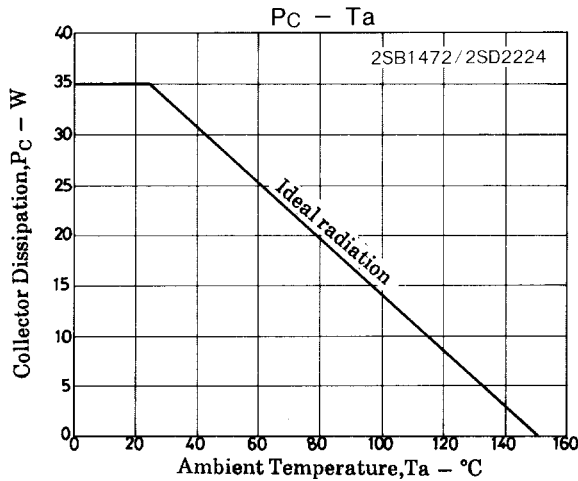
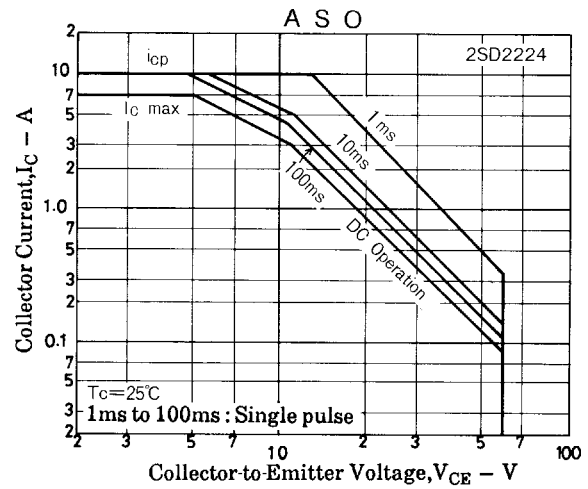
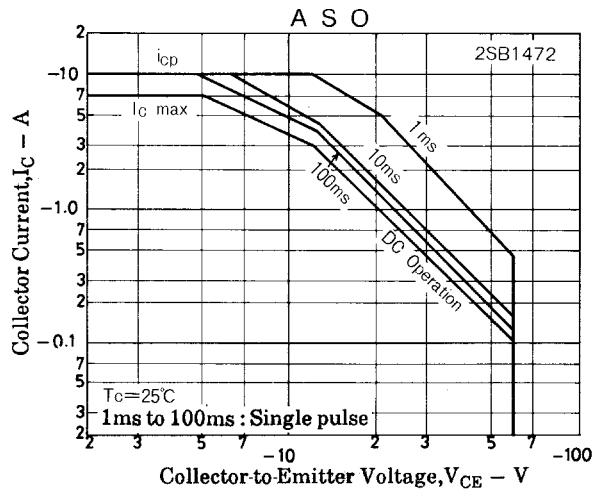
$$500I_{B1} = -500I_{B2} = I_C = 3\text{A}$$



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