PNP/NPN Epitaxial Planar Silicon Transistors



2SB1511/2SD2285

30V/20A High-Current Switching Applications

Applications

· Relay drivers, high-speed inverters, converters.

Features

- · Low collector-to-emitter saturation voltage: $V_{CE(sat)} = -0.5V (PNP), 0.4V (NPN) max.$
- · Large current capacity.
- · Micaless package facilitating easy mounting.

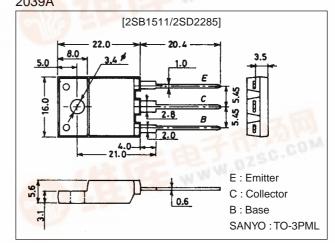
(): 2SB1511

Specifications

Absolute Maximum Ratings at Ta = 25°C

Package Dimensions

unit:mm 2039A



Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	Vсво		(–)60	V
Collector-to-Emitter Voltage	VCEO		(-)30	V
Emitter-to-Base Voltage	V _{EBO}	the second secon	(–)6	V
Collector Current	I _C	THE PERSON OF TH	(-)20	Α
Collector Current (Pulse)	ICP		(–)40	А
Collector Dissipation	PC	W AND AND ADDRESS OF THE PARTY	3.0	W
		Tc=25°C	40	W
Junction Temperature	Tj	run Fallis	150	°C
Storage Temperature	Tstg	D and a second	-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions		Ratings		
			min	typ	max	Unit
Collector Cutoff Current	ICBO	V _{CB} =(-)40V, I _E =0			(–)0.1	mA
Emitter Cutoff Current	I _{EBO}	V _{EB} =(-)4V, I _C =0			(-)0.1	mA
DC Current Gain	h _{FE} 1	V _{CE} =(-)2V, I _C =(-)1A	70*	4	280*	
	h _{FE} 2	V _{CE} =(-)2V, I _C =(-)10A	30		60-1	
Collector-to-Emitter Saturation Voltage	VCE(sat)	I _C =(-)8A, I _B =(-)0.4A		(-0.25)	(-0.5)	V
		477 715 12	-	0.2	0.4	V
Gain-Bandwidth Product	f _T	V _{CE} =(-)5V, I _C =(-)1A		120		MHz

* : The 2SB1511/2SD2285 are classified by 1A h_{FE} as follows : 70 Q

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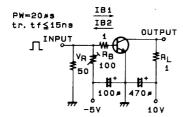
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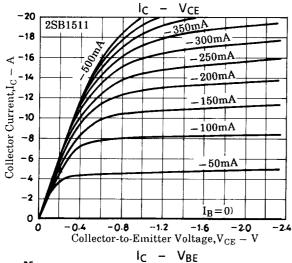
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Oill
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =(-)1mA, I _E =0	(–)60			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =(–)1mA, R _{BE} =∞	(–)30			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =(-)1mA, I _C =0	(–)6			V
Turn-ON Time	ton	See specified test circuit.		300		ns
Storage Time	t _{stg}	See specified test circuit.		(300)		ns
				600		ns
Fall Time	t _f	See specified test circuit.		20		ns

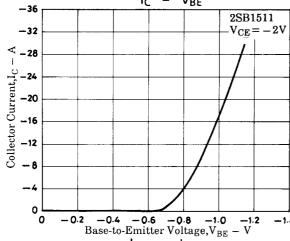
Switching Time Test Circuit

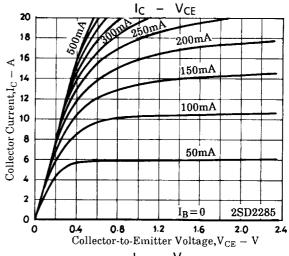


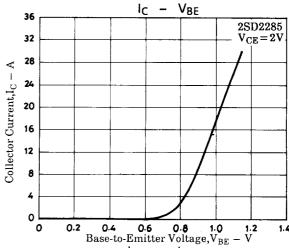
 $20I_B1 = -20I_B2 = I_C = 10A$ (For PNP, the polarity is reversed.)

Unit (resistance : Ω , capacitance : F)

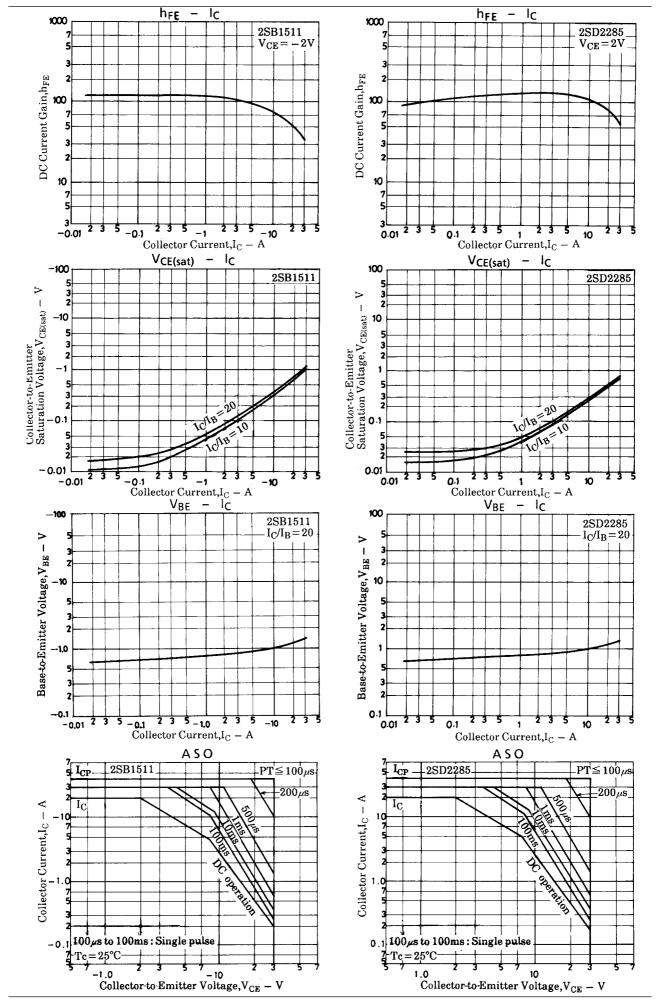




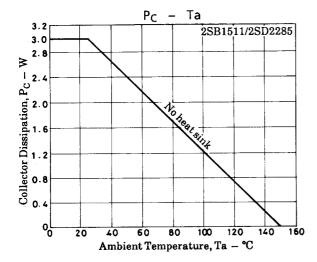


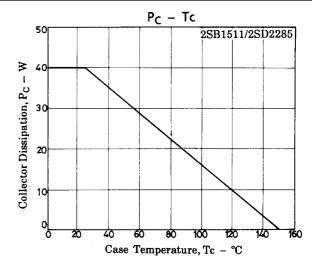


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