PNP/NPN Epitaxial Planar Silicon Tranasistors



2SB829/2SD1065

50V/15A Switching Applications

Applications

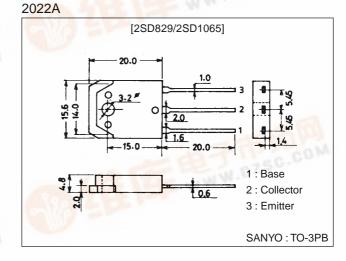
· Relay drivers, high-speed inverters, converters, and other general high-current switching applications.

Features

- · Low-saturation collector-to-emitter voltage : $V_{CE(sat)} = -0.5V \text{ max}$.
- · Wide ASO leading to high resistance to breakdown.

Package Dimensions

unit:mm



():2SB829

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		(-)60	V
Collector-to-Emitter Voltage	VCEO		(-)50	V
Emitter-to-Base Voltage	V _{EBO}	pull	(-)6	V
Collector Current	I _C	and the	(-)15	Α
Collector Current (Pulse)	I _{CP}	- s.de (1000 14)	(–)20	Α
Collector Dissipation	PC	Tc=25°C	90	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg	THU FILLS	-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions		Unit		
Farameter	Symbol	Conditions		typ	max	Offit
Collector Cutoff Current	I _{CBO}	V _{CB} =(-)40V, I _E =0			(-)0.1	mA
Emitter Cutoff Current	I _{EBO}	V _{EB} =(-)4V, I _C =0			(-)0.1	mA
DC Current Gain	h _{FE} 1	V _{CE} =(-)2V, I _C =(-)1A	70*		280*	W.
	h _{FE} 2	V _{CE} =(-)2V, I _C =(-)8A	30	- 11		1014
Gain-Bandwidth Product	fT	V _{CE} =(-)5V, I _C =(-)1A		20	20.	MHz
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =(-)8A, I _B =(-)0.4A	W.W.	(-0.26)	(-0.5)	V
				0.18	0.4	V

^{*:} The 2SB829/2SD1065 are classified by 1A h_{FE} as follows:

70	Q	140	100	R	200	140	S	280

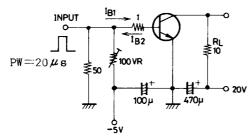
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SANYO Electric Co.,Ltd. Semiconductor Bussiness Headquaters

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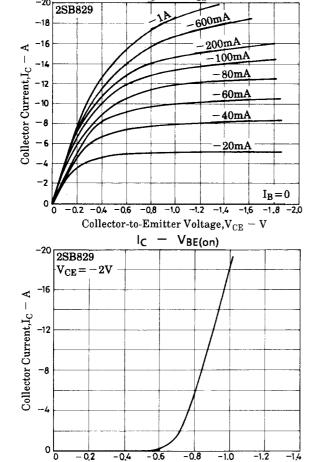
Parameter	Symbol	Conditions		Unit		
Faianielei	Symbol	Conditions	min	typ	max	Oille
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =(-)1mA, I _E =0	(–)60			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =(−)1mA, R _{BE} =∞	(–)50			V
Emitter-to-Base Breakdown Voltage	V _{(BR)EBO}	I _E =(-)1mA, I _C =0	(–)6			V
Turn-ON Time	ton	See specified Test Circuit		0.2		μs
Fall Time	t _f	See specified Test Circuit		(0.5)		μs
				1.0		μs
Storage Time	t _{stg}	See specified Test Circuit		0.1		μs

Switching Time Test Circuit

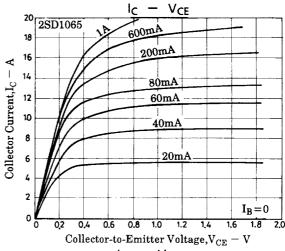


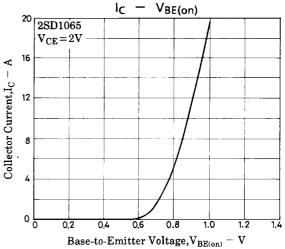
 $10I_{B1} = -10I_{B2} = I_{C} = 2A$

(For PNP, the polarity is reversed.) Unit (resistance: Ω , capacitance: F)

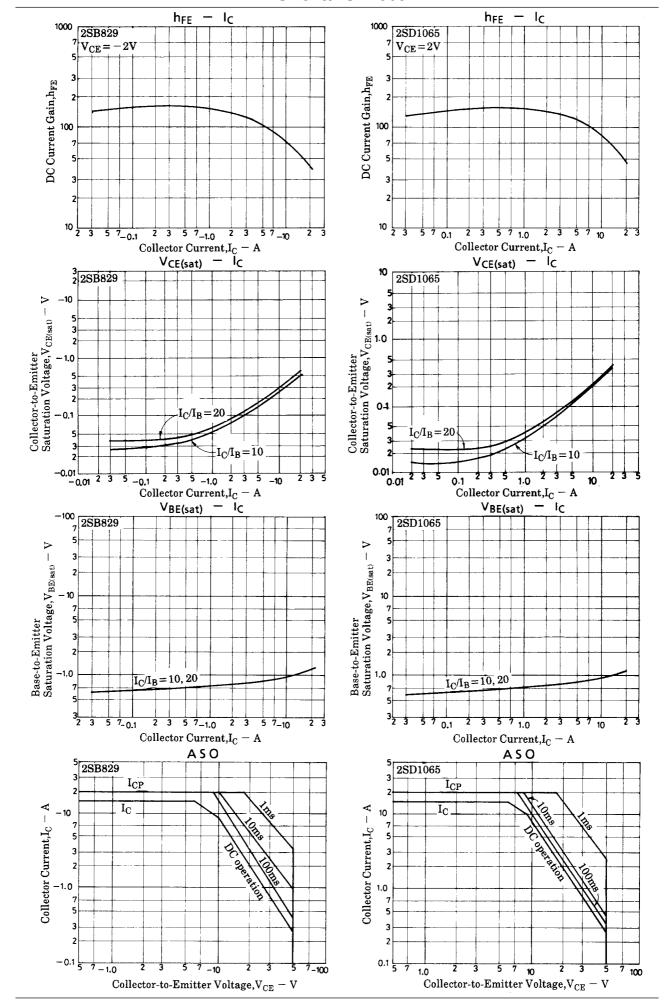


 ${\tt Base\text{-}to\text{-}Emitter\ Voltage,} V_{BE(on)} = V$

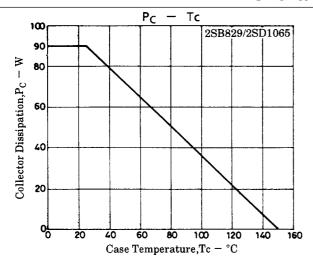




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