

NPN Triple Diffused Planar Silicon Transistor

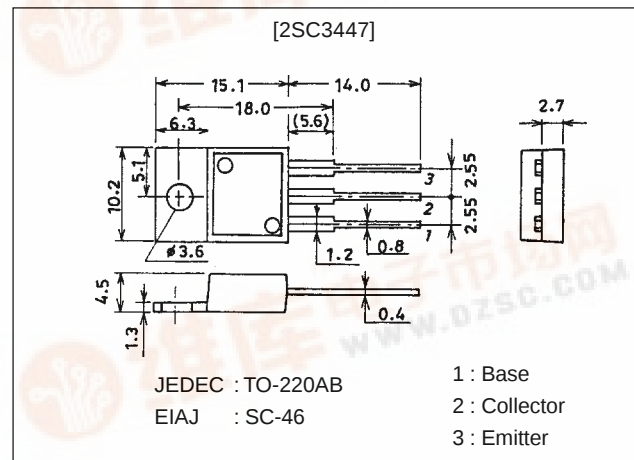
SANYO**2SC3447****500V/5A Switching Regulator Applications****Features**

- High breakdown voltage and high reliability.
- Fast switching speed (t_f : 0.1 μ s typ).
- Wide ASO.
- Adoption of MBIT process.

Package Dimensions

unit:mm

2010C

**Specifications****Absolute Maximum Ratings at Ta = 25°C**

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V_{CBO}		800	V
Collector-to-Emitter Voltage	V_{CEO}		500	V
Emitter-to-Base Voltage	V_{EBO}		7	V
Collector Current	I_C		5	A
Collector Current (Pulse)	I_{CP}	$PW \leq 300\mu s$, Duty Cycle $\leq 10\%$	10	A
Base Current	I_B		2	A
Collector Dissipation	P_C	$T_c = 25^\circ C$	50	W
Junction Temperature	T_j		150	$^\circ C$
Storage Temperature	T_{stg}		-55 to +150	$^\circ C$

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	I_{CBO}	$V_{CB} = 500V$, $I_E = 0$			10	μA
Emitter Cutoff Current	I_{EBO}	$V_{EB} = 5V$, $I_C = 0$			10	μA
DC Current Gain	h_{FE1}	$V_{CE} = 5V$, $I_C = 0.6A$	15*		50*	
	h_{FE2}	$V_{CE} = 5V$, $I_C = 3A$	8			
Gain-Bandwidth Product	f_T	$V_{CE} = 10V$, $I_C = 0.6A$		18		MHz
Output Capacitance	C_{ob}	$V_{CB} = 10V$, $f = 1MHz$		80		pF

* : The h_{FE1} of the 2SC3447 is classified as follows. When specifying the h_{FE1} rank, specify two ranks or more in principle.

15	L	30	20	M	40	30	N	50
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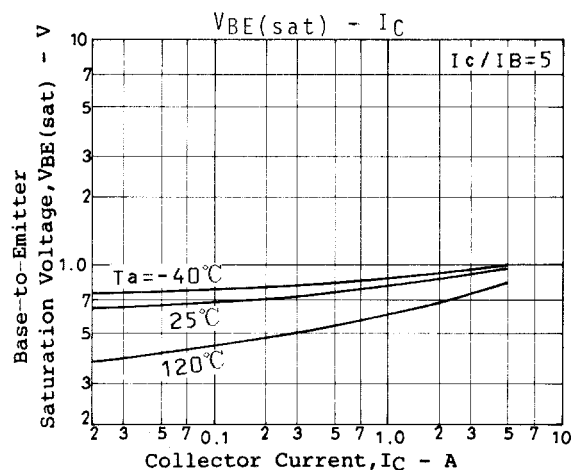
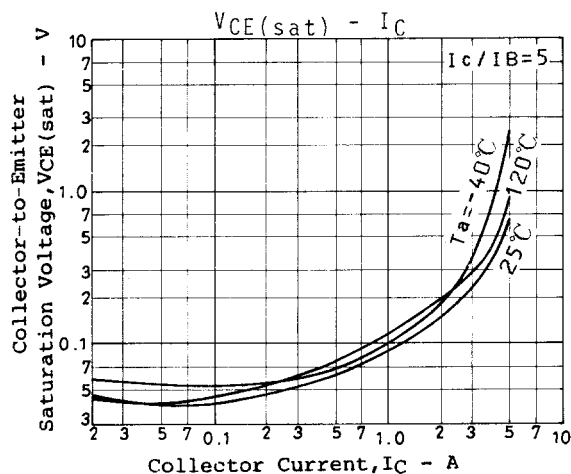
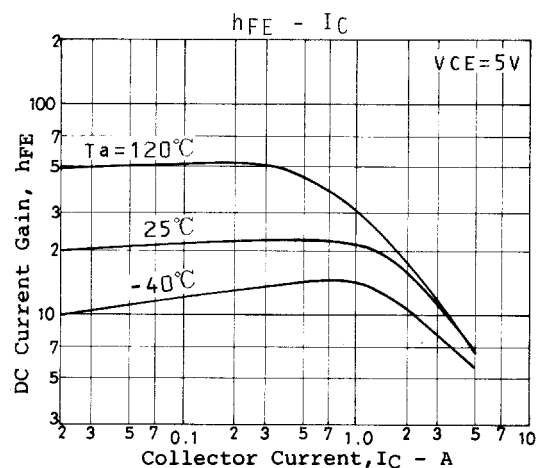
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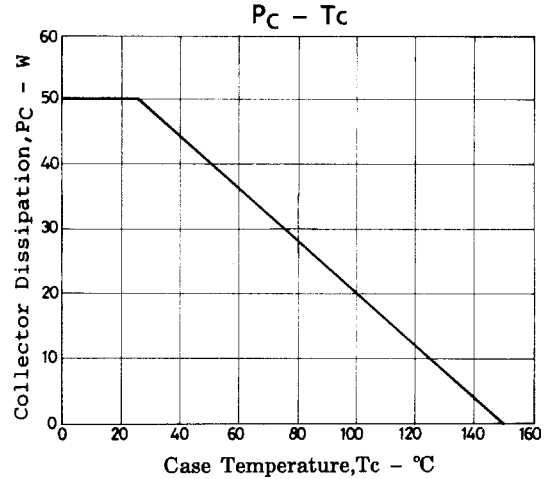
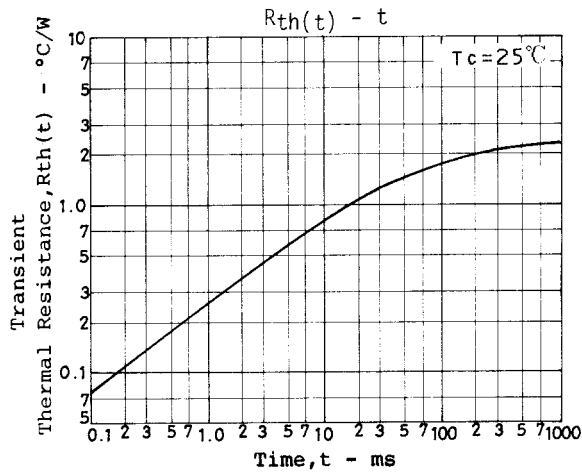
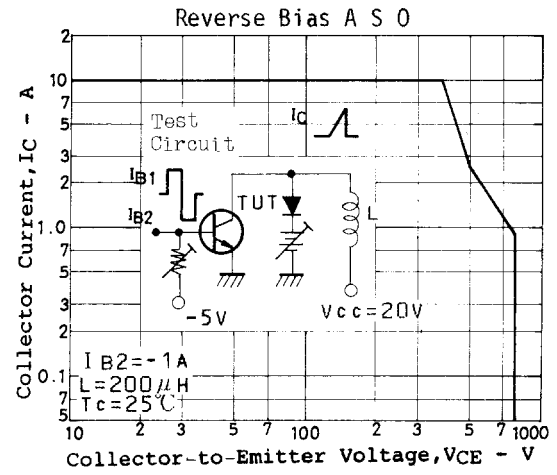
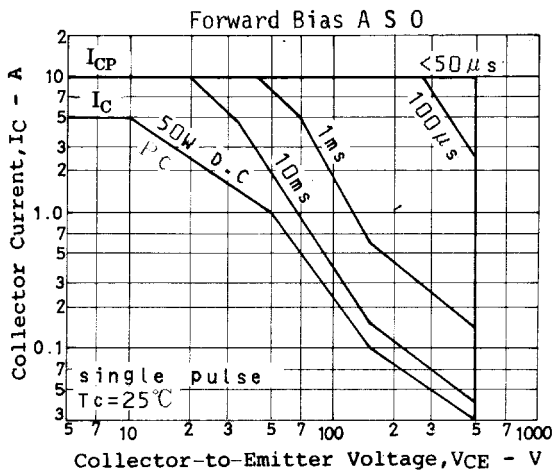
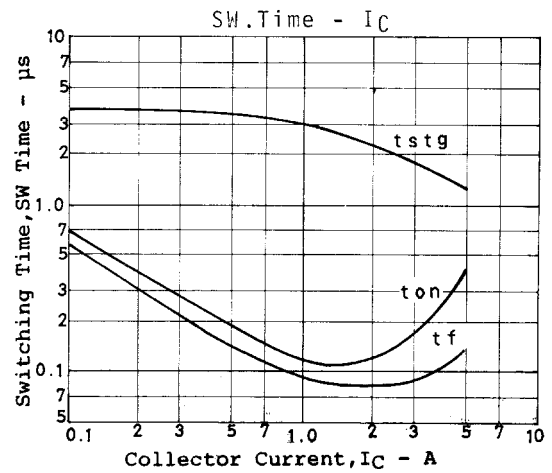
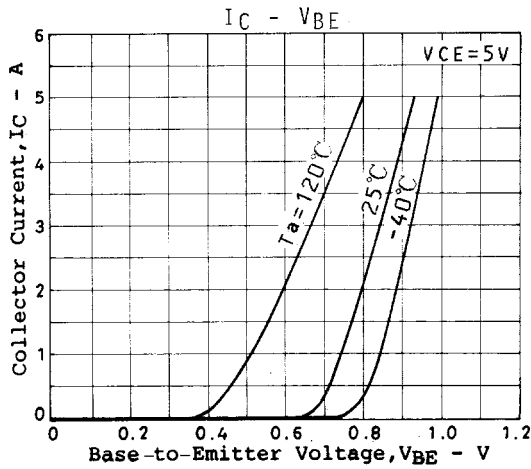
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C=3A, I_B=0.6A$			1.0	V
Base-to-Emitter Saturation Voltage	$V_{BE(sat)}$	$I_C=3A, I_B=0.6A$			1.5	V
Collector-to-Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C=1mA, I_E=0$	800			V
Collector-to-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C=5mA, R_{BE}=\infty$	500			V
Emitter-to-Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E=1mA, I_C=0$	7			V
Collector-to-Emitter Sustain Voltage	$V_{CEX(sus)}$	$I_C=2.5A, I_{B1}=-I_{B2}=1A, L=1mH, \text{clamped}$	500			V
Turn-ON Time	t_{on}	$V_{CC}=200V, 5I_{B1}=-2.5I_{B2}=I_C=4A, R_L=50\Omega$			0.5	μs
Storage Time	t_{stg}	$V_{CC}=200V, 5I_{B1}=-2.5I_{B2}=I_C=4A, R_L=50\Omega$			3.0	μs
Fall Time	t_f	$V_{CC}=200V, 5I_{B1}=-2.5I_{B2}=I_C=4A, R_L=50\Omega$			0.3	μs

The graph shows the relationship between Collector Current (I_C) and Collector-to-Emitter Voltage (V_{CE}) for a 2N4350 PNP silicon NPN transistor. The curves are plotted for various base currents (I_B).

V_{CE} (V)	$I_C = I_B = 0$ (A)	$I_B = 20\text{mA}$ (A)	$I_B = 50\text{mA}$ (A)	$I_B = 100\text{mA}$ (A)	$I_B = 200\text{mA}$ (A)	$I_B = 400\text{mA}$ (A)	$I_B = 600\text{mA}$ (A)	$I_B = 800\text{mA}$ (A)	$I_B = 1.0\text{A}$ (A)	$I_B = 1.2\text{A}$ (A)
0	0.00	0.00	0.00	0.00	0.00	0.00	0.00	0.00	0.00	0.00
1	0.00	0.02	0.05	0.10	0.20	0.40	0.60	0.80	1.00	1.20
2	0.00	0.02	0.05	0.10	0.20	0.40	0.60	0.80	1.00	1.20
3	0.00	0.02	0.05	0.10	0.20	0.40	0.60	0.80	1.00	1.20
4	0.00	0.02	0.05	0.10	0.20	0.40	0.60	0.80	1.00	1.20
5	0.00	0.02	0.05	0.10	0.20	0.40	0.60	0.80	1.00	1.20
6	0.00	0.02	0.05	0.10	0.20	0.40	0.60	0.80	1.00	1.20
7	0.00	0.02	0.05	0.10	0.20	0.40	0.60	0.80	1.00	1.20
8	0.00	0.02	0.05	0.10	0.20	0.40	0.60	0.80	1.00	1.20
9	0.00	0.02	0.05	0.10	0.20	0.40	0.60	0.80	1.00	1.20
10	0.00	0.02	0.05	0.10	0.20	0.40	0.60	0.80	1.00	1.20



2SC3447



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