NPN Triple Diffused Planar Silicon Transistor



2SC3449

500V/7A Switching Regulator Applications

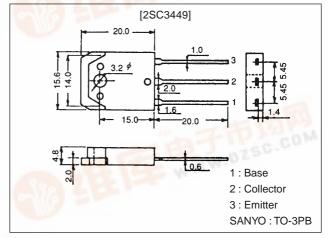
Features

- · High breakdown voltage and high reliability.
- · Fast switching speed (t_f : 0.1 μ s typ).
- · Wide ASO.
- · Adoption of MBIT process.

Package Dimensions

unit:mm

2022A



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions Ratings		Unit
Collector-to-Base Voltage	V _{CBO}		800	V
Collector-to-Emitter Voltage	V _{CEO}		500	V
Emitter-to-Base Voltage	V _{EBO}	pal .	7	V
Collector Current	IC			Α
Collector Current (Pulse)	ICP	PW≤300μs, Duty Cycle≤10%	14	А
Base Current	Ι _Β	AND AND THE W	3	Α
Collector Dissipation	PC	Tc=25°C	80	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg	0 - 24	-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions		Ratings		
	Symbol			typ	max	Unit
Collector Cutoff Current	I _{CBO}	V _{CB} =500V, I _E =0			10	μΑ
Emitter Cutoff Current	I _{EBO}	V _{EB} =5V, I _C =0			10	μΑ
DC Current Gain	h _{FE} 1	V _{CE} =5V, I _C =0.6A	15*	-11	50*	- 07
	h _{FE} 2	V _{CE} =5V, I _C =3A	8	f. n7	20.	
Gain-Bandwidth Product	f _T	V _{CE} =10V, I _C =0.6A	THE WWW	18		MHz
Output Capacitance	C _{ob}	V _{CB} =10V, f=1MHz		80		pF

*: The h_{FE}1 of the 2SC3449 is classified as follows. When specifying the h_{FE}1 rank, specify two ranks or more in principle.

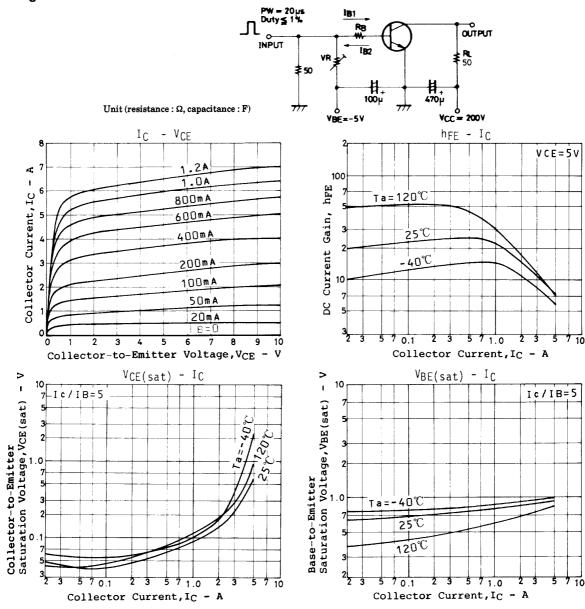
15	Т	30	20	М	40	30	N	50	

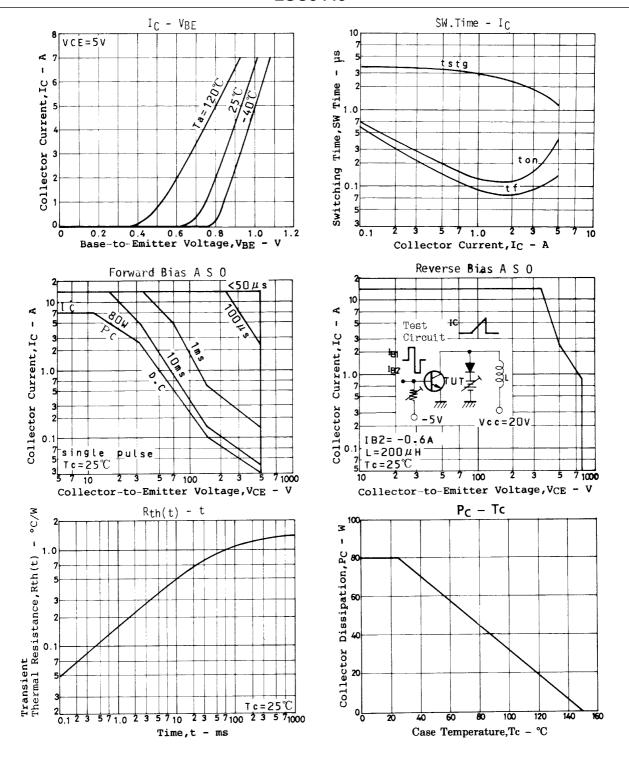
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Parameter	Symbol	Conditions		Unit		
Falanielei	Symbol	Conditions		typ	max	Offic
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =3A, I _B =0.6A			1.0	V
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =3A, I _B =0.6A			1.5	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =1mA, I _E =0	800			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =5mA, R _{BE} =∞	500			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =1mA, I _C =0	7			V
Collector-to-Emitter Sustain Voltage	V _{CEX(sus)}	I _C =2.5A, I _{B1} =-I _{B2} =1A, L=1mH, clamped	500			V
Turn-ON Time	ton	V_{CC} =200V, $5I_{B1}$ =-2. $5I_{B2}$ = I_{C} =4A, R_{L} = 50Ω			0.5	μs
Storage Time	t _{stg}	V _{CC} =200V, 5l _{B1} =-2.5l _{B2} =l _C =4A, R _L =50Ω			3.0	μs
Fall Time	t _f	V_{CC} =200V, $5I_{B1}$ =-2. $5I_{B2}$ = I_{C} =4A, R_{L} = 50Ω			0.3	μs

Switching Time Test Circuit





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