NPN Triple Diffused Planar Silicon Transistor



2SC4220

## 400V/7A Switching Regulator Applications

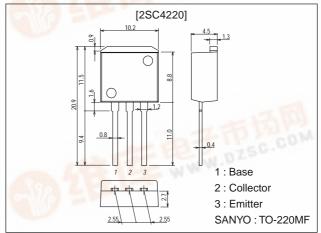
#### **Features**

- · High breakdown voltage, high reliability.
- · Fast switching speed ( $t_f=0.1\mu s$  typ).
- · Wide ASO.
- · Adoption of MBIT process.
- · Suitable for sets whose height is restricted.

## **Package Dimensions**

unit:mm

2049C



## **Specifications**

#### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		500	V
Collector-to-Emitter Voltage	VCEO		400	V
Emitter-to-Base Voltage	V <sub>EBO</sub>	140	7	V
Collector Current	IC	- CF	7	Α
Collector Current (Pulse)	I <sub>CP</sub>	PW≤300μs, duty cycle≤10%	14	Α
Base Current	I <sub>B</sub>	AND ARE INC. W	3	Α
Collector Dissipation	PC		1.65	W
		Tc=25°C	50	W
Junction Temperature	Tj	O Table	150	°C
Storage Temperature	Tstg	Com	-55 to +150	°C

#### Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector Cutoff Current	ICBO	V <sub>CB</sub> =400V, I <sub>E</sub> =0		51	10	μΑ
Emitter Cutoff Current	I <sub>EBO</sub>	V <sub>EB</sub> =5V, I <sub>C</sub> =0	40.7	- 11	10	μΑ
	*hFE1	V <sub>CE</sub> =5V, I <sub>C</sub> =0.8A	15	u 07	50	77-
DC Current Gain	h <sub>FE</sub> 2	V <sub>CE</sub> =5V, I <sub>C</sub> =4A	10			
	h <sub>FE</sub> 3	V <sub>CE</sub> =5V, I <sub>C</sub> =10mA	10			

<sup>\*:</sup> The h<sub>FE</sub>1 of the 2SC4220 is classified as follows. When specifying the h<sub>FE</sub>1 rank, specify two ranks or more in principle.

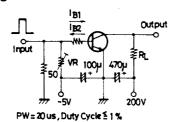
15 L 30 20 M 40 30 N 50

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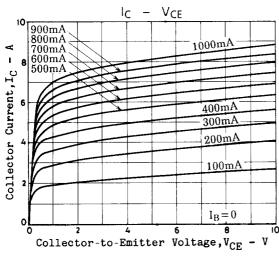
# SANYO Electric Co.,Ltd. Semiconductor Bussiness Headquaters

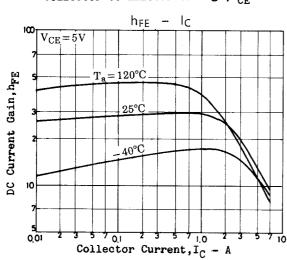
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Uill
Collector-to-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =4A, I <sub>B</sub> =0.8A			0.8	V
Base-to-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =4A, I <sub>B</sub> =0.8A			1.5	V
Gain-Bandwidth Product	fT	V <sub>CE</sub> =10V, I <sub>C</sub> =0.8A		20		MHz
Output Capacitance	C <sub>ob</sub>	V <sub>CB</sub> =10V, f=1MHz		80		pF
Collector-to-Base Breakdown Voltage	V(BR)CBO	I <sub>C</sub> =1mA, I <sub>E</sub> =0	500			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I <sub>C</sub> =5mA, R <sub>BE</sub> =∞	400			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I <sub>E</sub> =1mA, I <sub>C</sub> =0	7			V
Collector-to-Emitter Sustain Voltage	VCEX(sus)	I <sub>C</sub> =3A, I <sub>B1</sub> =-0.3A, L=1mH, I <sub>B2</sub> =-1.2A, clamped	400			V
Turn-ON Time	ton	I <sub>C</sub> =5A, I <sub>B1</sub> =1A, I <sub>B2</sub> =-2A, R <sub>L</sub> =40Ω, V <sub>CC</sub> =200V			0.5	μs
Storage Time	t <sub>stg</sub>	I <sub>C</sub> =5A, I <sub>B1</sub> =1A, I <sub>B2</sub> =-2A, R <sub>L</sub> =40Ω, V <sub>CC</sub> =200V			2.5	μs
Fall Time	t <sub>f</sub>	I <sub>C</sub> =5A, I <sub>B1</sub> =1A, I <sub>B2</sub> =-2A, R <sub>L</sub> =40Ω, V <sub>CC</sub> =200V			0.3	μs

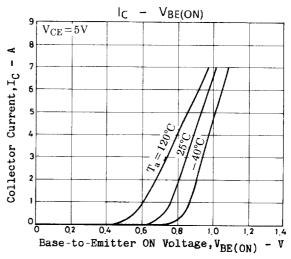
### **Switching Time Test Circuit**

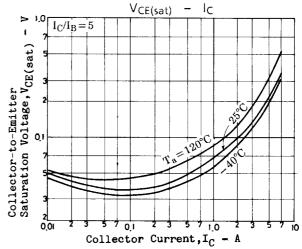


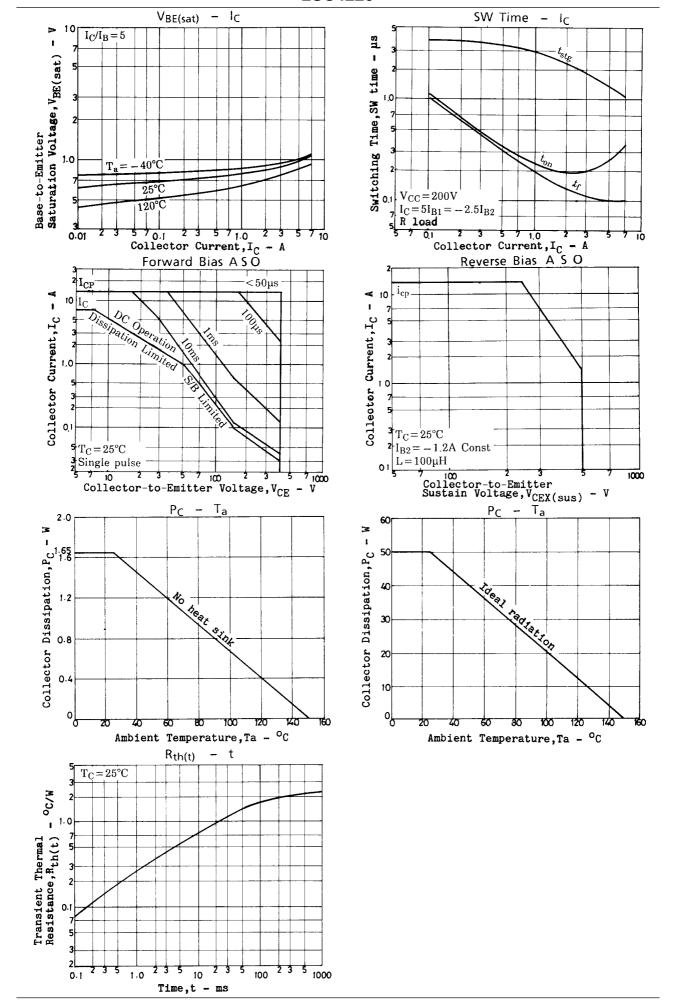
Unit (resistance:  $\Omega$ , capacitance: F)











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