PNP/NPN Epitaxial Planar Silicon Transistors



# 2SB1121/2SD1621

# **High-Current Driver Applications**

## **Applications**

· Voltage regulators, relay drivers, lamp drivers, electrical equipment.

#### **Features**

- · Adoption of FBET, MBIT processes.
- · Low collector-to-emitter saturation voltage.
- · Large current capacity and wide ASO.
- · Fast switching speed.
- Very small size making it easy to provide highdensity, small-sized hybrid IC's.

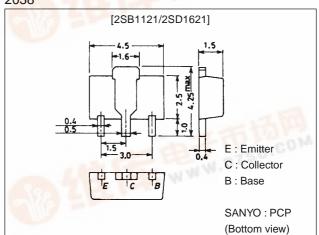
#### (): 2SB1121

## **Specifications**

## Absolute Maximum Ratings at Ta = 25°C

## **Package Dimensions**

unit:mm 2038



Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V <sub>CBO</sub>		(-)30	V
Collector-to-Emitter Voltage	VCEO		(-)25	V
Emitter-to-Base Voltage	VEBO		(-)6	V
Collector Current	IC	THE RESERVE THE PARTY OF THE PA	(-)2	Α
Collector Current (Pulse)	I <sub>CP</sub>	1 to	(–)5	Α
Collector Dissipation	PC	AND AND LOS W	500	mW
		Mounted on ceramic board (250mm²×0.8mm)	1.3	W
Junction Temperature	Tj	- 100	150	°C
Storage Temperature	Tstg	- n M	-55 to +150	°C

### Electrical Characteristics at Ta = 25°C

Parameter		Conditions	Ratings			Linit	
	Symbol		min	typ	max	Unit	
Collector Cutoff Current	I <sub>CBO</sub>	V <sub>CB</sub> =(-)20V, I <sub>E</sub> =0				(-)0.1	μΑ
Emitter Cutoff Current	I <sub>EBO</sub>	V <sub>EB</sub> =(-)4V, I <sub>C</sub> =0	- 10		1.77	(-)0.1	μΑ
DC Current Gain	h <sub>FE</sub> 1	V <sub>CE</sub> =(-)2V, I <sub>C</sub> =(-)100mA		100*		560*	,,,,,,,
	h <sub>FE</sub> 2	V <sub>CE</sub> =(-)2V, I <sub>C</sub> =(-)1.5A		65	M.D.		4 T W
Gain-Bandwidth Product	f⊤	V <sub>CF</sub> =(-)10V, I <sub>C</sub> =(-)50mA		W	150		MHz

 $\ensuremath{^*}$  ; The 2SB1121/2SD1621 are classified by 100mA  $\ensuremath{h_{FE}}$  as follows :

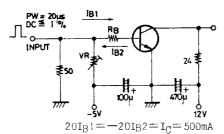
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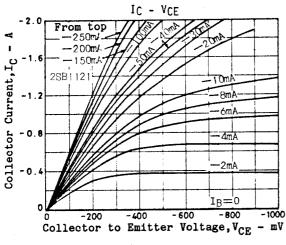
Parameter	Symbol	Conditions		Ratings		
	Symbol		min	typ	max	Unit
Collector-to-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =(-)1.5A, I <sub>B</sub> =(-)75mA		0.18	0.4	V
				(-0.35)	(-0.6)	V
Base-to-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =(-)1.5A, I <sub>B</sub> =(-)75mA		(-)0.85	(-)1.2	V
Collector-to-Base Breakdown Voltage	V <sub>(BR)</sub> CBO	I <sub>C</sub> =(-)10μA, I <sub>E</sub> =0	(-)30			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I <sub>C</sub> =(−)1mA, R <sub>BE</sub> =∞	(-)25			V
Emitter-to-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	I <sub>E</sub> =(-)10μΑ, I <sub>C</sub> =0	(-)6			V
Output Capacitance	C <sub>ob</sub>	V <sub>CB</sub> =(-)10V, f=1MHz		19		pF
				(32)		pF
Turn-ON Time	t <sub>on</sub>	See specified Test Circuit.		60		ns
				(60)		ns
Storage Time	t <sub>stg</sub>	See specified Test Circuit.		500		ns
				(350)		ns
Fall Time	t <sub>f</sub>	See specified Test Circuit.		25		ns
				(25)		ns

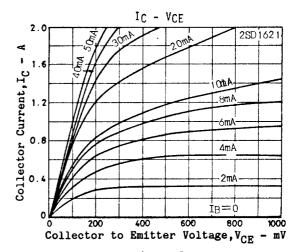
#### **Switching Time Test Circuit**

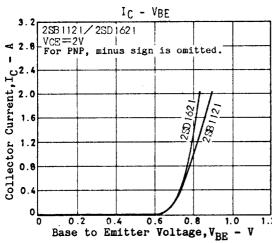


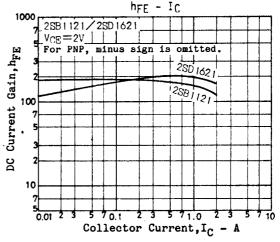
Marking 2SB1121:BD 2SD1621:DD h<sub>FE</sub> rank :R,S,T,U

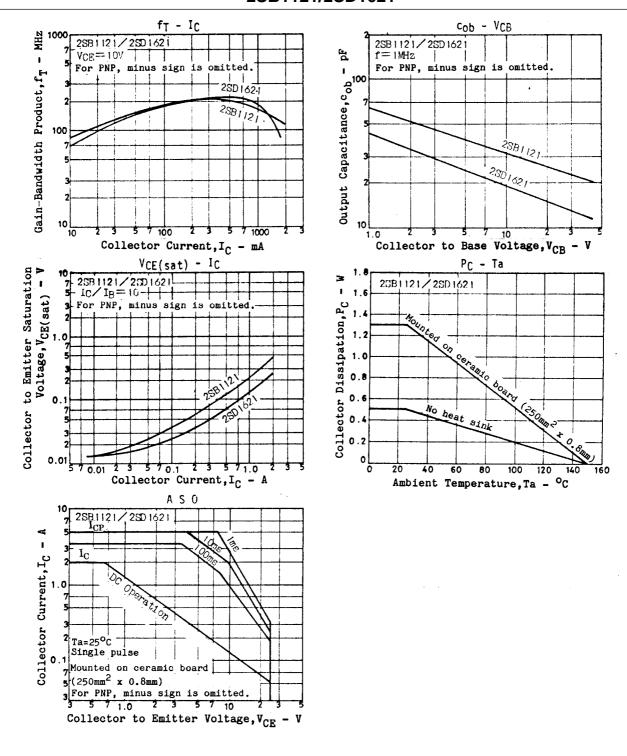
(For PNP, the polarity is reversed.) Unit (resistance:  $\Omega$ , capacitance: F)











#### 2SB1121/2SD1621

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