Ordering number : ENN6979

P-Channel Silicon MOSFET

2SJ596



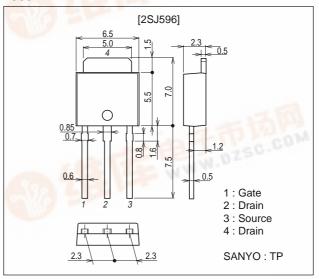
DC / DC Converter Applications

Features

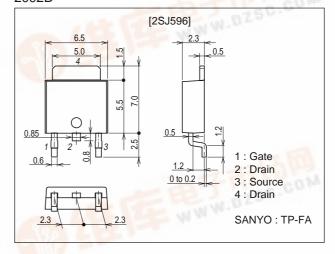
- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · 4V drive.

Package Dimensions

unit : mm 2083B



unit : mm 2092B



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Specifications

Absolute Maximum Ratings at Ta=25°C

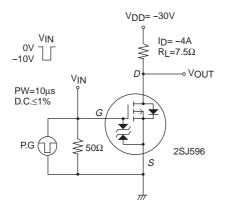
Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		-60	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		-8	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-32	Α
Allowable Power Dissipation	D-		1	W
	PD	Tc=25°C	20	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

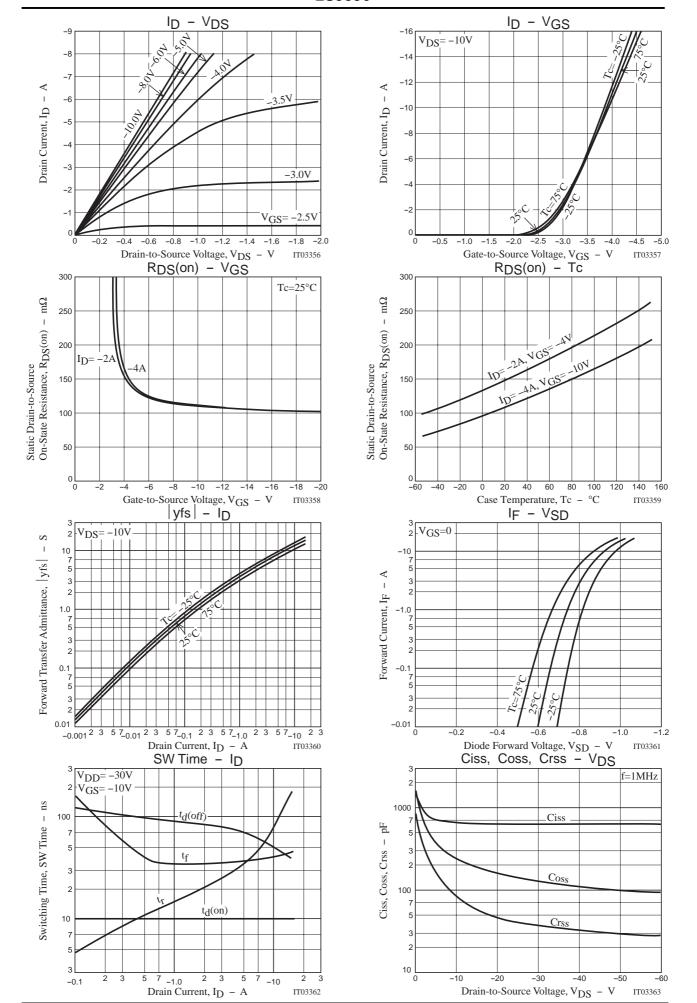
Electrical Characteristics at Ta=25°C

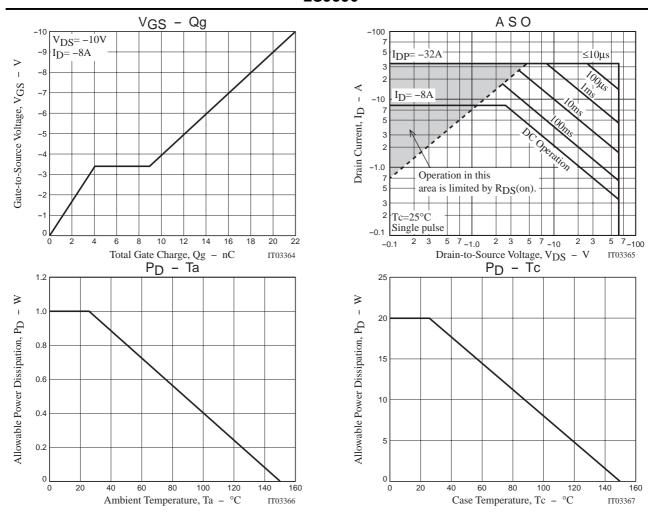
Parameter	Symbol	Conditions		Ratings		
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0	-60			V
Zero-Gate Voltage Drain Current	IDSS	VDS=-60V, VGS=0			-10	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =-10V, I _D =-1mA	-1.0		-2.4	V
Forward Transfer Admittance	yfs	V _D S=-10V, I _D =-4A	5	7		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =-4A, V _G S=-10V		110	145	mΩ
	R _{DS} (on)2	I _D =-2A, V _G S=-4V		155	220	mΩ
Input Capacitance	Ciss	VDS=-20V, f=1MHz		680		pF
Output Capacitance	Coss	V _{DS} =-20V, f=1MHz		170		pF
Reverse Transfer Capacitance	Crss	V _{DS} =-20V, f=1MHz		50		pF
Turn-ON Delay Time	td(on)	See specified Test Circuit		10		ns
Rise Time	t _r	See specified Test Circuit		30		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		75		ns
Fall Time	tf	See specified Test Circuit		35		ns
Total Gate Charge	Qg	V _{DS} =-10V, V _{GS} =-10V, I _D =-8A		22		nC
Gate-to-Source Charge	Qgs	V _{DS} =-10V, V _{GS} =-10V, I _D =-8A		4		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =-10V, V _{GS} =-10V, I _D =-8A		5		nC
Diode Forward Voltage	V _{SD}	I _S =-8A, V _G S=0		-0.9	-1.2	V

Marking: J596

Switching Time Test Circuit







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