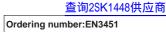
#### 捷多邦,专业PCB打样工厂,24小时加急出货



N-Channel Silicon MOSFET

# 2SK1448 Ultrahigh-Speed Switching Applications

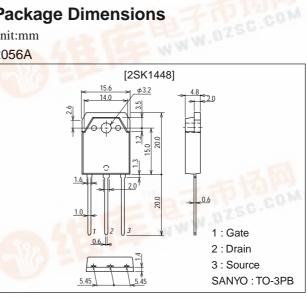
## **Features**

- · Low ON-state resistance.
- · Ultrahigh-speed switching.

## **Package Dimensions**

## unit:mm





## **Specifications**

## Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		450	V
Gate-to-Source Voltage	V <sub>GSS</sub>		±30	V
Drain Current (DC)	۱ <sub>D</sub>		8	А
Drain Current (Pulse)	IDP	PW≤10µs, duty cycle≤1%	32	A
Allowable Power Dissipation	P-	Tc=25°C	100	W
	PD		2.5	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

## Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =1mA, V <sub>GS</sub> =0	450			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =450V, V <sub>GS</sub> =0			1.0	mA
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0		15	±100	nA
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	2.0		3.0	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =4A	3.0	6.0	201	S
Static Drain-to-Source ON-State Resistance	R <sub>DS(on)</sub>	I <sub>D</sub> =4A, V <sub>GS</sub> =10V	11.14	0.6	0.8	Ω

(Note) Be careful in handling the 2SK1448 because it has no protection diode between gate and source.

Continued on next page.

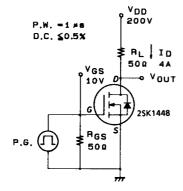
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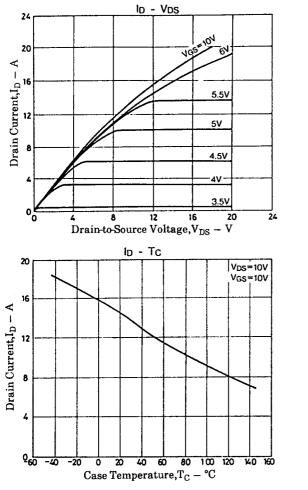
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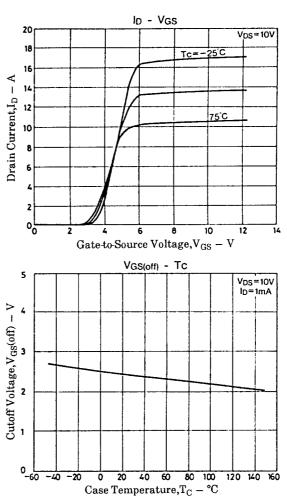
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Parameter	Symbol	Conditions	Ratings			Unit
	Symbol		min	typ	max	Offic
Input Capacitance	Ciss	V <sub>DS</sub> =20V, f=1MHz		1200		pF
Output Capacitance	Coss	V <sub>DS</sub> =20V, f=1MHz		180		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =20V, f=1MHz		70		pF
Turn-ON Delay Time	<sup>t</sup> d(on)	I <sub>D</sub> =4A, V <sub>GS</sub> =10V, V <sub>DD</sub> =200V, R <sub>GS</sub> =50Ω		20		ns
Rise Time	t <sub>r</sub>	I <sub>D</sub> =4A, V <sub>GS</sub> =10V, V <sub>DD</sub> =200V, R <sub>GS</sub> =50Ω		40		ns
Turn-OFF Delay Time	<sup>t</sup> d(off)	$I_D=4A, V_{GS}=10V, V_{DD}=200V, R_{GS}=50\Omega$		160		ns
Fall Time	tf	I <sub>D</sub> =4A, V <sub>GS</sub> =10V, V <sub>DD</sub> =200V, R <sub>GS</sub> =50Ω		60		ns
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =8A, V <sub>GS</sub> =0			1.8	V

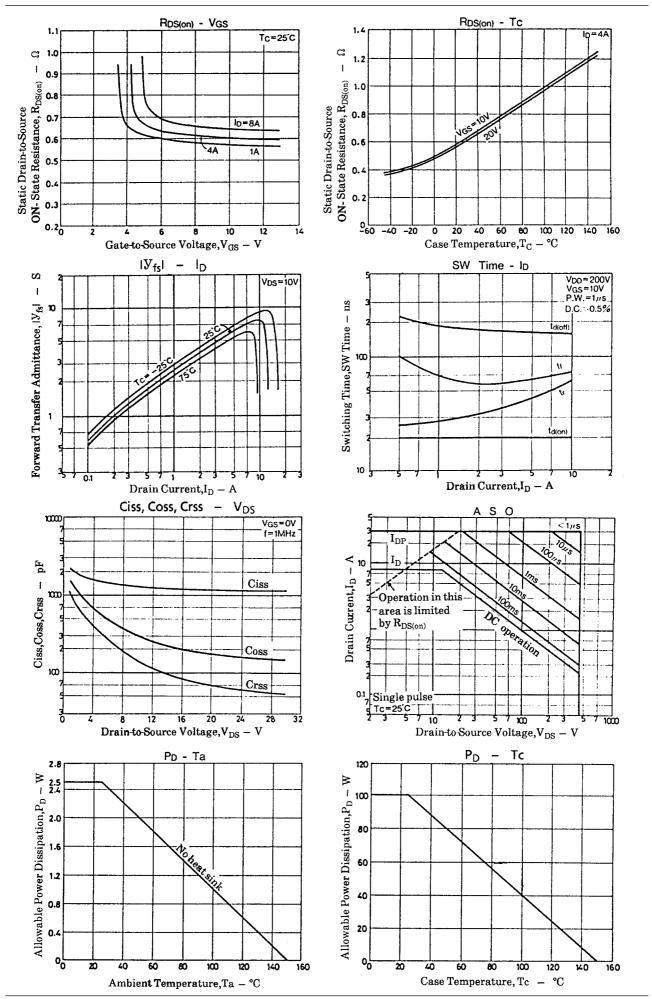
#### **Switching Time Test Circuit**







2SK1448



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