Ordering number: EN3774

N-Channel Silicon MOSFET



2SK1473

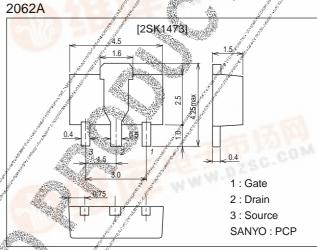
Ultrahigh-Speed Switching Applications

Features

- · Low ON resistance.
- · Ultrahigh-speed switching.
- · Low-voltage drive.

Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol / / Conditions	Ratings	Unit
Drain-to-Source Voltage	VD\$\$	100	V
Gate-to-Source Voltage	Vess	±15	V
Drain Current (DC)	/J6	2	Α
Drain Current (pulse)	PW≤10µs, duty cycle≤1%	8	Α
Allowable Power Dissipation	PD TC=25°C	3.5	W
	Mounted on a ceramic board (250mm²×0.8mm)	1.5	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tatg	-55 to +150	°C

Electrical Characteristics at Ta = 25°C

I I Warm	90000	r - <i>f J</i>	1			
Parameter	Symbol	Conditions	Ratings			Unit
T diameter 4	Symbol .		min	typ	max	Onne
Drain-to-Source Breakdown Voltage	V(BR)DSS/	ID=1mA, V _{GS} =0	100			V
Zero-Gate Votlage Drain Current	l _{DSS} /	V _{DS} =100V, V _{GS} =0			100	μΑ
Gate-to-Source Leakage Current	IG\$S.	$V_{GS}=\pm 12V$, $V_{DS}=0$		-	±10	μΑ
Cutoff Voltage	VGS(off)	V_{DS} =10V, I_D =1mA	1.0	- 11	2.0	V
Forward Transfer Admittance	/ Jyfs	V _{DS} =10V, I _D =1A	1.2	2.0	90.	S
Static Drain-to-Source On State Resistance	R _{DS(on)} 1	I _D =1A, V _{GS} =10V	A.A.	0.7	0.95	Ω
Static Drain-to-Source On-State Resistance	R _{DS(on)} 2	I _D =1A, V _{GS} =4V		0.95	1.3	Ω

Continued on next page.

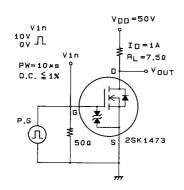
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Continued from preceding page.

Parameter	Symbol	Conditions	Ratings			Unit
Input Capacitance	Ciss	V _{DS} =20V, f=1MHz		150		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		35		pF
Reverse Transfer Capacitance	Crss	V _{DS} =20V, f=1MHz	aš.	6		pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit	1	6		ns
Rise Time	t _r	See specified Test Circuit	A A STATE OF THE PARTY OF THE P	10		ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit	A F	- 60	**************************************	ns
Fall Time	t _f	See specified Test Circuit	A A	20	W. W. British	ns
Diode Forward Voltage	V _{SD}	I _S =2A, V _{GS} =0		1.0	All Parish Control	V

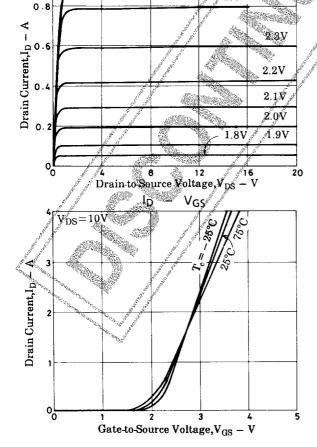
Switching Time Test Circuit

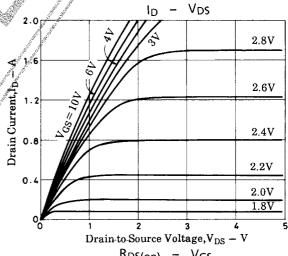


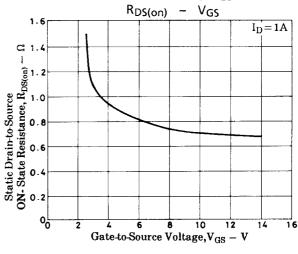
ID - VDS

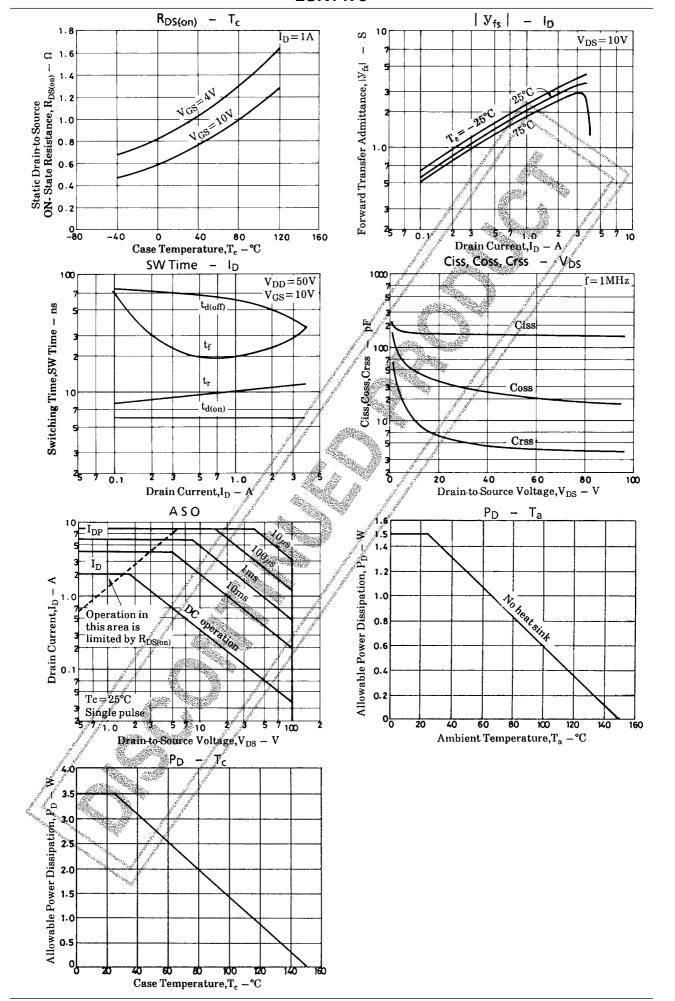
2.4V

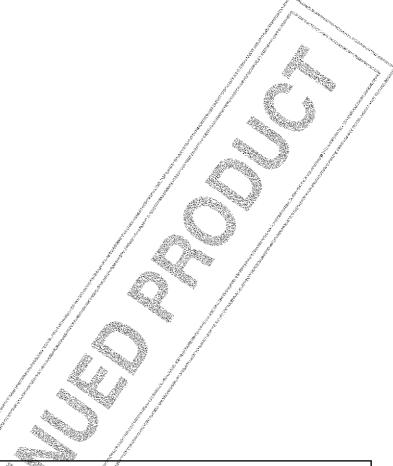
 $\overline{V_{\rm GS}} = 2.5 {
m V}$











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