

SANYO assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO products described or contained herein.

Specifications

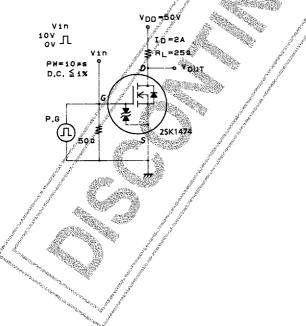
Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions		Ratings	Unit
Drain-to-Source Voltage	V _{DSS}			100	V
Gate-to-Source Voltage	V _{GSS}		6	±15	V
Drain Current (DC)	۱ _D		and a second	4	A
Drain Current (pulse)	I _{DP}	PW≤10µs, duty cycle≤1%	y at a set	16	A
Allowable Power Dissipation	PD	Tc=25°C	get get	20	W
Channel Temperature	Tch		and the second	150	°C
Storage Temperature	Tstg		1	5 5 to +150	∫^°C
Electrical Characteristics a	t Ta = 25°C	•		2.41 B J	

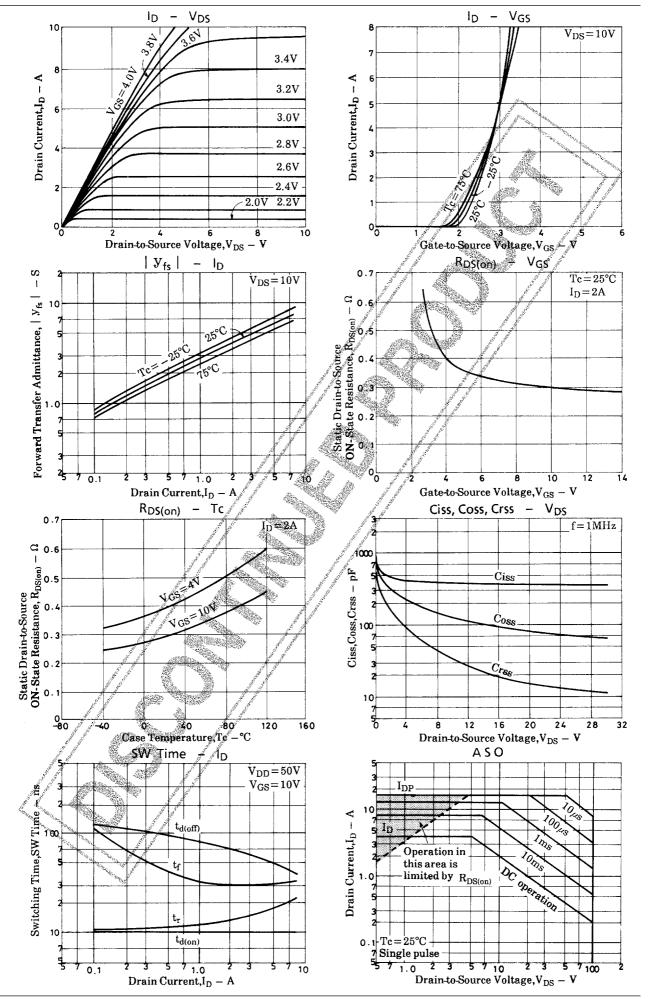
Electrical Characteristics at Ta = 25°C

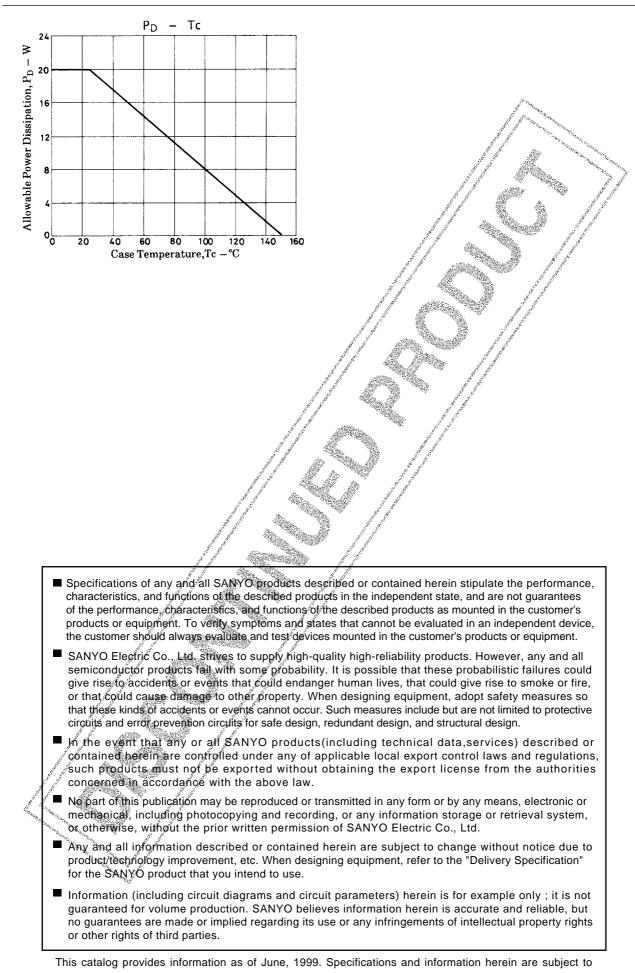
23 C			1 Star		
Symbol	Conditions	min	Ratings typ	max	Unit
V(BR)DSS	ID=1mA, VGS=0	00° 👷	and and		V
	IG=±100μA, V _{DS} =0	±15			V
IDSS	V _{DS} =100V, V _{GS} =0	and the second se	Allar	100	μA
IGSS	V _{GS} =±12V, V _{DS} =0	See	E.	±10	μA
VGS(off)	V _{DS} =10V, I _D =1mA	1:0		2.0	V
yfs	V _{DS} =10V, I _D =2A	and 2.5	4		S
R _{DS(on)} 1	I _D =2A, V _{GS} =10V	A.	0.3	0.4	Ω
R _{DS(on)} 2	I _D =2A, V _{GS} =4V		0.4	0.55	Ω
Ciss	V _{DS} =20V, f=1MHz		380		pF
Coss	V _{DS} =20V, f=1MHz		80		pF
Crss	V _{DS} =20V f=1MHz		15		pF
^t d(on)	See specified Test Circuit		10		ns
tr	See specified Test Circuit		13		ns
td(off)	See specified Test Circuit		70		ns
t _f	See specified Test Circuit		30		ns
VSD	IS=4A, VGS=0		1.0	1.5	V
	Symbol V(BR)DSS V(BR)GSS IDSS IGSS VGS(off) yfs RDS(on)1 RDS(on)2 Ciss Coss Crss td(on) tr td(off) tf	$\begin{tabular}{ c c c c c } \hline Symbol & Conditions \\ \hline V_{(BR)DSS} & I_D=1mA, V_{GS}=0 \\ \hline V_{(BR)GSS} & IG=\pm100\muA, V_{DS}=0 \\ \hline I_{DSS} & V_{DS}=100V, V_{GS}=0 \\ \hline I_{GSS} & V_{GS}=\pm12V, V_{DS}=0 \\ \hline V_{GS}(off) & V_{DS}=10V, I_D=1mA \\ \hline I_{y}fs & V_{DS}=10V, I_D=2A \\ \hline R_{DS}(on)1 & I_D=2A, V_{GS}=10V \\ \hline R_{DS}(on)2 & I_D=2A, V_{GS}=4V \\ \hline Ciss & V_{DS}=20V, f=1MHz \\ \hline Coss & V_{DS}=20V, f=1MHz \\ \hline Crss & V_{DS}=20V, f=1MHz \\ \hline t_d(on) & See specified Test Circuit \\ \hline t_r & See specified Test Circuit \\ \hline t_d(off) & See specified Test Circuit \\ \hline t_f & See$	$\begin{tabular}{ c c c c c } \hline Symbol & Conditions & min \\ \hline V(BR)DSS & I_D=1mA, V_GS=0 & 100 \\ \hline V(BR)GSS & IG=\pm100\muA, V_DS=0 & \pm15 \\ \hline I_DSS & V_DS=100V, V_GS=0 & 100 \\ \hline I_GSS & V_GS=\pm12V, V_DS=0 & 100 \\ \hline V_GS(off) & V_DS=10V, I_D=1mA & 100 \\ \hline I_Jfs & V_DS=10V, I_D=2A & 2.5 \\ \hline R_DS(on)1 & I_D=2A, V_GS=10V & 100 \\ \hline R_DS(on)2 & I_D=2A, V_GS=4V & 100 \\ \hline Ciss & V_DS=20V, f=1MHz & 100 \\ \hline Crss & V_DS=20V, f=1MHz & 100 \\ \hline Crss & V_DS=20V, f=1MHz & 100 \\ \hline I_d(on) & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circuit & 100 \\ \hline I_T & See specified Test Circ$	$\begin{tabular}{ c c c c c c } \hline Symbol & Conditions & Ratings \\ \hline Min & typ \\ \hline V(BR)DSS & I_D=1mA, V_{GS}=0 & foo \\ \hline V(BR)GSS & IG=\pm100\muA, V_{DS}=0 & \pm15 \\ \hline I_{DSS} & V_{DS}=100V, V_{GS}=0 & \\ \hline I_{GSS} & V_{GS}=\pm12V, V_{DS}=0 & \\ \hline V_{GS(off)} & V_{DS}=10V, I_D=1mA & 1/0 & \\ \hline I_{J}fs & V_{DS}=10V, I_D=2A & 2.5 & 4 \\ \hline R_{DS}(on)1 & I_D=2A, V_{GS}=4V & 0.3 \\ \hline R_{DS}(on)2 & I_D=2A, V_{GS}=4V & 0.4 \\ \hline Ciss & V_{DS}=20V, f=1MHz & 380 \\ \hline Coss & V_{DS}=20V, f=1MHz & 15 \\ \hline t_d(on) & See specified Test Circuit & 10 \\ \hline t_r & See specified Test Circuit & 70 \\ \hline t_f & See specified Test Circuit & 30 \\ \hline \end{tabular}$	$\begin{tabular}{ c c c c c } \hline Symbol & Conditions & Ratings \\ \hline Min & typ & max \\ \hline V(BR)DSS & ID=1mA, V_{GS}=0 & 100 & \\ \hline V(BR)GSS & IG=\pm100\muA, V_{DS}=0 & \pm15 & \\ \hline IDSS & V_{DS}=100V, V_{GS}=0 & \pm15 & \\ \hline IDSS & V_{DS}=10V, I_{D}=0 & \pm10 & \\ \hline V_{GS}(off) & V_{DS}=10V, I_{D}=1mA & 1/0 & 2.0 & \\ \hline V_{SS}(off) & V_{DS}=10V, I_{D}=2A & 2.5 & 4 & \\ \hline R_{DS}(on)1 & ID=2A, V_{GS}=10V & 0.3 & 0.4 & \\ \hline R_{DS}(on)2 & ID=2A, V_{GS}=4V & 0.4 & 0.55 & \\ \hline Ciss & V_{DS}=20V, f=1MHz & 380 & \\ \hline Crss & V_{DS}=20V, f=1MHz & 15 & \\ \hline t_{d}(on) & See specified Test Circuit & 10 & \\ \hline t_{f} & See specified Test Circuit & 70 & \\ \hline t_{f} & See specified Test Circuit & 30 & \\ \hline \end{tabular}$

Switching Time Test Circuit



2SK1474





change without notice