



# 2SK1840

## Analog Switch Applications

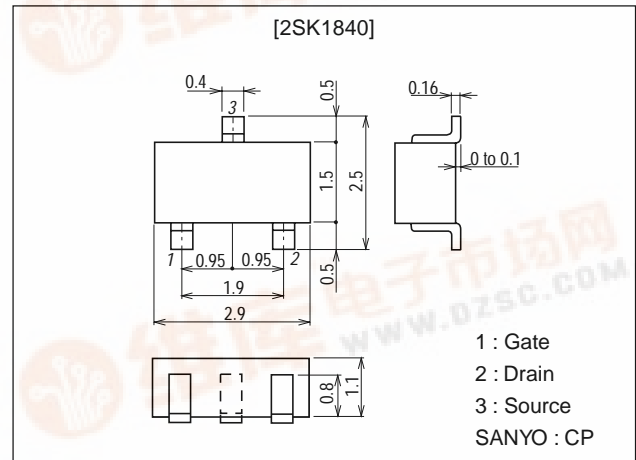
### Features

- Large  $|y_{fs}|$ .
- Enhancement type.
- Low ON resistance.

### Package Dimensions

unit:mm

2024B



### Specifications

#### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	$V_{DSS}$		30	V
Gate-to-Source Voltage	$V_{GSS}$		±12	V
Drain Current (DC)	$I_D$		100	mA
Drain Current (pulse)	$I_{DP}$		300	mA
Allowable Power Dissipation	$P_D$		200	mW
Channel Temperature	$T_{ch}$		125	°C
Storage Temperature	$T_{stg}$		-55 to +125	°C

#### Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=10\mu A, V_{GS}=0$	30			V
Zero-Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=15V, V_{GS}=0$			1	$\mu A$
Gate-to-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 10V, V_{DS}=0$		0.01	±10	nA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=10V, I_D=100\mu A$	0.3	0.9	1.5	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=10V, I_D=50mA, f=1kHz$	25	50		mS
Static Drain-to-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=10mA$		15	25	$\Omega$
Input Capacitance	$C_{iss}$	$V_{DS}=10V, V_{GS}=0, f=1MHz$		12		pF
Output Capacitance	$C_{oss}$	$V_{DS}=10V, V_{GS}=0, f=1MHz$		4		pF
Reverse Transfer Capacitance	$C_{rss}$	$V_{DS}=10V, V_{GS}=0, f=1MHz$		0.4		pF

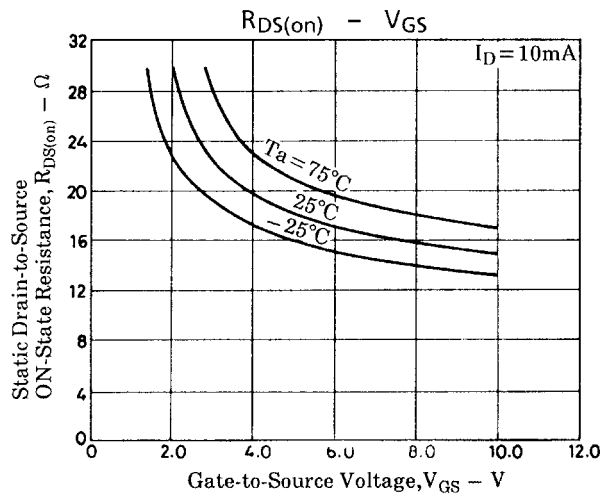
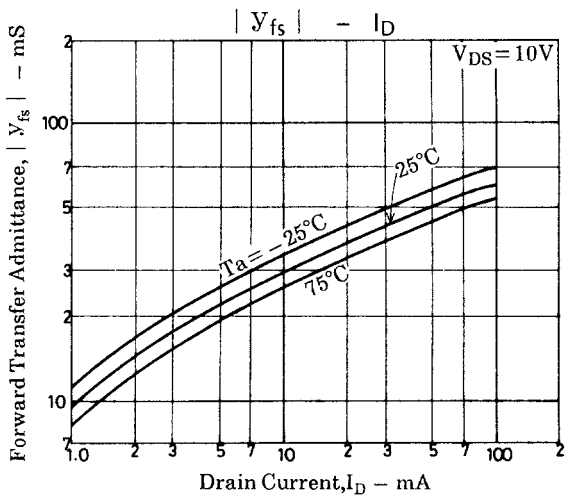
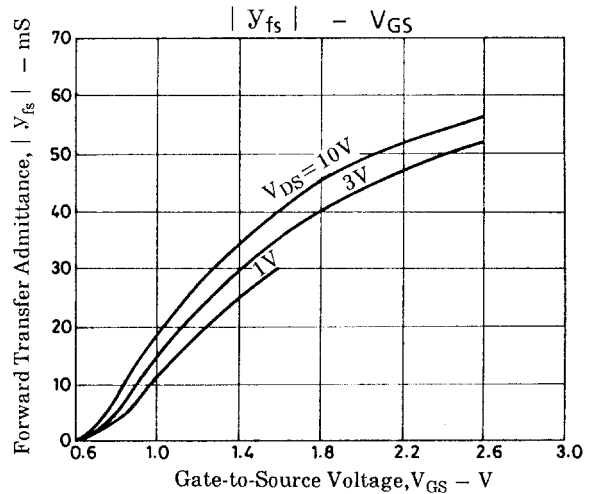
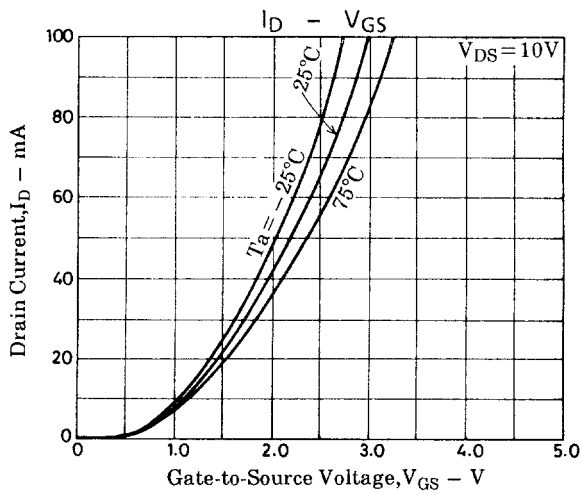
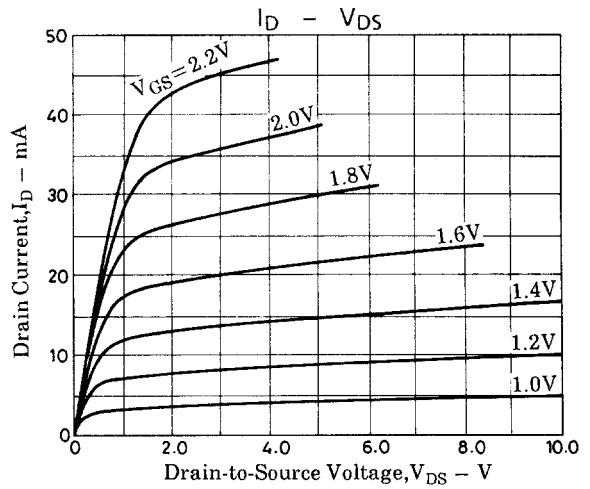
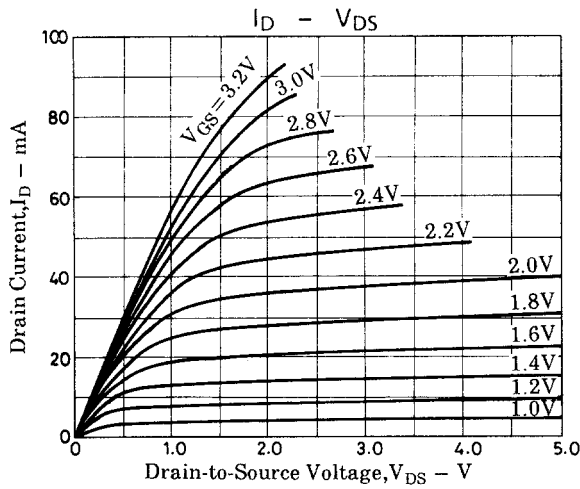
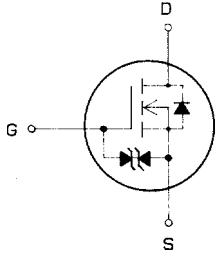
Marking : JJ

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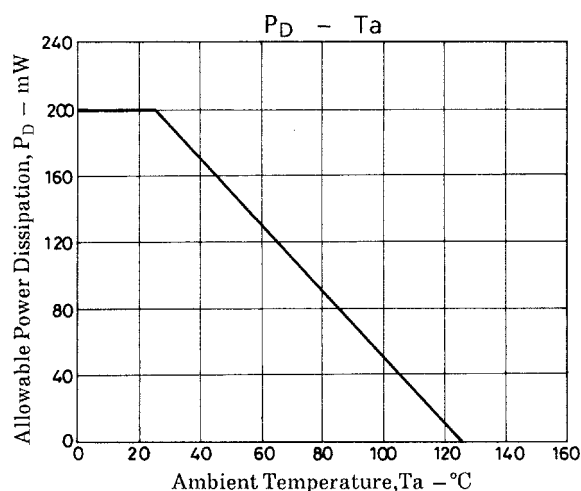
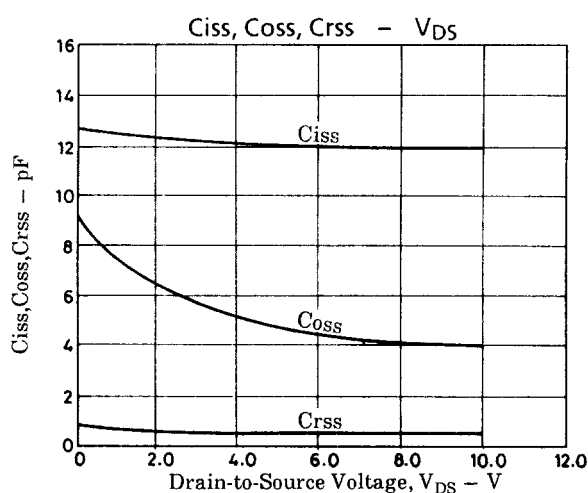
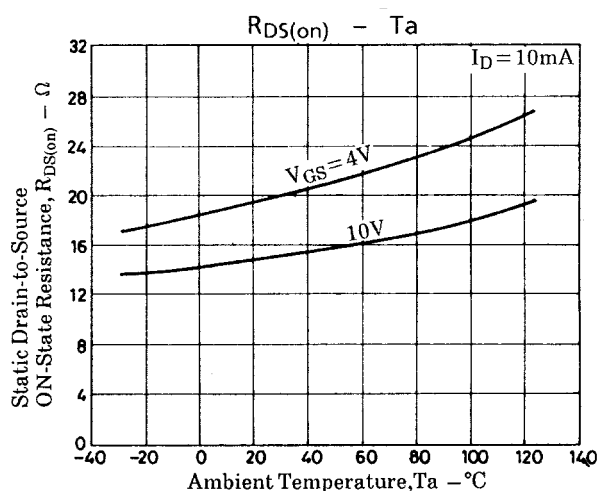


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## Electrical Connection



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