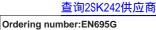
捷多邦,专业PCB打样工厂,24小时加急出货



N-Channel Junction Silicon FET

2SK242 **Low-Frequency General-Purpose Amplifier Applications**

Features

- · Ultrasmall-sized package permitting 2SK242-applied sets to be made small and slim.
- · Small Crss (Crss=0.04pF typ).

Package Dimensions

unit:mm 2024B

[2SK242] 0.16 <u>0 to 0.1</u> <u>1</u> 2.5 19 2.9 1 : Gate 2 : Drain 3 : Source SANYO : CP

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Gate-to-Drain Voltage	V _{GDO}		-20	V
Gate Current	۱ _G		10	mA
Drain Current	۱ _D		20	mA
Allowable Power Dissipation	PD	and the	150	mW
Junction Temperature	Tj	1 4 T 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	125	°C
Storage Temperature	Tstg		-55 to +125	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Gate-to-Drain Breakdown Voltage	V(BR)GDO	I _G =-10μΑ	-20			V
Gate-to-Source Leakage Current	IGSS	V _{GS} =-0.5V, V _{DS} =0			-10	nA
Zero-Gate Voltage Drain Current	IDSS*	V _{DS} =5V, V _{GS} =0	0.6*		12.0*	mA
Cutoff Voltage	VGS(off)	V _{DS} =5V, I _D =10μA			-2.5	V
Forward Transfer Admittance	yfs 1	V _{DS} =5V, V _{GS} =0, f=1kHz	2.0	6.0		mS
	yfs 2	V _{DS} =5V, V _{GS} =0, f=100MHz	2.0	6.0	C.	mS
Input Capacitance	Ciss	V _{DS} =5V, V _{GS} =0, f=1MHz		4.0	1.00	pF
Output Capacitance	Coss	V _{DS} =5V, V _{GS} =0, f=1MHz	11.44	4.0		pF
Reverse Transfer Capacitance	Crss	V _{DS} =5V, V _{GS} =0, f=1MHz		0.04	0.15	pF
* : The 2SK242 is classified by I _{DSS} as follows : (unit : mA).				Contin	ued on ne	ext page

* : The 2SK242 is classified by I_{DSS} as follows : (unit : mA).

6.0 5.0 0.6 2 1.5 1.2 3 3.0 2.5 4 12.0 5

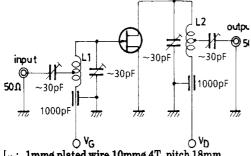
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> SANYO assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO products described or contained herein.

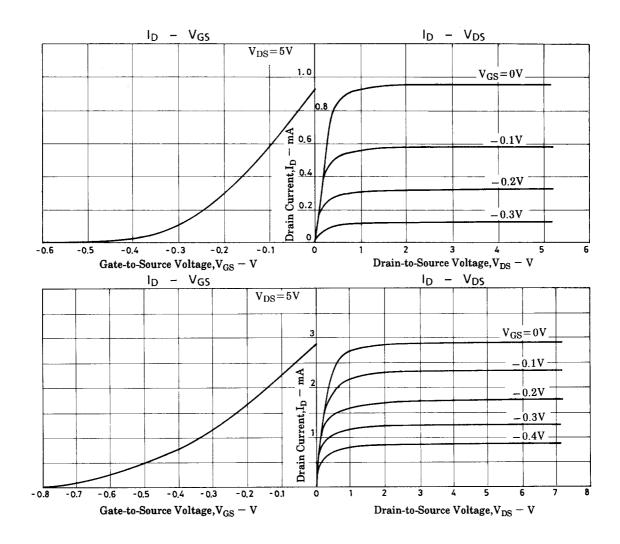
Continued from preceding page.

Parameter	Symbol	Conditions	Ratings		Unit	
Power Gain	PG	V _{DS} =5V, V _{GS} =0, f=100MHz, See specified Test Circuit		24		dB
Noise Figure	NF	See specified Test Circuit		3.5	6.0	dB

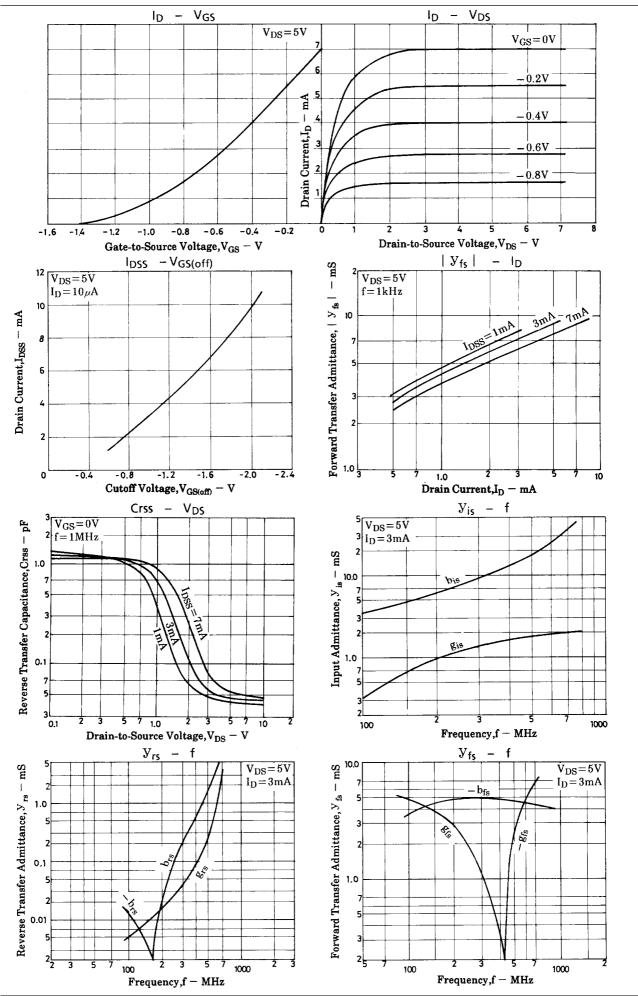
PG, NF Specified Test Circuit



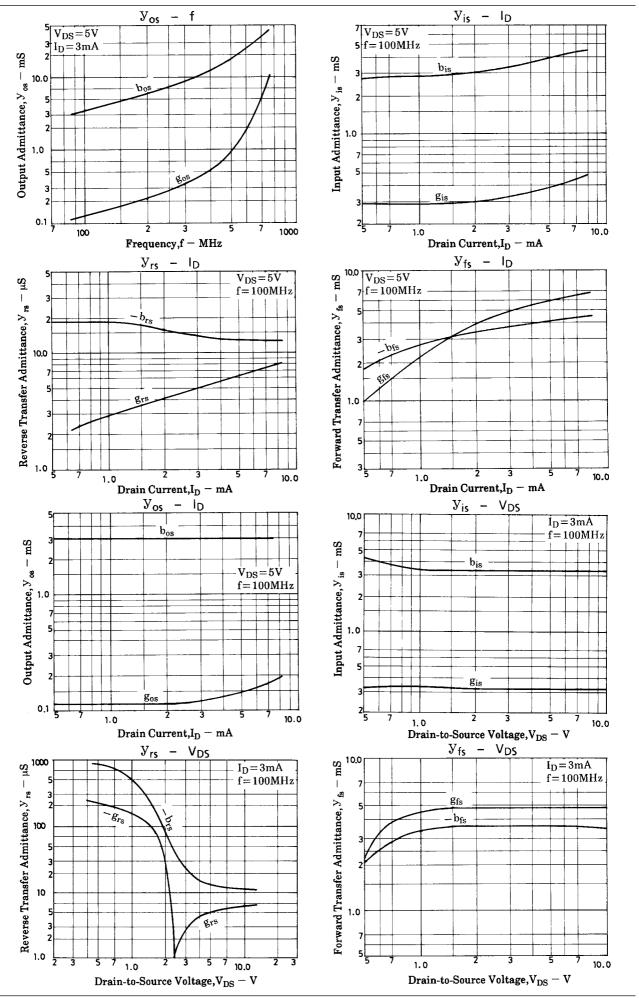
V_G V_D L₁: 1mmø plated wire 10mmø 4T, pitch 18mm, tap:1T from gate side L₂: 1mmø plated wire 10mmø 6T, pitch 10mm, tap:1T from drain side

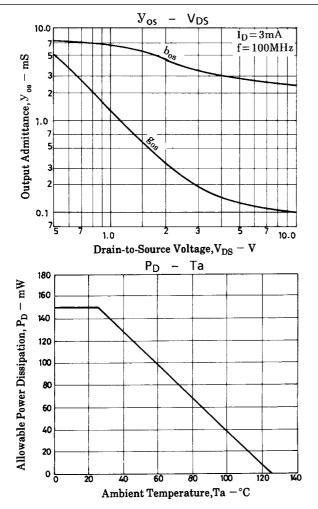


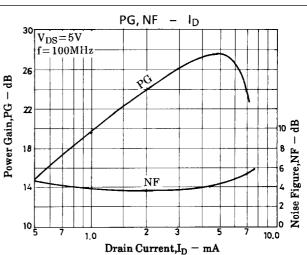
2SK242



2SK242







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