TSM1011

CONSTANT VOLTAGE AND CONSTANT CURRENT CONTROLLER FOR BATTERY CHARGERS AND ADAPTORS

- CONSTANT VOLTAGE AND CONSTANT CURRENT CONTROL
- LOW VOLTAGE OPERATION
- LOW EXTERNAL COMPONENT COUNT
- CURRENT SINK OUTPUT STAGE
- EASY COMPENSATION

VOLTAGE REFERENCE

- FIXED OUTPUT VOLTAGE REFERENCE 2.545V
- 0.5% AND 1% VOLTAGE PRECISION

DESCRIPTION

TSM1011 is a highly integrated solution for SMPS applications requiring CV (constant voltage) and CC (constant current) mode.

TSM1011 integrates one voltage reference and two operational amplifiers (with ORed outputs - common collectors).

The voltage reference combined with one operational amplifier makes it an ideal voltage controller. The other operational, combined with few external resistors and the voltage reference, can be used as a current limiter.

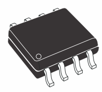
APPLICATIONS

- **ADAPTERS**
- **BATTERY CHARGERS**

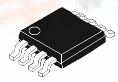
ORDER CODE

Part Number	Temperature Range	Package		Marking
		S	D	
TSM1011I	0 to 105°C		•	M1011
TSM1011AI	0 to 105°C		•	M1011A
TSM1011I	0 to 105°C	•		M802
TSM1011AI	0 to 105°C	•		M803

D = Small Outline Package (SO) - also available in Tape & Reel (DT
S = Small Outline Package (MiniSO8) - also available in Tape & Reel (ST)

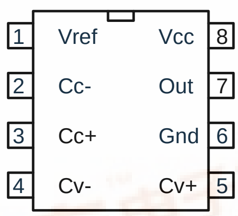


D
SO8
(Plastic Package)



D
MiniSO8
(Plastic Micropackage)

PIN CONNECTIONS (top view)



PIN DESCRIPTION

SO8 & MiniSO8 Pinout

Name	Pin #	Type	Function
Vref	1	Analog Output	Voltage Reference
Cc-	2	Analog Input	Input pin of the operationnal amplifier
Cc+	3	Analog Input	Input pin of the operationnal amplifier
Cv-	4	Analog Input	Input pin of the operationnal amplifier
Cv+	5	Analog Input	Input pin of the operationnal amplifier
Gnd	6	Power Supply	Ground Line. 0V Reference For All Voltages
Out	7	Analog Output	Output of the two operational amplifier
Vcc	8	Power Supply	Power supply line.

ABSOLUTE MAXIMUM RATINGS

Symbol	DC Supply Voltage	Value	Unit
Vcc	DC Supply Voltage (50mA =< Icc)	-0.3V to Vz	V
Vi	Input Voltage	-0.3 to Vcc	V
PT	Power dissipation		W
Toper	Operational temperature	0 to 105	°C
Tstg	Storage temperature	-55 to 150	°C
Tj	Junction temperature	150	°C
Iref	Voltage reference output current	10	mA
ESD	Electrostatic Discharge	2	KV
Rthja	Thermal Resistance Junction to Ambient Mini SO8 package	180	°C/W
Rthja	Thermal Resistance Junction to Ambient SO8 package	175	°C/W

OPERATING CONDITIONS

Symbol	Parameter	Value	Unit
Vcc	DC Supply Conditions	4.5 to Vz	V

ELECTRICAL CHARACTERISTICS

Tamb = 25°C and Vcc = +18V (unless otherwise specified)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
Total Current Consumption						
Icc	Total Supply Current, excluding current in Voltage Reference.	Vcc = 18V, no load Tmin. < Tamb < Tmax.			1	mA
Vz	Vcc clamp voltage	Icc = 50mA		28		V
Operators						
V_{io}	Input Offset Voltage TSM1011 TSM1011A	T_{amb} = 25°C $T_{min.} \leq T_{amb} \leq T_{max.}$ T_{amb} = 25°C $T_{min.} \leq T_{amb} \leq T_{max.}$		1 0.5	4 5 2 3	mV
DV_{io}	Input Offset Voltage Drift			7		µV/°C
I_{io}	Input Offset Current	T_{amb} = 25°C $T_{min.} \leq T_{amb} \leq T_{max.}$		2	30 50	nA
I_{ib}	Input Bias Current	T_{amb} = 25°C $T_{min.} \leq T_{amb} \leq T_{max.}$		20 50	150 200	nA
SVR	Supply Voltage Rejection Ratio	V_{CC} = 4.5V to 28V	65	100		dB
Vicm	Input Common Mode Voltage Range for CV op-amp		1.5		Vcc-1.5	V
Vicm	Input Common Mode Voltage Range for CC op-amp		0		Vcc-1.5	V
CMR	Common Mode Rejection Ratio	T_{amb} = 25°C $T_{min.} \leq T_{amb} \leq T_{max.}$	70 60	85		dB
Output stage						
Gm	Transconduction Gain. Sink Current Only[1)]	T_{amb} = 25°C $T_{min.} \leq T_{amb} \leq T_{max.}$	1	3.5 2.5		mA/mV
Vol	Low level output voltage at 10 mA sinking current			200	600	mV
Ios	Output Short Circuit Current. Output to Vcc. Sink Current Only	T_{amb} = 25°C $T_{min.} \leq T_{amb} \leq T_{max.}$		27	50	mA
Voltage reference						
V_{ref}	Reference Input Voltage, Iload=1mA TSM1011 1% precision TSM1011A 0.5% precision	T_{amb} = 25°C	 2.519 2.532	 2.545 2.545	 2.57 2.557	V
ΔV_{ref}	Reference Input Voltage Deviation Over Temperature Range	$T_{min.} \leq T_{amb} \leq T_{max.}$		20	30	mV
RegLine	Reference input voltage deviation over Vcc range.	Iload = 5mA			20	mV
RegLoad	Reference input voltage deviation over output current.	Vcc = 18V, 0 < Iload < 10mA			10	mV

1. The current depends on the difference voltage beween the negative and the positive inputs of the amplifier. If the voltage on the minus input is 1mV higher than the positive amplifier, the sinking current at the output OUT will be increased by 3.5mA.

Figure 1 : Internal Schematic

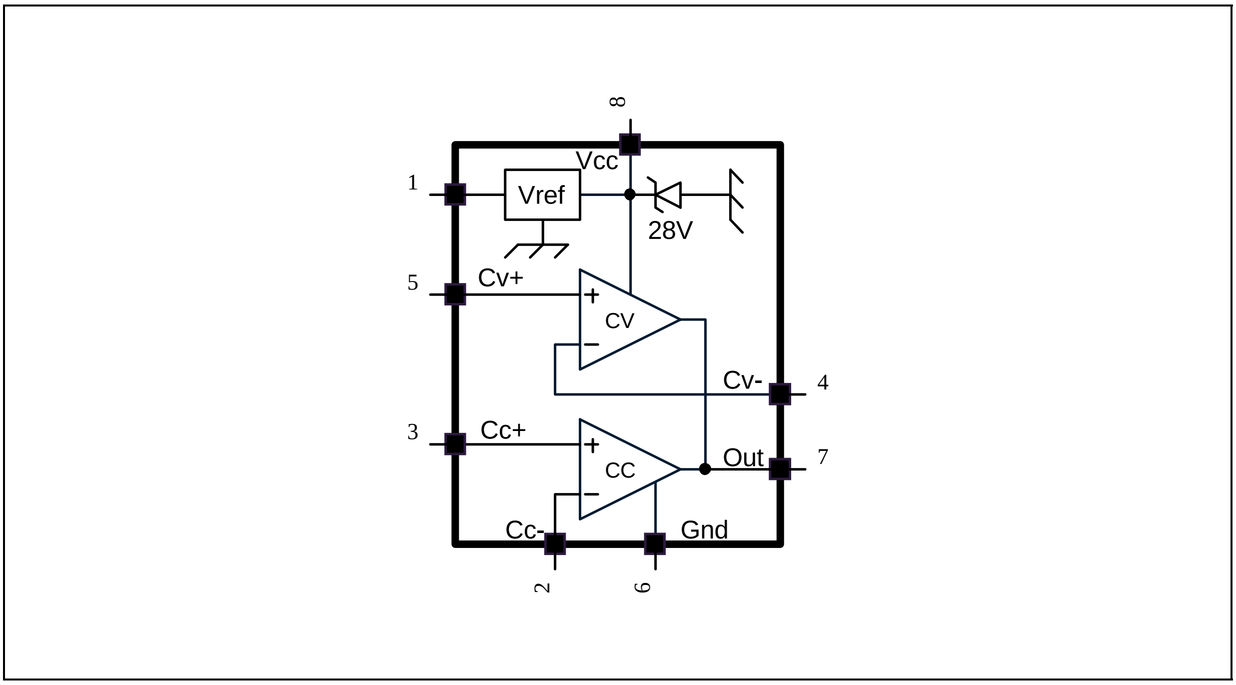
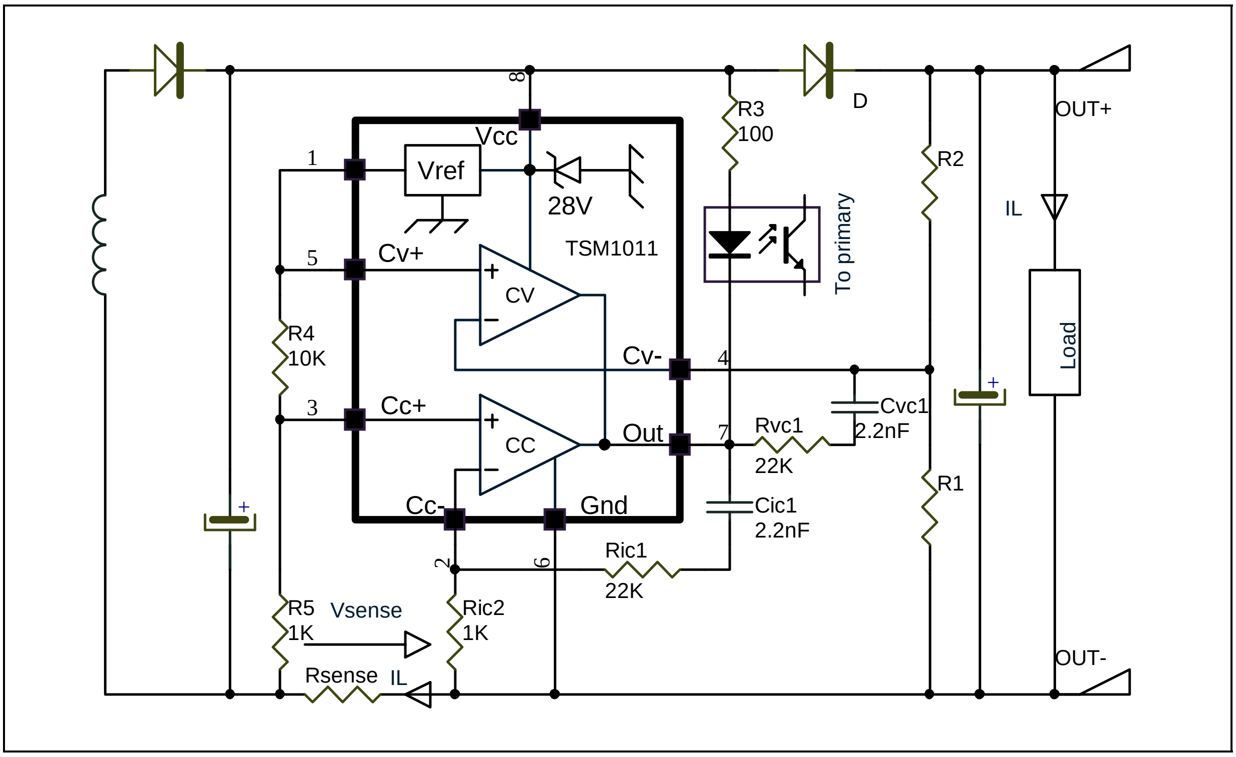


Figure 2 : Typical Adapter Application Using TSM1011



In the above application schematic, the TSM1011 is used on the secondary side of a flyback adapter (or battery charger) to provide an accurate control of voltage and current. The above feedback loop is made with an optocoupler.

PRINCIPLE OF OPERATION AND APPLICATION HINTS

1. Voltage and Current Control

1.1. Voltage Control

The voltage loop is controlled via a first transconductance operational amplifier, the resistor bridge R1, R2, and the optocoupler which is directly connected to the output.

The relation between the values of R1 and R2 should be chosen as writen in Equation 1.

R1 = R2 x Vref / (Vout - Vref) Eq1

Where Vout is the desired output voltage.

To avoid the discharge of the load, the resistor bridge R1, R2 should be highly resistive. For this type of application, a total value of 100KΩ (or more) would be appropriate for the resistors R1 and R2.

As an example, with R2 = 100KΩ, Vout = 4.10V, Vref = 2.5V, then R1 = 41.9KΩ.

Note that if the low drop diode should be inserted between the load and the voltage regulation resistor bridge to avoid current flowing from the load through the resistor bridge, this drop should be taken into account in the above calculations by replacing Vout by (Vout + Vdrop).

1.2. Current Control

The current loop is controlled via the second trans-conductance operational amplifier, the sense resistor Rsense, and the optocoupler.

Vsense threshold is achieved externally by a resistor bridge tied to the Vref voltage reference. Its middle point is tied to the positive input of the current control operational amplifier, and its foot is to be connected to lower potential point of the sense resistor as shown on the following figure. The resistors of this bridge are matched to provide the best precision possible

The control equation verifies:

Rsense x Ilim = Vsense eq2

Vsense = R5*Vref/(R4+R5)

Ilim = R5*Vref/(R4+R5)*Rsense eq2’

where Ilim is the desired limited current, and Vsense is the threshold voltage for the current control loop.

Note that the Rsense resistor should be chosen taking into account the maximum dissipation (Plim) through it during full load operation.

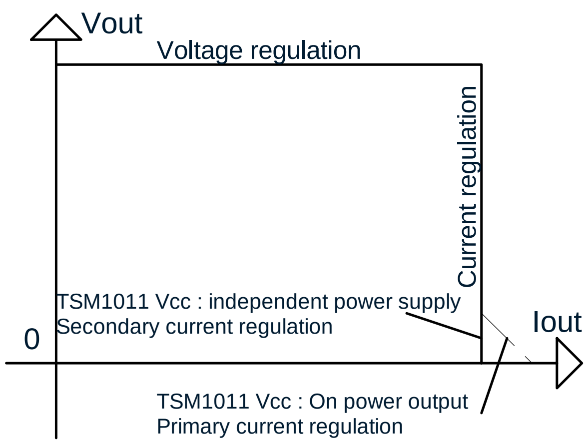
Plim = Vsense x Ilim. eq3

Therefore, for most adapter and battery charger applications, a quarter-watt, or half-watt resistor to make the current sensing function is sufficient.

The current sinking outputs of the two trans-connuctance operational amplifiers are common (to the output of the IC). This makes an ORing function which ensures that whenever the current or the voltage reaches too high values, the optocoupler is activated.

The relation between the controlled current and the controlled output voltage can be described with a square characteristic as shown in the following V/I output-power graph.

Figure 3 : Output voltage versus output current



2. Compensation

The voltage-control trans-conductance operational amplifier can be fully compensated. Both of its output and negative input are directly accessible for external compensation components.

An example of a suitable compensation network is shown in Fig.2. It consists of a capacitor Cvc1=2.2nF and a resistor Rcv1=22KΩ in series.

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3. Start Up and Short Circuit Conditions

Under start-up or short-circuit conditions the TSM1011 is not provided with a high enough supply voltage. This is due to the fact that the chip has its power supply line in common with the power supply line of the system.

Therefore, the current limitation can only be ensured by the primary PWM module, which should be chosen accordingly.

If the primary current limitation is considered not to be precise enough for the application, then a sufficient supply for the TSM1011 has to be ensured under any condition. It would then be necessary to add some circuitry to supply the chip with a separate power line. This can be achieved in numerous ways, including an additional winding on the transformer.

4. Voltage clamp

The following schematic shows how to realise a low-cost power supply for the TSM1011 (with no additional windings).Please pay attention to the fact that in the particular case presented here, this low-cost power supply can reach voltages as high as twice the voltage of the regulated line. Since the Absolute Maximum Rating of the TSM1011 supply voltage is 28V. In the aim to protect he TSM1011 against such how voltage values a internal zener clamp is integrated.

Rlimit = (Vcc-Vz)Ivz

Figure 4 : Clamp voltage

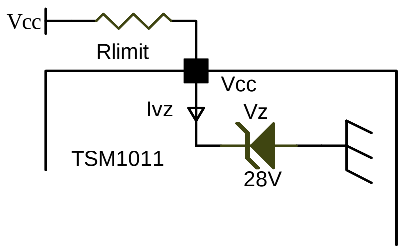
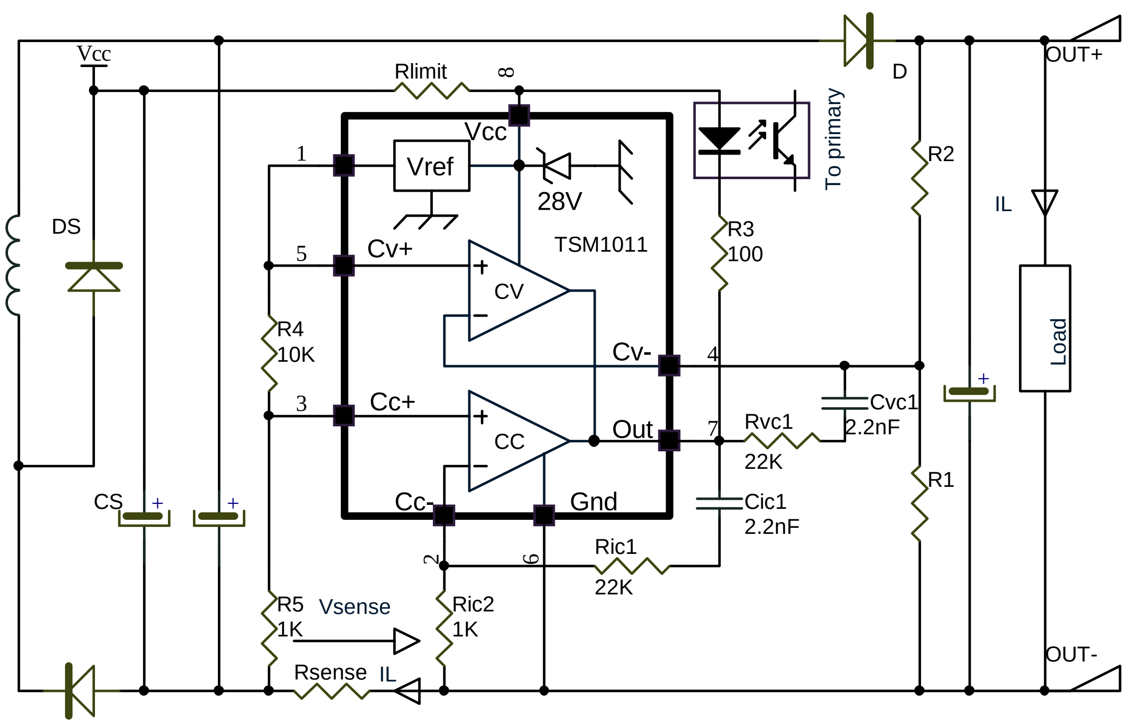
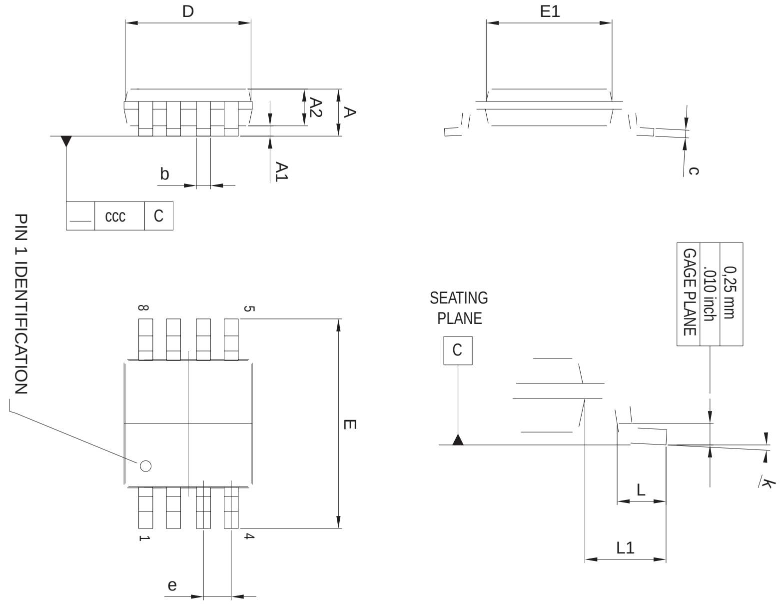


Figure 5 :



PACKAGE MECHANICAL DATA

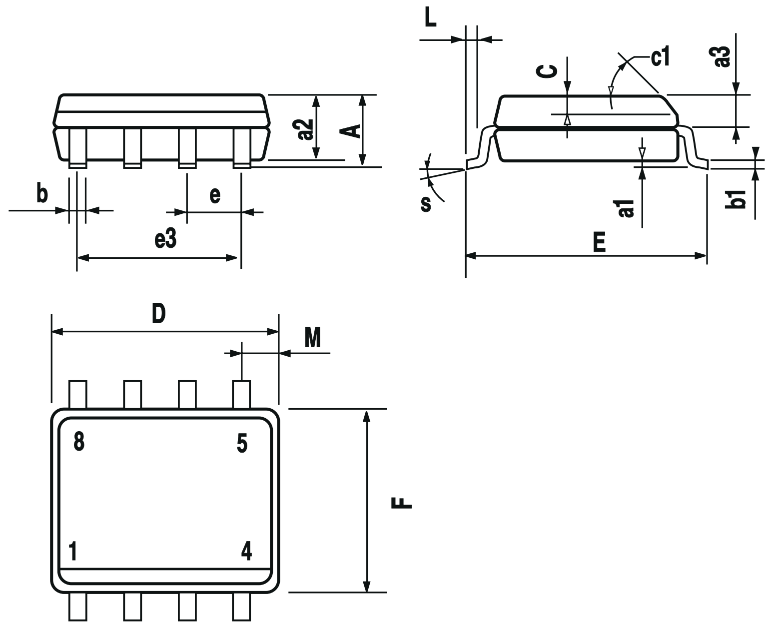
8 PINS - PLASTIC MICROPACKAGE (MiniSO8)



Dimensions	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			1.100			0.043
A1	0.050	0.100	0.150	0.002	0.004	0.006
A2	0.780	0.860	0.940	0.031	0.034	0.037
b	0.250	0.330	0.400	0.010	0.013	0.016
c	0.130	0.180	0.230	0.005	0.007	0.009
D	2.900	3.000	3.100	0.114	0.118	0.122
E	4.750	4.900	5.050	0.187	0.193	0.199
E1	2.900	3.000	3.100	0.114	0.118	0.122
e		0.650			0.026	
L	0.400	0.550	0.700	0.016	0.022	0.028
L1		0.950			0.037	
k	0d	3d	6d	0d	3d	6d
aaa			0.100			0.004

PACKAGE MECHANICAL DATA

8 PINS - PLASTIC MICROPACKAGE (SO8)



Dimensions	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			1.75			0.069
a1	0.1		0.25	0.004		0.010
a2			1.65			0.065
a3	0.65		0.85	0.026		0.033
b	0.35		0.48	0.014		0.019
b1	0.19		0.25	0.007		0.010
C	0.25		0.5	0.010		0.020
c1	45° (typ.)					
D	4.8		5.0	0.189		0.197
E	5.8		6.2	0.228		0.244
e		1.27			0.050	
e3		3.81			0.150	
F	3.8		4.0	0.150		0.157
L	0.4		1.27	0.016		0.050
M			0.6			0.024
S	8° (max.)					