ST13005N

HIGH VOLTAGE FAST-SWITCHING NPN POWER TRANSISTOR

- MEDIUM VOLTAGE CAPABILITY
- NPN TRANSISTOR
- LOW SPREAD OF DYNAMIC PARAMETERS
- MINIMUM LOT-TO-LOT SPREAD FOR RELIABLE OPERATION
- VERY HIGH SWITCHING SPEED

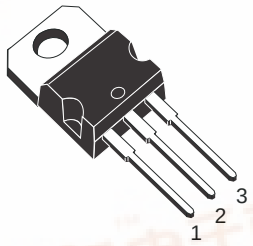
APPLICATIONS:

- COMPACT FLUORESCENT LAMP (CFL)
- ELECTRONIC BALLASTS FOR FLUORESCENT LIGHTING
- SWITCH MODE POWER SUPPLIES

DESCRIPTION

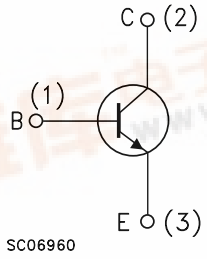
The device is manufactured using high voltage Multi Epitaxial Planar technology for high switching speeds and medium voltage capability.

It uses a Cellular Emitter structure with planar edge termination to enhance switching speeds while maintaining a satisfactory RBSOA.



TO-220

INTERNAL SCHEMATIC DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_{CES}	Collector-Emitter Voltage (V_{BE} = 0)	700	V
V_{CEO}	Collector-Emitter Voltage (I_B = 0)	400	V
V_{EBO}	Emitter-Base Voltage (I_C = 0)	9	V
I_C	Collector Current	4	A
I_{CM}	Collector Peak Current (t_p < 5 ms)	8	A
I_B	Base Current	2	A
I_{BM}	Base Peak Current (t_p < 5 ms)	4	A
P_{tot}	Total Dissipation at T_c = 25 °C	60	W
T_{stg}	Storage Temperature	-65 to 150	°C
T_j	Max. Operating Junction Temperature	150	°C

THERMAL DATA

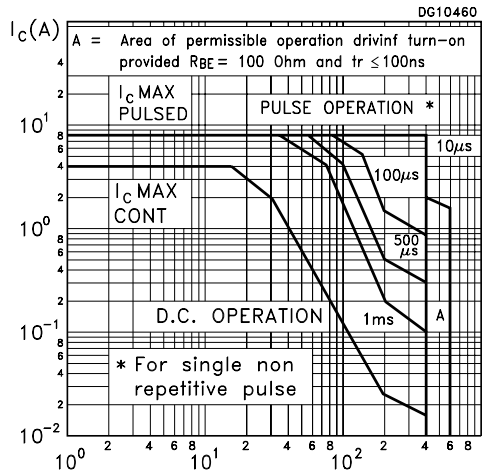
$R_{thj\text{-}case}$	Thermal Resistance Junction-case	Max	2.08	$^oC/W$

ELECTRICAL CHARACTERISTICS (T_{case} = 25 ^oC unless otherwise specified)

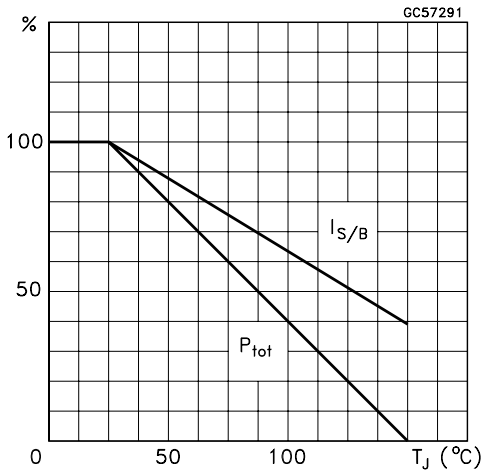
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{CEV}	Collector Cut-off Current (V_{BE} = -1.5V)	V_{CE} = 700V V_{CE} = 700V T_{case} = 100^oC			1 5	mA mA
I_{EBO}	Emitter Cut-off Current (I_C = 0)	V_{EB} = 9 V			1	mA
$V_{CEO(sus)}$*	Collector-Emitter Sustaining Voltage (I_B = 0)	I_C = 10 mA	400			V
$V_{CE(sat)}$*	Collector-Emitter Saturation Voltage	I_C = 1 A I_B = 0.2 A I_C = 2 A I_B = 0.5 A I_C = 3 A I_B = 0.75 A			0.5 0.6 5	V V V
$V_{BE(sat)}$*	Base-Emitter Saturation Voltage	I_C = 1 A I_B = 0.2 A I_C = 2 A I_B = 0.5 A			1.2 1.6	V V
h_{FE}*	DC Current Gain	I_C = 1 A V_{CE} = 5 V I_C = 2 A V_{CE} = 5 V	10 8		30 24	
t_s t_f	RESISTIVE LOAD Storage Time Fall Time	I_C = 2 A I_{B1} = -I_{B2} = 0.4 A V_{CC} = 125 V T_p = 30 µs		1.65 260		µs ns

∗ Pulsed: Pulse duration = 300µs, duty cycle = 1.5 %.

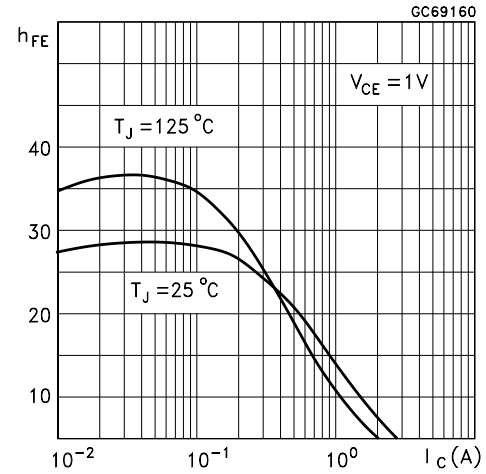
Safe Operating Areas



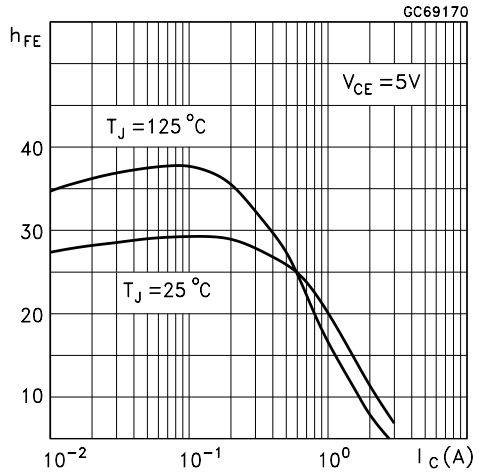
Derating Curve



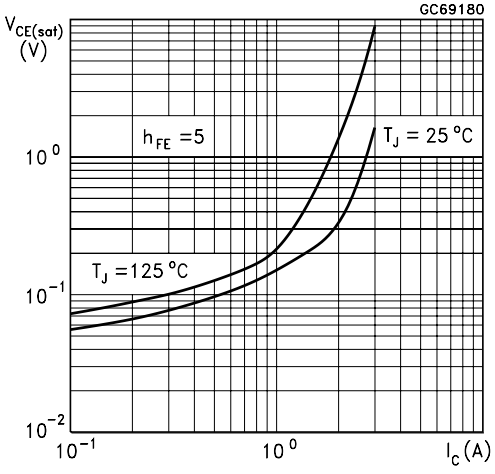
DC Current Gain



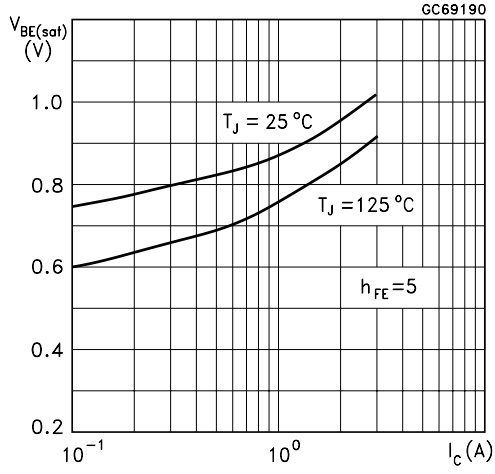
DC Current Gain



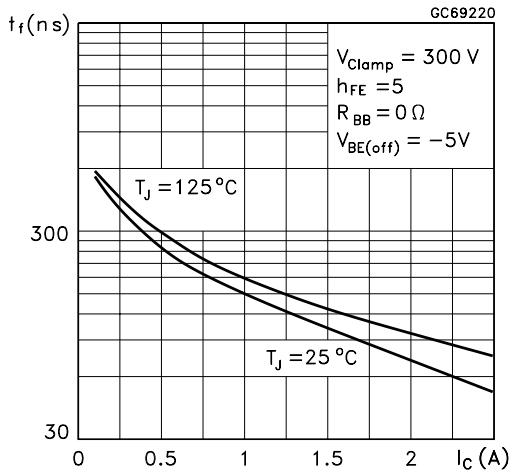
Collector Emitter Saturation Voltage



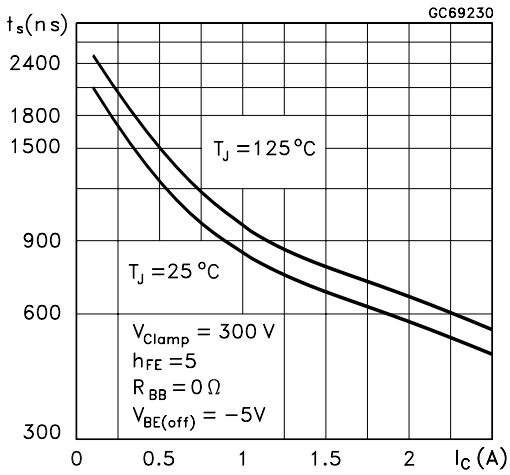
Base Emitter Saturation Voltage



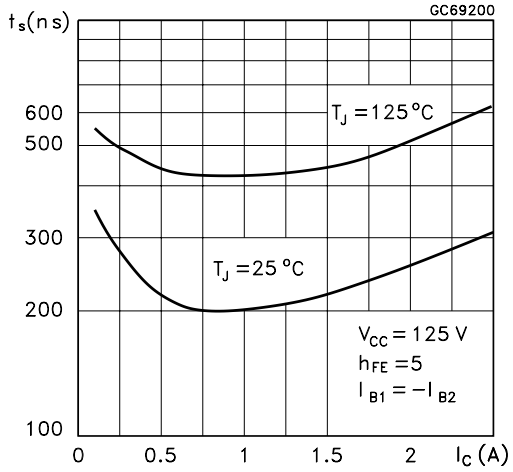
Inductive Fall Time



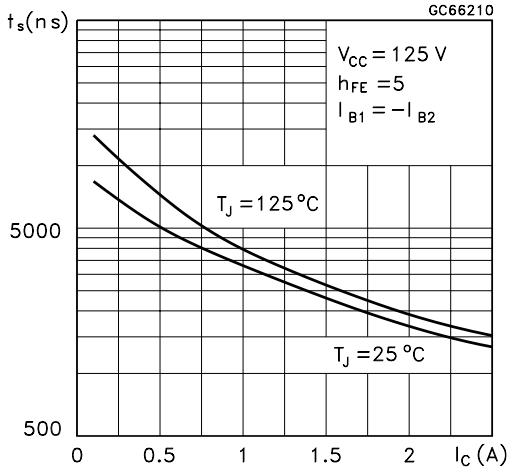
Inductive Storage Time



Resistive Fall Time



Resistive Load Storage Time



Reverse Biased SOA

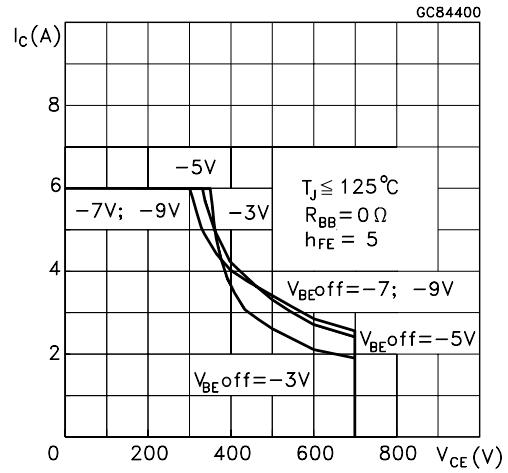
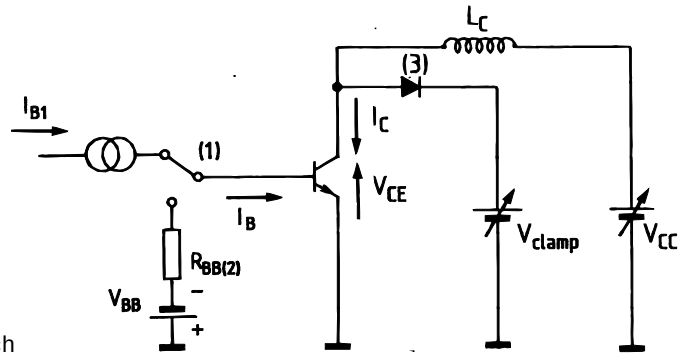
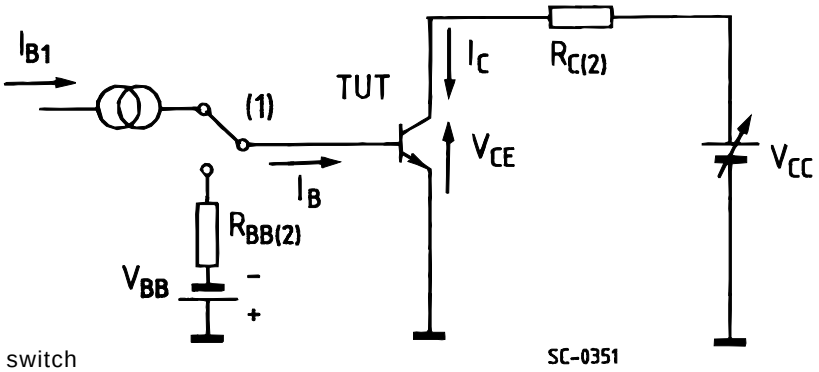


Figure 1: Inductive Load Switching Test Circuit.



1) Fast electronic switch
2) Non-inductive Resistor
3) Fast recovery rectifier

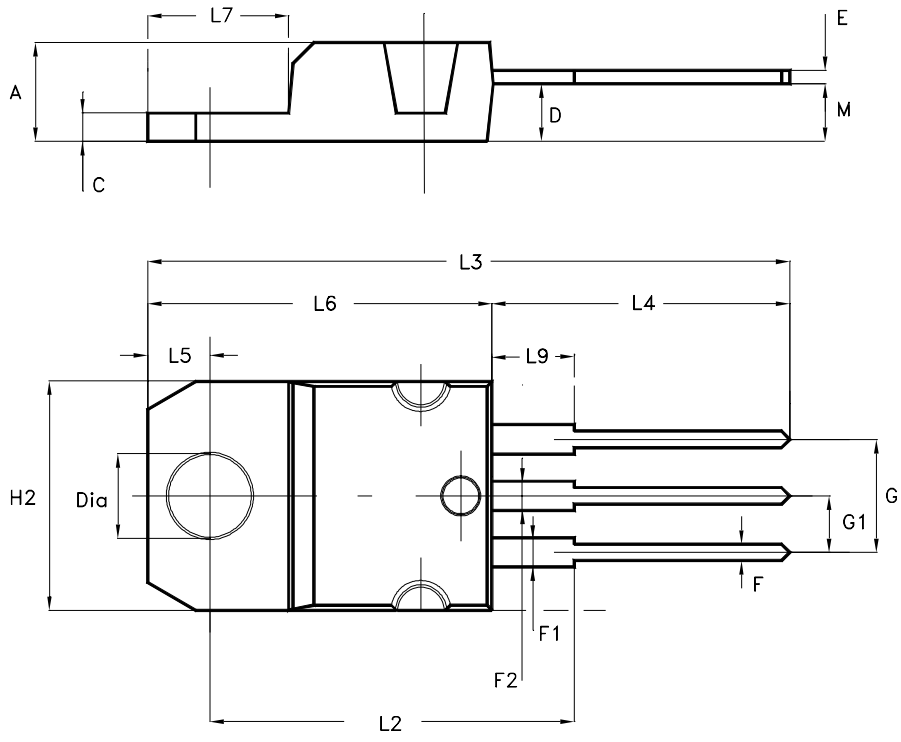
Figure 2: Resistive Load Switching Test Circuit.



1) Fast electronic switch
2) Non-inductive Resistor

TO-220 MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.40		4.60	0.173		0.181
C	1.23		1.32	0.048		0.052
D	2.40		2.72	0.094		0.107
E	0.49		0.70	0.019		0.027
F	0.61		0.88	0.024		0.034
F1	1.14		1.70	0.044		0.067
F2	1.14		1.70	0.044		0.067
G	4.95		5.15	0.194		0.202
G1	2.40		2.70	0.094		0.106
H2	10.00		10.40	0.394		0.409
L2		16.40			0.645	
L4	13.00		14.00	0.511		0.551
L5	2.65		2.95	0.104		0.116
L6	15.25		15.75	0.600		0.620
L7	6.20		6.60	0.244		0.260
L9	3.50		3.93	0.137		0.154
M		2.60			0.102	
DIA.	3.75		3.85	0.147		0.151



P011CI