BTW67 and BTW69 Series

50A SCRs

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#### **STANDARD**

#### MAIN FEATURES:

Symbol	Value	Unit
I <sub>T(RMS)</sub>	50 50	A
V <sub>DRM</sub> /V <sub>RRM</sub>	600 to 1200	V
I <sub>GT</sub>	80	mA

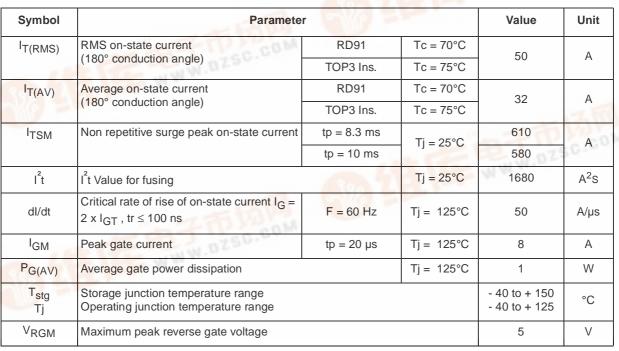
## DESCRIPTION

Available in high power BTW69 Series is suitab power handling and pow such as solid state rel high power motor control.

Based on a clip assembly technology, they offer a superior performance in surge current handling capabilities.

Thanks to their internal ceramic pad, they provide high voltage insulation (2500V RMS), complying with UL standards (file ref: E81734).

### ABSOLUTE RATINGS (limiting values)



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TOP3 7) (BTW69)

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# **BTW67 and BTW69 Series**

# ELECTRICAL CHARACTERISTICS (Tj = 25°C, unless otherwise specified)

Symbol	Test Condit	tions		Value	Unit
I <sub>GT</sub>		MIN.	8		
	$V_D = 12 V$ $R_L = 33 \Omega$		MAX.	80	— mA
$V_{GT}$		MAX.	1.3	V	
$V_{GD}$	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$	Tj = 125°C	MIN.	0.2	V
Ι <sub>Η</sub>	I <sub>T</sub> = 500 mA Gate open		MAX.	150	mA
۱L	$I_{G} = 1.2 I_{GT}$		MAX.	200	mA
dV/dt	V <sub>D</sub> = 67 % V <sub>DRM</sub> Gate open	Tj = 125°C	MIN.	1000	V/µs
$V_{TM}$	I <sub>TM</sub> = 100 A tp = 380 μs	Tj = 25°C	MAX.	1.9	V
V <sub>t0</sub>	Threshold voltage	Tj = 125°C	MAX.	1.0	V
R <sub>d</sub>	Dynamic resistance	Tj = 125°C	MAX.	8.5	mΩ
I <sub>DRM</sub>	V <sub>DRM</sub> = V <sub>RRM</sub>	Tj = 25°C	MAX.	10	μA
I <sub>RRM</sub>		Tj = 125°C		5	mA

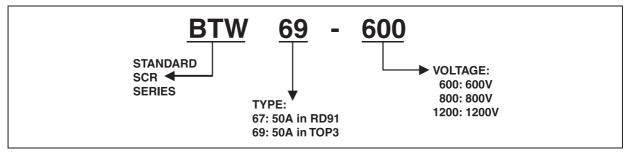
# THERMAL RESISTANCES

Symbol	Parameter		Value	Unit
R <sub>th(j-c)</sub>	Junction to case (DC)	RD91 (Insulated)	1.0	°C/W
		TOP3 Insulated	0.9	
R <sub>th(j-a)</sub>	Junction to ambient	TOP3 Insulated	50	°C/W

## PRODUCT SELECTOR

Part Number		Voltage (xxx)		Sensitivity	Package
	600 V	800 V	1200 V		. actuge
BTW67-xxx	Х	Х	Х	80 mA	RD91
BTW69-xxx	Х	Х	Х	80 mA	TOP3 Ins.

# **ORDERING INFORMATION**



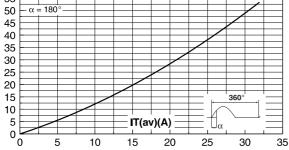
## **OTHER INFORMATION**

Part Number	Marking	Weight	Base Quantity	Packing mode
BTW67-xxx	BTW67xxx	20.0 g	25	Bulk
BTW69-xxx	BTW69xxx	4.5 g	120	Bulk

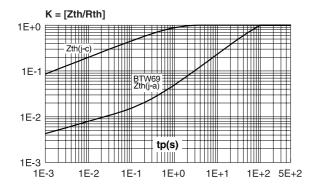
Note: xxx = voltage



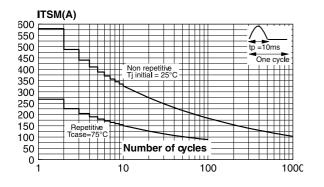
Fig. 1: Maximum average power dissipation



**Fig. 3:** Relative variation of thermal impedance versus pulse duration.

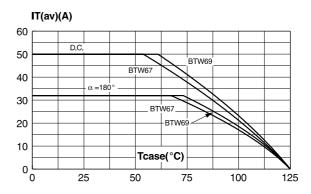


**Fig. 5:** Surge peak on-state current versus number of cycles.

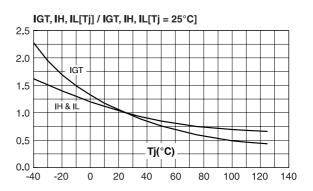


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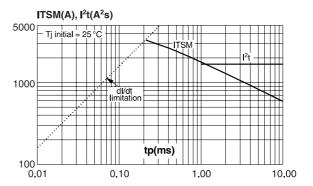
**Fig. 2:** Average and D.C. on-state current versus case temperature.



**Fig. 4:** Relative variation of gate trigger current, holding current and latching current versus junction temperature.

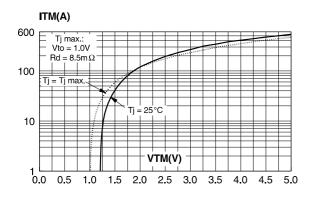


**Fig. 6:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp < 10ms, and corresponding value of  $l^2t$ .



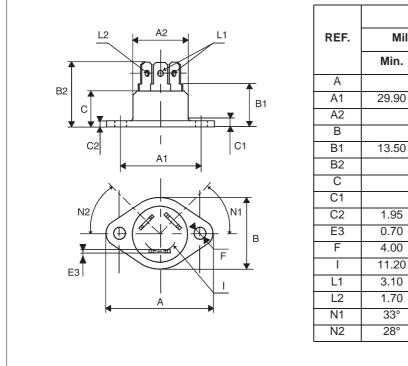
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**Fig. 7:** On-state characteristics (maximum values).



## PACKAGE MECHANICAL DATA

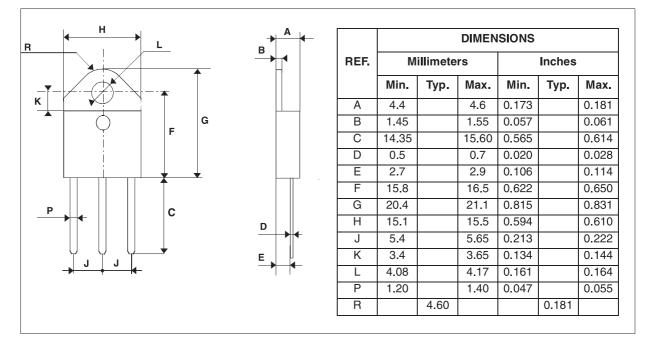
RD91 (Plastic)



	DIMENSIONS				
REF.	Millimeters		Inches		
	Min.	Max.	Min.	Max.	
A		40.00		1.575	
A1	29.90	30.30	1.177	1.193	
A2		22.00		0.867	
В		27.00		1.063	
B1	13.50	16.50	0.531	0.650	
B2		24.00		0.945	
С		14.00		0.551	
C1		3.50		0.138	
C2	1.95	3.00	0.077	0.118	
E3	0.70	0.90	0.027	0.035	
F	4.00	4.50	0.157	0.177	
Ι	11.20	13.60	0.441	0.535	
L1	3.10	3.50	0.122	0.138	
L2	1.70	1.90	0.067	0.075	
N1	33°	43°	33°	43°	
N2	28°	38°	28°	38°	

## PACKAGE MECHANICAL DATA

TOP3 Ins.(Plastic)



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