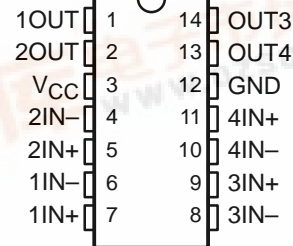


- **Controlled Baseline**
 - One Assembly/Test Site, One Fabrication Site
- **Extended Temperature Performance of**
 -40°C to 125°C
- **Enhanced Diminishing Manufacturing Sources (DMS) Support**
- **Enhanced Product-Change Notification**
- **Qualification Pedigree†**
- **Single Supply or Dual Supplies**
- **Wide Range of Supply Voltage**
 $\dots 2\text{ V}$ to 36 V
- **Low Supply-Current Drain Independent of Supply Voltage** $\dots 0.8\text{ mA Typ}$

- **Low Input Bias Current** $\dots 25\text{ nA Typ}$
- **Low Input Offset Current** $\dots 5\text{ nA Typ}$
- **Low Input Offset Voltage** $\dots 2\text{ mV Typ}$
- **Common-Mode Input Voltage Range Includes Ground**
- **Differential Input Voltage Range Equal to Maximum-Rated Supply Voltage** $\dots \pm 36\text{ V}$
- **Low Output Saturation Voltage**
- **Output Compatible With TTL, MOS, and CMOS**

† Component qualification in accordance with JEDEC and industry standards to ensure reliable operation over an extended temperature range. This includes, but is not limited to, Highly Accelerated Stress Test (HAST) or biased 85/85, temperature cycle, autoclave or unbiased HAST, electromigration, bond intermetallic life, and mold compound life. Such qualification testing should not be viewed as justifying use of this component beyond specified performance and environmental limits.

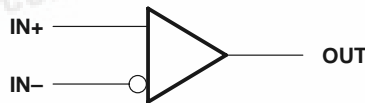
D PACKAGE
(TOP VIEW)



description

This device consists of four independent voltage comparators that are designed to operate from a single power supply over a wide range of voltages. Operation from dual supplies also is possible as long as the difference between the two supplies is 2 V to 36 V , and V_{CC} is at least 1.5 V more positive than the input common-mode voltage. Current drain is independent of the supply voltage. The outputs can be connected to other open-collector outputs to achieve wired-AND relationships.

symbol (each comparator)



ORDERING INFORMATION

TA	PACKAGE‡		ORDERABLE PART NUMBER	TOP-SIDE MARKING
-40°C to 125°C	SOP – D	Tape and reel	LM239AQDREP§	LM239AEP

‡ Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

§ This package is only available taped and reeled with standard quantities of 2500 pieces per reel.

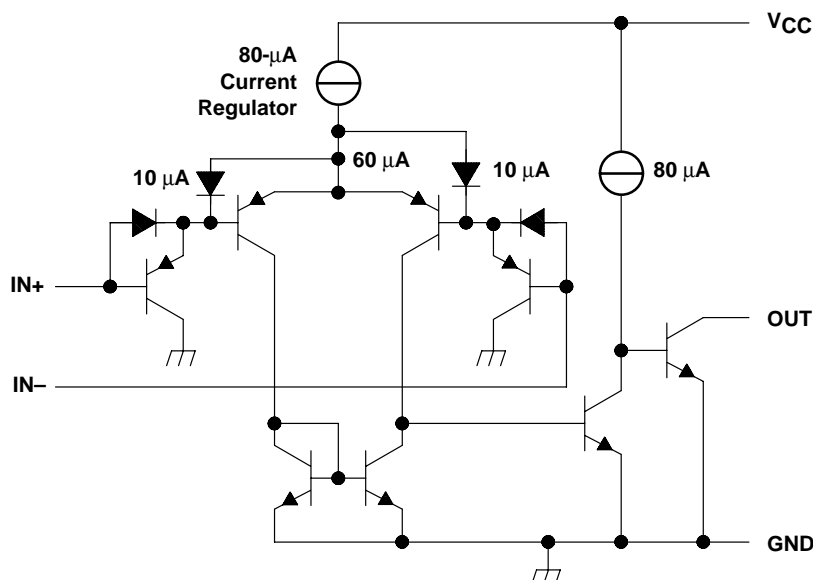
Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

LM239A-EP

QUAD DIFFERENTIAL COMPARATOR

SCLS496 – MAY 2003

schematic (each comparator)



All current values shown are nominal.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage, V_{CC} (see Note 1)	36 V
Differential input voltage, V_{ID} (see Note 2)	± 36 V
Input voltage range, V_I (either input)	-0.3 V to 36 V
Output voltage, V_O	36 V
Output current, I_O	20 mA
Duration of output short circuit to ground (see Note 3)	Unlimited
Package thermal impedance, θ_{JA} (see Note 4)	86°C/W
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C
Maximum operating junction temperature, T_J	136°C
Storage temperature range, T_{stg} (see Note 5)	-65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. All voltage values, except differential voltages, are with respect to network ground.

2. Differential voltages are at $IN+$ with respect to $IN-$.

3. Short circuits from outputs to V_{CC} can cause excessive heating and eventual destruction.

4. The package thermal impedance is calculated in accordance with JESD 51-7.

5. Long term high-temperature storage and/or extended use at maximum recommended operating conditions may result in a reduction of overall device life. See http://www.ti.com/ep_quality for additional information on enhanced plastic packaging.

LM239A-EP QUAD DIFFERENTIAL COMPARATOR

SCLS496 – MAY 2003

electrical characteristics at specified free-air temperature, $V_{CC} = 5\text{ V}$ (unless otherwise noted)

PARAMETER		TEST CONDITIONS†		T _A ‡	MIN	TYP	MAX	UNIT
V _{IO}	Input offset voltage	V _{CC} = 5 V to 30 V, V _{IC} = V _{ICR} (min), V _O = 1.4 V		25°C		1	2.5	mV
				Full range			5.5	
I _{IO}	Input offset current	V _O = 1.4 V		25°C		5	50	nA
				Full range			150	
I _{IB}	Input bias current	V _O = 1.4 V		25°C		–25	–250	nA
				Full range			–400	
V _{ICR}	Common-mode input-voltage range			25°C		0 to V _{CC} –1.5		V
				Full range		0 to V _{CC} –2		
A _{VD}	Large-signal differential-voltage amplification	V _{CC} = 15 V, V _O = 1.4 V to 11.4 V, R _L ≥ 15 kΩ to V _{CC}		25°C		50	200	V/mV
I _{OH}	High-level output current	V _{ID} = 1 V	V _{OH} = 5 V	25°C		0.1	50	nA
			V _{OH} = 30 V	Full range			1	μA
V _{OL}	Low-level output voltage	V _{ID} = –1 V, I _{OL} = 4 mA	25°C		150	400	mV	
			Full range			700		
I _{OL}	Low-level output current	V _{ID} = –1 V, V _{OL} = 1.5 V	25°C		6	16	mA	
I _{CC}	Supply current (four comparators)	V _O = 2.5 V, No load	25°C		0.8	2	mA	

† All characteristics are measured with zero common-mode input voltage, unless otherwise specified.

‡ Full range (MIN to MAX) for LM239AQ is –40°C to 125°C. All characteristics are measured with zero common-mode input voltage, unless otherwise specified.

switching characteristics, $V_{CC} = 5\text{ V}$, $T_A = 25^\circ\text{C}$

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Response time	R_L connected to 5 V through 5.1 k Ω , $C_L = 15\text{ pF}$ §, See Note 5	100-mV input step with 5-mV overdrive		1.3	μs
		TTL-level input step		0.3	

§ C_L includes probe and jig capacitance.

NOTE 5: The response time specified is the interval between the input step function and the instant when the output crosses 1.4 V.

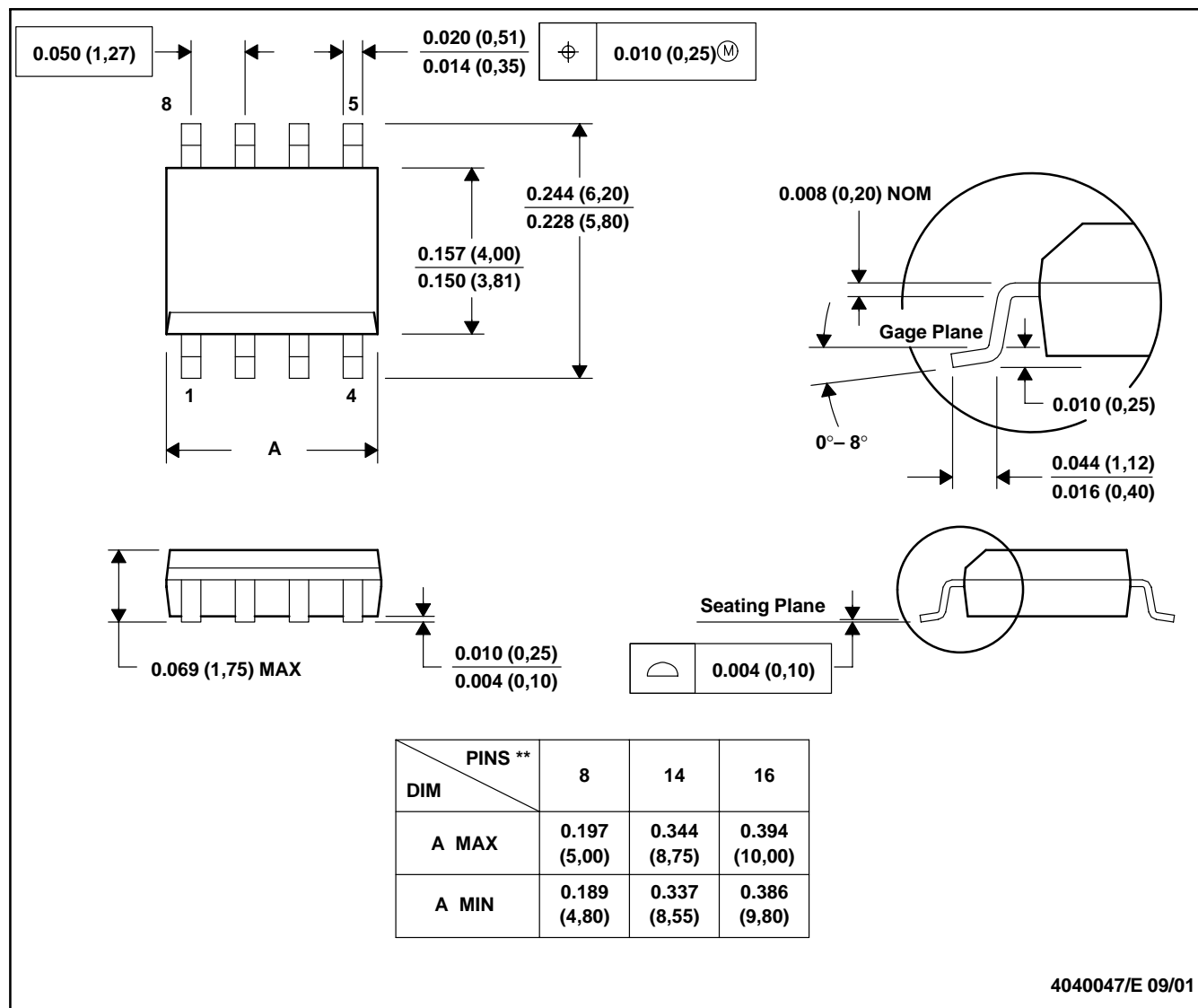
MECHANICAL DATA

MSOI002B – JANUARY 1995 – REVISED SEPTEMBER 2001

D (R-PDSO-G)**

PLASTIC SMALL-OUTLINE PACKAGE

8 PINS SHOWN



- NOTES:
- A. All linear dimensions are in inches (millimeters).
 - B. This drawing is subject to change without notice.
 - C. Body dimensions do not include mold flash or protrusion, not to exceed $0.006 (0,15)$.
 - D. Falls within JEDEC MS-012

IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

Products		Applications	
Amplifiers	amplifier.ti.com	Audio	www.ti.com/audio
Data Converters	dataconverter.ti.com	Automotive	www.ti.com/automotive
DSP	dsp.ti.com	Broadband	www.ti.com/broadband
Interface	interface.ti.com	Digital Control	www.ti.com/digitalcontrol
Logic	logic.ti.com	Military	www.ti.com/military
Power Mgmt	power.ti.com	Optical Networking	www.ti.com/opticalnetwork
Microcontrollers	microcontroller.ti.com	Security	www.ti.com/security
		Telephony	www.ti.com/telephony
		Video & Imaging	www.ti.com/video
		Wireless	www.ti.com/wireless

Mailing Address: Texas Instruments
Post Office Box 655303 Dallas, Texas 75265