FAIRCHILD SEMICONDUCTOR™

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DM74LS243
Quadruple Bus Transceiver

General Description

This four data line transceiver is designed for asynchronous two-way communications between data buses. It can be used to drive terminated lines down to 133Ω.

Features

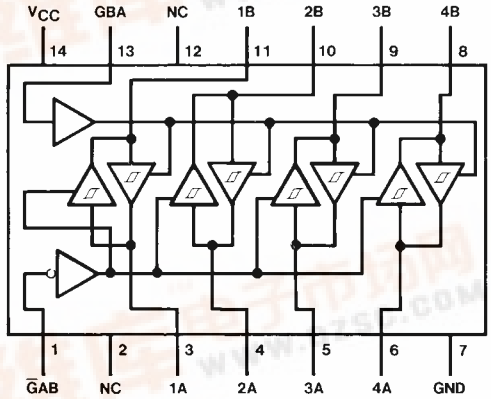
- Two-way asynchronous communication between data buses
- PNP inputs reduce DC loading on bus line
- Hysteresis at data inputs improves noise margin

Ordering Code:

Order Number	Package Number	Package Description
DM74LS243M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow
DM74LS243N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Connection Diagram



Function Table

Control Inputs		Data Port Status	
$\overline{G}$ AB	GBA	A	B
H	H	O	I
L	H	(Note 1)	(Note 1)
H	L	ISOLATED	
L	L	I	O

I = Input
O = Output
H = HIGH Logic Level
L = LOW Logic Level

Note 1: Possibly destructive oscillation may occur if the transceivers are enabled in both directions at once.

Absolute Maximum Ratings(Note 2)

Supply Voltage	7V
Input Voltage	
Any G	7V
A or B	5.5V
Operating Free Air Temperature Range	0°C to +70°C
Storage Temperature Range	−65°C to +150°C

Note 2: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V_{CC}	Supply Voltage	4.75	5	5.25	V
V_{IH}	HIGH Level Input Voltage	2			V
V_{IL}	LOW Level Input Voltage			0.8	V
I_{OH}	HIGH Level Output Current			−15	mA
I_{OL}	LOW Level Output Current			24	mA
T_A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions			Min	Typ (Note 3)	Max	Units
V_I	Input Clamp Voltage	V_{CC} = Min, I_I = −18 mA					−1.5	V
HYS	Hysteresis ($V_{T+} - V_{T-}$) (Data Inputs Only)	V_{CC} = Min			0.2	0.4		V
V_{OH}	HIGH Level Output Voltage	V_{CC} = Min, V_{IH} = Min V_{IL} = Max, I_{OH} = −1 mA			2.7			V
		V_{CC} = Min, V_{IH} = Min V_{IL} = Max, I_{OH} = −3 mA			2.4	3.4		
		V_{CC} = Min, V_{IH} = Min V_{IL} = 0.5V, I_{OH} = Max			2			
V_{OL}	LOW Level Output Voltage	V_{CC} = Min V_{IL} = Max V_{IH} = Min	I_{OL} = 12 mA				0.4	V
			I_{OL} = Max				0.5	
I_{OZH}	Off-State Output Current, HIGH Level Voltage Applied	V_{CC} = Max V_{IL} = Max	V_O = 2.7V				40	µA
I_{OZL}	Off-State Output Current, LOW Level Voltage Applied	V_{IH} = Min	V_O = 0.4V				−200	µA
I_I	Input Current at Maximum Input Voltage	V_{CC} = Max	V_I = 5.5V	A or B			0.1	mA
			V_I = 7V	Any G			0.1	mA
I_{IH}	HIGH Level Input Current	V_{CC} = Max, V_I = 2.7V					20	µA
I_{IL}	LOW Level Input Current	V_{CC} = Max, V_I = 0.4V					−0.2	mA
I_{OS}	Short Circuit Output Current	V_{CC} = Max (Note 4)			−40		−225	mA
I_{CC}	Supply Current	V_{CC} = Max	Outputs HIGH			22	38	mA
		Outputs OPEN	Outputs LOW			29	50	
			Outputs Disabled			32	54	

Note 3: All typicals are at V_{CC} = 5V, T_A = 25°C.

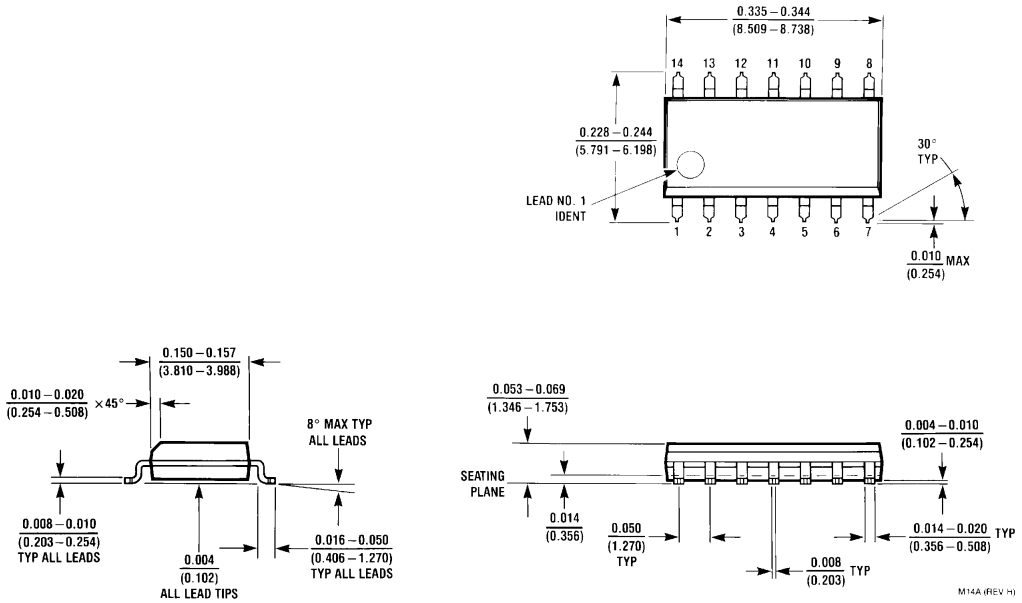
Note 4: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Switching Characteristics

at V_{CC} = 5V, T_A = 25°C

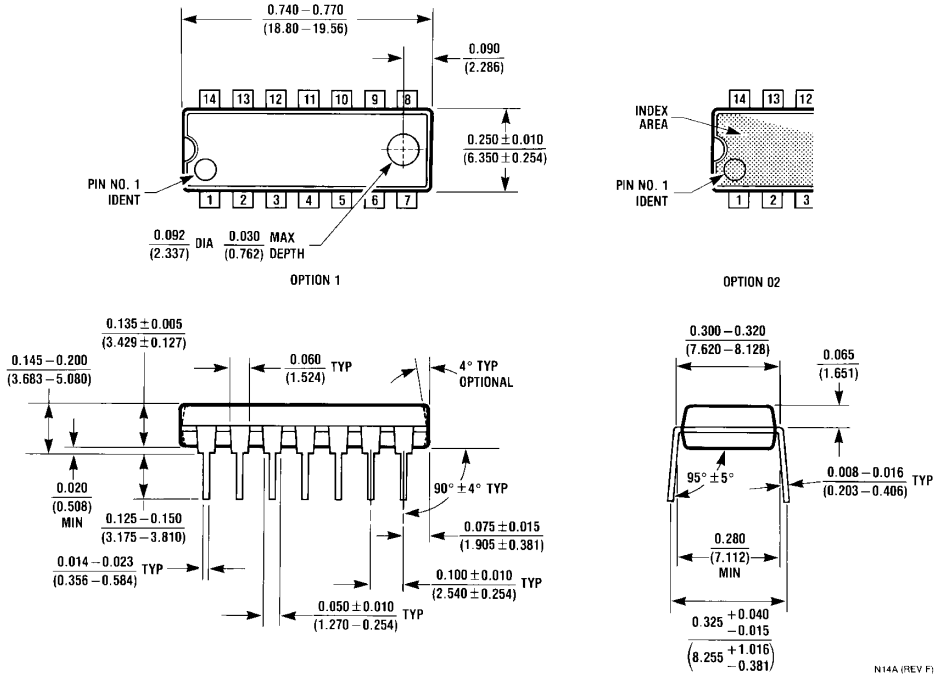
Symbol	Parameter	Conditions	Min	Max	Units
t_{PLH}	Propagation Delay Time LOW-to-HIGH Level Output	C_L = 45 pF R_L = 667Ω		18	ns
t_{PHL}	Propagation Delay Time HIGH-to-LOW Level Output	C_L = 45 pF R_L = 667Ω		18	ns
t_{PZL}	Output Enable Time to LOW Level	C_L = 45 pF R_L = 667Ω		30	ns
t_{PZH}	Output Enable Time to HIGH Level	C_L = 45 pF R_L = 667Ω		23	ns
t_{PLZ}	Output Disable Time from LOW Level	C_L = 5 pF R_L = 667Ω		25	ns
t_{PHZ}	Output Disable Time from HIGH Level	C_L = 5 pF R_L = 667Ω		18	ns
t_{PLH}	Propagation Delay Time LOW-to-HIGH Level Output	C_L = 150 pF R_L = 667Ω		21	ns
t_{PHL}	Propagation Delay Time HIGH-to-LOW Level Output	C_L = 150 pF R_L = 667Ω		22	ns
t_{PZL}	Output Enable Time to LOW Level	C_L = 150 pF R_L = 667Ω		33	ns
t_{PZH}	Output Enable Time to HIGH Level	C_L = 150 pF R_L = 667Ω		26	ns

Physical Dimensions inches (millimeters) unless otherwise noted



14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow
Package Number M14A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide
Package Number N14A