

# 2-INPUT 3CHANNEL VIDEO SWITCH

#### **■ GENERAL DESCRIPTION**

NJM2285 is a switching IC for switching over from one audio or video input signal to another. Internalizing 2 inputs, 1 output, and then each set of 3 can be operated independently. Two of them are Clamp type", and they can be operated while setting DC level fixed in position of the video signal. It is a higher efficiency video switch, featuring the operating supply voltage 5 to 12V, the frequency feature 10MHz, and then the crosstalk 75dB (at 4.43MHz).

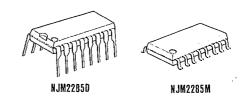
#### FEATURES

- 2 Input-1 Output
  - Internalizing 3 Circuits (Two of them are Clamp type).
- Wide Operating Supply Voltage (4.75~13.0V)
- Crosstalk 75dB(at 4.43MHz)
- Wide Bandwidth Frequency Feature 10MHz(2V<sub>P-P</sub> Input)
- Package Outline DIP16, DMP16, SSOP16
- Bipolar Technology

#### **■ APPLICATIONS**

• VCR, Video Camera, AV-TV, Video Disk Player.

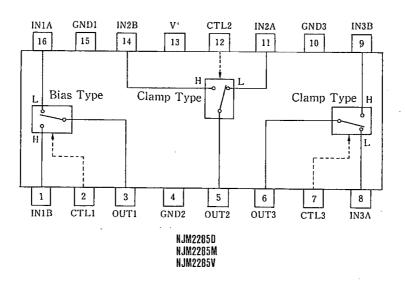
#### **■ PACKAGE OUTLINE**





NJM2285V

#### **■ BLOCK DIAGRAM**



# ■ MAXIMUM RATINGS

(Ta=25°C)

| PARAMETER                   | SYMBOL | RATINGS      | UNIT            |  |
|-----------------------------|--------|--------------|-----------------|--|
| Supply Voltage              | V*     | 14           |                 |  |
| Power Dissipation           | PD     | (DIP16) 700  | mW              |  |
|                             |        | (DMP16) 350  | mW              |  |
|                             |        | (SSOP16) 300 | mW              |  |
| Operating Temperature Range | Торг   | -40~+85      | r               |  |
| Storage Temperature Range   | Tstg   | -40~+125     | ${\mathfrak C}$ |  |

# **■ ELECTRICAL CHARACTERISTICS**

(V\*=5V, Ta=25℃)

| PARAMETER                  | SYMBOL           | TEST CONDITION   | MIN. | TYP. | MAX.     | UNIT |
|----------------------------|------------------|--|------|------|----------|------|
| Operating Current (1)      | I <sub>CC1</sub> | V <sup>+</sup> =5V (Notel)                                     | 8.0  | 11.4 | 14.8     | mA   |
| Operating Current (2)      | I <sub>CC2</sub> | V+=9V (Notel)  | 0.01 | 14.3 | 18.6     | mA   |
| Voltage Gain               | Gv               | $V_1 = 100 \text{kHz}, 2V_{P-P}, V_O / V_1$                    | -0.6 | -0.1 | +0.4     | dB   |
| Frequency Gain             | Gr               | $V_1 = 2V_{P-P}, V_O(10MHz)/V_O(100kHz)$                       | -1.0 | 0    | +1.0     | dB   |
| Differential Gain          | DG               | V <sub>1</sub> = 2V <sub>P-P</sub> , Standard Staircase Signal | -    | 0.3  |          | %    |
| Differential Phasa         | DP               | V <sub>1</sub> = 2V <sub>P-P</sub> , Standard Staircase Signal |      | 0.3  | <u> </u> | deg  |
| Output offset Voltage      | Vos              | (Note2)  | -10  | 0    | +10      | mV   |
| Crosstalk                  | CT               | $V_1 = 2V_{P-P}$ , 4.43MHz, $V_0/V_1$                          | _    | -75  | l —      | dB   |
| Switch Change Over Voltage | V <sub>CII</sub> | All inside Switches ON   | 2.5  |      |          | v    |
| Switch Change Over Voltage | V <sub>CL</sub>  | All inside Switches OFF  | -    | _    | 1.0      | v    |

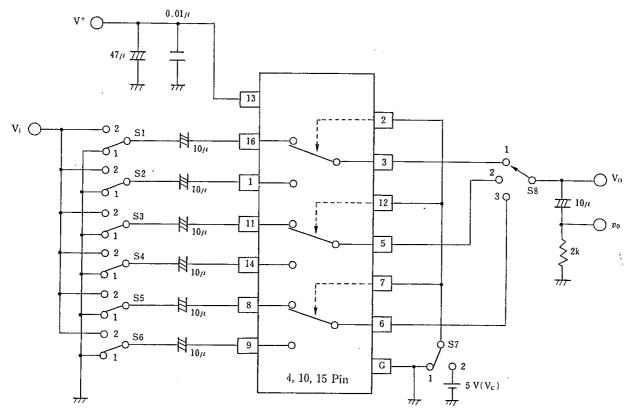
<sup>(</sup>Notel) S1=S2=S3=S4=S5=S6=S7=1

<sup>(</sup>Note2) S1=S2=S3=S4=S5=S6=1,  $S7=1\rightarrow 2$  Measure the output DC voltage difference

# **■ TERMINAL EXPLANATION**

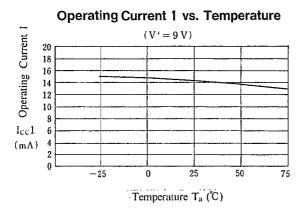
| PIN No.            | PIN NAME  | VOLTAGE | INSIDE EQUIVALENT CIRCUIT |
|--------------------|---|---------|---------------------------|
| 16<br>1            | IN 1 A<br>IN 1 B<br>(Input)                     | 2.5V    | IN O                      |
|                    |   |         | 500 15k 2.5V              |
| 11<br>14<br>8<br>9 | IN 2 A<br>IN 2 B<br>IN 3 A<br>IN 3 B<br>(Input) | 1.5V    | 500<br>500                |
|                    |   |         | 2.2V                      |
| 2<br>12<br>7       | CTL 1 CTL 2 CTL 3 (Switching)                   |         | 2.3V                      |
| 3                  | OUT 1   | 1.8V    |                           |
| 5<br>6             | OUT 2<br>OUT 3<br>(Output)                      | 0.8 V   | O OUT                     |
| 13                 | V+  | 5 V     |                           |
| 15<br>4<br>10      | GND 1<br>GND 2<br>GND 3                         |         |                           |

# **■ TEST CIRCUIT**

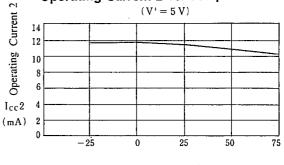


This IC requires  $1M\Omega$  resistance between INPUT and GND pin for clamp type input since the minute current causes an unstable pin voltage.

| Parameter       | SI  | S 2 | S 3 | S 4 | S 5 | S 6 | S 7 | S 8 | Test Par       |
|-----------------|-----|-----|-----|-----|-----|-----|-----|-----|----------------|
| Icc1            | 1   | 1   | 1   | 1   | 1   | 1   | 1   | 1   | V+             |
| Icc2            | 1   | 1   | 1   | 1   | 1   | 1   | 1   | 1   |                |
| Gvl             | 2   | 1   | 1   | 1   | 1   | 1   | 1   | 1   | $v_0$          |
| Gri             | 2   | 1   | 1   | 1   | 1   | 1   | 1   | 1   |                |
| DG <sub>1</sub> | 2   | 1   | 1   | 1   | 1   | 1   | 1   | 1   |                |
| $DP_1$          | 2   | 1   | 1   | 1   | 1   | 1   | 1   | 1   |                |
| CT 1            | 2   | 1   | 1   | 1   | 1   | 1   | 2   | 1   | $v_0$          |
| CT 2            | 1   | 2   | 1   | 1   | 1   | 1   | 1   | 1   |                |
| CT3             | 1   | 1   | 2   | 1   | 1   | 1   | 2   | 2   | }              |
| CT 4            | 1   | 1   | 1   | 2   | 1   | 1   | 1   | 2   |                |
| CT 5            | 1   | 1   | 1   | 1   | 2   | 1   | 2   | 3   |                |
| CT 6            | 1   | 1   | 1   | 1   | 1   | 2   | 1   | 3   |                |
| Vosi            | 1   | 1   | 1   | 1   | 1   | . 1 | 1/2 | 1   | Vo             |
| Vcı             | 1/2 | 2/1 | 1   | 1   | 1   | 1   | Vc  | 1   | Vc             |
| THD             | 2   | 1   | 1   | 1   | 1   | 1   | 1   | 1   | v <sub>0</sub> |

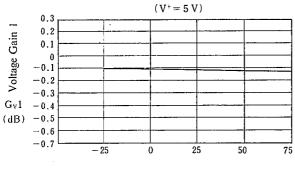


# Operating Current 2 vs. Temperature (V = 5 V)



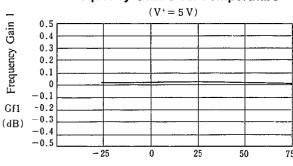
Temperature  $T_a$  (°C)

# Voltage Gain 1 vs. Temperature



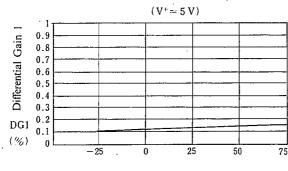
Temperature  $T_a$  (°C)

### Frequency Gain 1 vs. Temperature



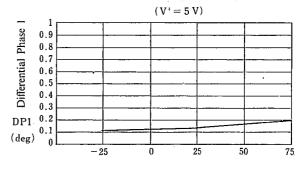
Temperature T<sub>a</sub> (°C)

#### Differential Gain 1 vs. Temperature



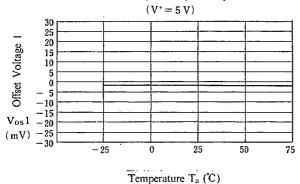
Temperature T<sub>a</sub> (°C)

# Differential Phase 1 vs. Temperature

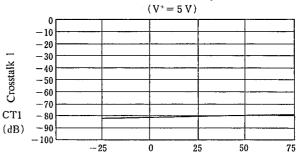


Temperature Ta (℃)

#### Offset Voltage 1 vs. Temperature

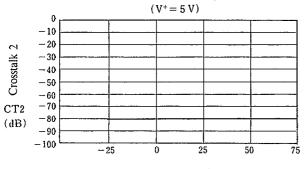


#### Crosstalk 1 vs. Temperature



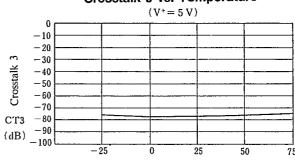
Temperature T<sub>a</sub> (°C)

### Crosstalk 2 vs. Temperature



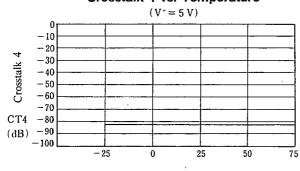
Temperature T<sub>a</sub> (°C)

#### Crosstalk 3 vs. Temperature



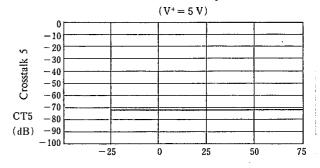
Temperature  $T_a$  (°C)

# Crosstalk 4 vs. Temperature



Temperature T<sub>a</sub> (℃)

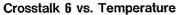
#### Crosstalk 5 vs. Temperature

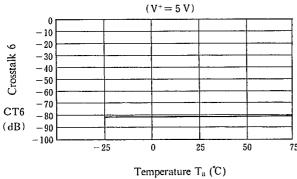


Temperature T<sub>a</sub> (°C)

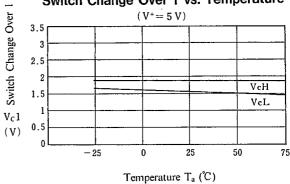
# 5

# **■ TYPICAL CHARACTERISTICS**

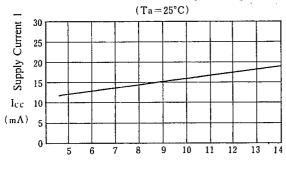




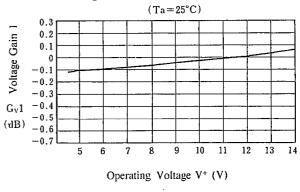
# Switch Change Over 1 vs. Temperature



#### Supply Current 1 vs. Operating Voltage

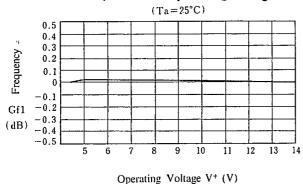


# Voltage Gain 1 vs. Operating Voltage

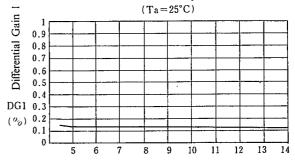


#### Frequency vs. Operating Voltage

Operating Voltage V+ (V)

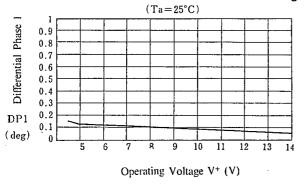


# Differential Gain 1 vs. Operating!Voltage

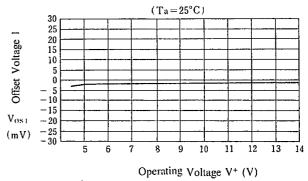


Operating Voltage V+ (V)

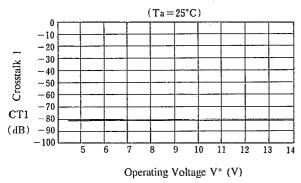
# Differential Phase 1 vs. Operating Voltage



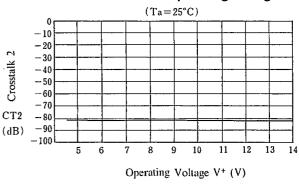
#### Offset Voltage 1 vs. Operating Voltage



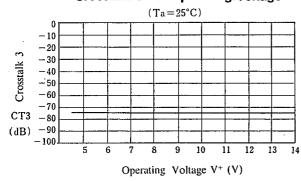
# Crosstalk 1 vs. Operating Voltage



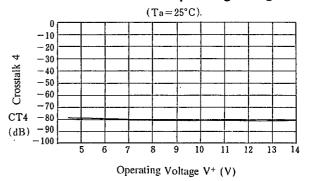
# Crosstalk 2 vs. Operating Voltage



#### Crosstalk 3 vs. Operating Voltage



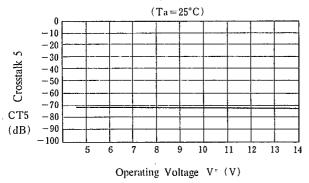
# Crosstalk 4 vs. Operating Voltage



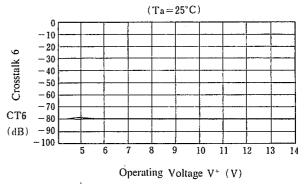
# 5

#### **■ TYPICAL CHARACTERISTICS**

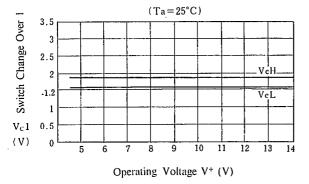
## Crosstalk 5 vs. Operating Voltage V+ (V)



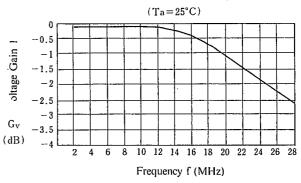
# Crosstalk 6 vs. Operating Voltage V+ (V)



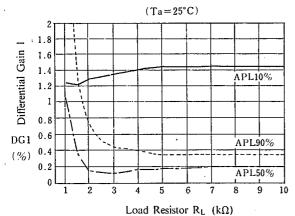
Switch Change Over 1 vs., Operating Voltage



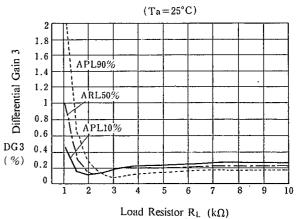
Voltage Gain 1 vs. Frequency



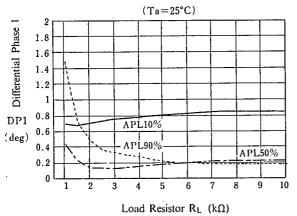
Differential Gain 1 vs. Load Resistor



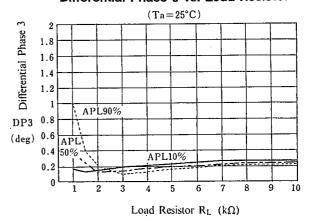
#### Differential Gain 3 vs. Load Resistor



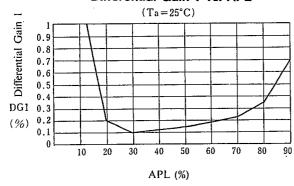
#### Differential Phase 1 vs. Load Resistor



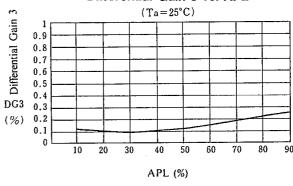
# Differential Phase 3 vs. Load Resistor



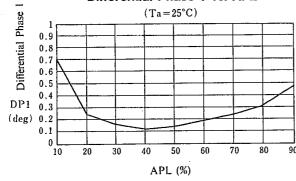
#### Differential Gain 1 vs. APL



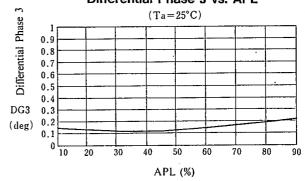
#### Differential Gain 3 vs. APL

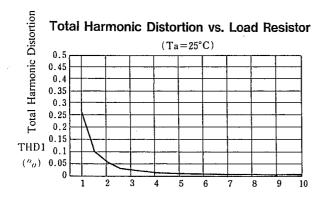


#### Differential Phase 1 vs. APL



# Differential Phase 3 vs. APL

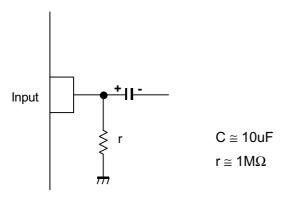




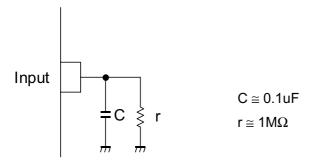
Load Resistor  $R_L$  (k $\Omega$ )

#### **■**APPLICATION

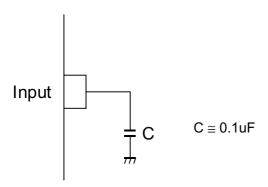
This IC requires  $1M\Omega$  resistance between INPUT and GND pin for clamp type input since the minute current causes an unstable pin voltage.



This IC requires 0.1uF capacitor between INPUT and GND, 1MΩ resistance between INPUT and GND for clamp type input at mute mode.



This IC requires 0.1uF capacitor between INPUT and GND for bias type input at mute mode.



#### [CAUTION]

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