

M5243BFP

3-element 2-ch Graphic Equalizer IC

REJ03F0082-0100Z Rev.1.0 Sep.22.2003

Description

This 2-ch, 3-element graphic equalizer IC is ideal for Hi-Fi audio devices, and features three transistor-type resonance circuits and an output operating amp that handles two channels built into it. It is designed for use in radio cassette players, car stereos, portable stereos and other devices.

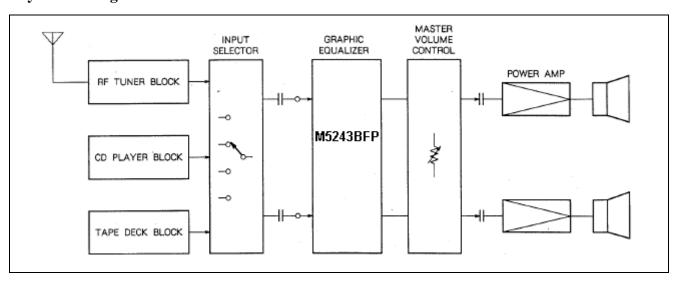
Features

- Two-channel (stereo) processing is possible with single IC.
- An internal reference voltage circuit eliminates the need for a large-volume capacitor and makes it possible to use fewer components.
- The Gv can be varied using an external resistor.
- Low noise V_{NO} FLAT = 4 μV_{rms} (standard)

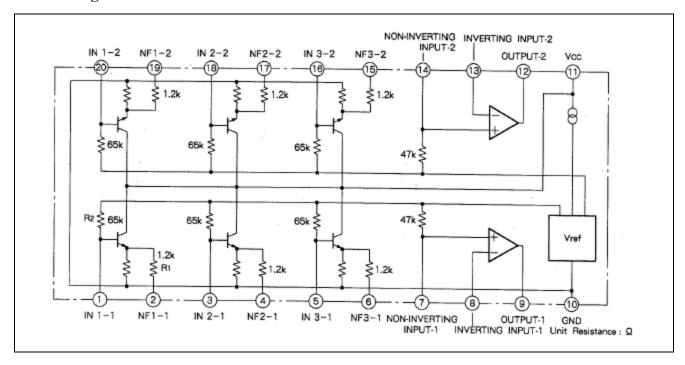
Recommended Operating Conditions

Rated power dissipation: 550 mW (FP)

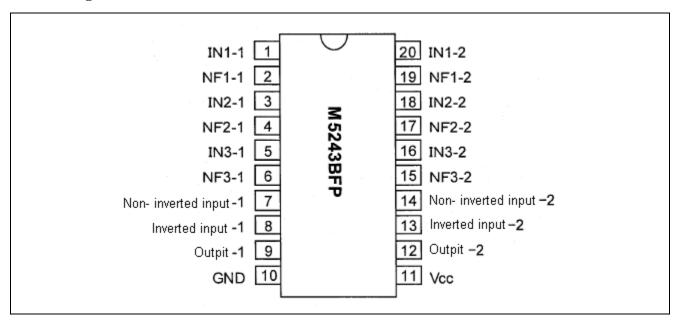
System Configuration



Block Diagram



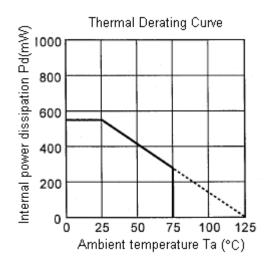
Pin Configuration



Absolute Maximum Ratings

(Unless otherwise noted, $Ta = 25^{\circ}C$)

Symbol	Item	Conditions	Rated values	Unit	
Vcc	Power supply voltage		16	V	
I _{LP}	Current load		30	mA	
P _d	Internal power dissipation		550	mW	
T _{opr}	Ambient operating temperature		-20 to +75	°C	
T _{stg}	Storage temperature		-55 to +125	°C	

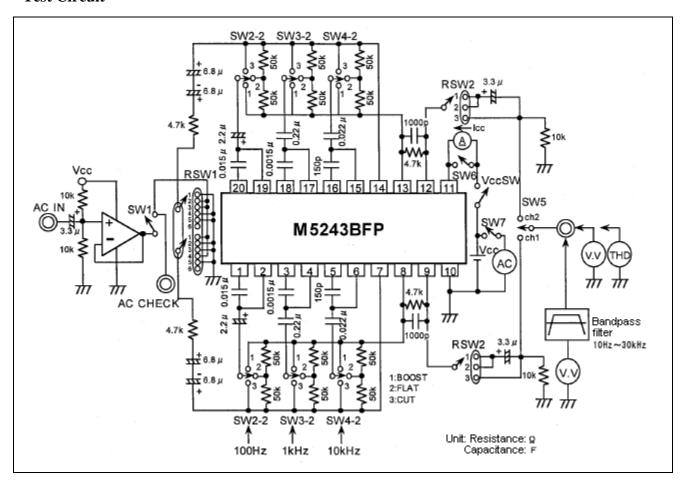


Electrical Characteristics

(Unless otherwise noted, $Ta = 25^{\circ}C$)

Symbol	Item		Measurement		Limits	Unit		
			conditions f (Hz)		Min.	Тур.	Max.	
Icc	Circuit current			_	9.0	12.5	16.0	mA
Gv (FLAT)	Voltage gain Flat		V1 = -10dBm	1k	-2.0	-0.5	1.0	dB
Gv	-	Boost	V1 = −10dBm	100	10.0	12.0	14.0	_
(BOOST)			Vi = 0 dBm	1k	10.0	12.0	14.0	
			Rg = 4.7k	10k	10.0	12.0	14.0	
Gv (CUT)	-	Cut	_	100	-14.0	-12.0	-10.0	
				1k	-14.0	-12.0	-10.0	
				10k	-10.0	-12.0	-10.0	
THD	Total harmonic	distortion	Vi = 1Vms All FLAT	1k	_	0.003	0.1	%
V _{OM}	Maximum out p	out voltage	THD = 0.1% All FLAT	1k	1.5	1.9	_	Vrms
C.C	Channel separa	ation	V1 = -10 dBm All FLAT	1k	60	75	_	dB
H.R	Hum rejection		V1 = -10 dBm All FLAT	120	55	65	_	dB
V _{NO}	Output noise vo	oltage	All FLAT BW: 10 Hz to 30 kHz	_	_	3.5	15	μVrms
V _M	Midpoint potent	tial		_	3.5	4.5	5.5	V

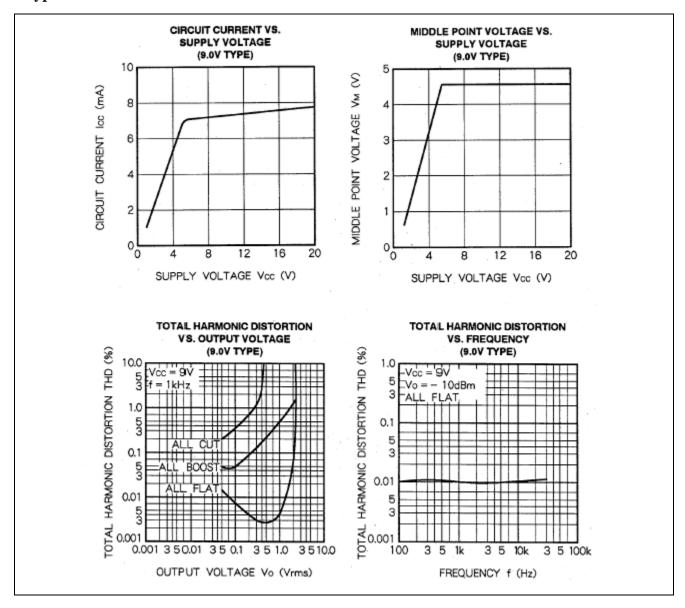
Test Circuit



Switch matrices

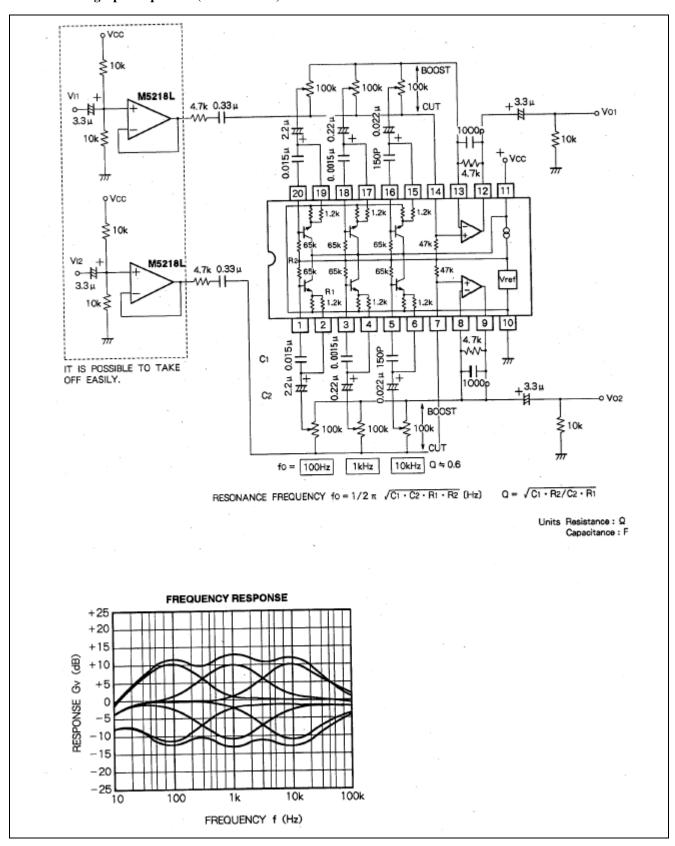
Item				RSW 1	RSW 2	SW 1	SW 2	SW 3-1	SW 4-1	SW 2-2	SW 3-2	SW 4-2	SW 5	SW 6	SW 7	Remarks
Circuit current Icc			-	1or 2	-	-	-	-	-	-	-	-	OFF	OFF		
Volt	Gv	ch1		1	1	ON	2	2	2	_	-	-	ch1	ON	OFF	
age	(FLAT)	ch2		2	2	ON	_	_	-	2	2	2	ch2	ON	OFF	
gain	Gv (BOOST)	ch	100 Hz	1	1	ON	1	2	2	_	_	-	ch1	ON	OFF	
		1	1 Hz	1	1	ON	2	1	2	-	-	-	ch1	ON	OFF	
			10 Hz	1	1	ON	2	2	1	-	-	-	ch1	ON	OFF	
		ch	100 Hz	2	2	ON	-	-	-	1	2	2	ch2	ON	OFF	
		2	1 Hz	2	2	ON	_	_	_	2	1	2	ch2	ON	OFF	
			10 Hz	2	2	ON	_	_	_	2	2	1	ch2	ON	OFF	
	Gv	ch	100 Hz	1	1	ON	3	2	2	_	-	-	ch1	ON	OFF	
	(CUT)	1	1 Hz	1	1	ON	2	3	2	-	-	-	ch1	ON	OFF	
			10 Hz	1	1	ON	2	2	3	-	-	-	ch1	ON	OFF	
		ch	100 Hz	2	2	ON	_	_	_	3	2	2	ch2	ON	OFF	
		2	1 Hz	2	2	ON	_	_	_	2	3	2	ch2	ON	OFF	
	10 Hz		10 Hz	2	2	ON	_	_	_	2	2	3	ch2	ON	OFF	
	Maximum output ch voltage V _{OM}		ch1	1	1	ON	2	2	2	-	-	-	ch1	ON	OFF	
			ch2	2	2	ON	-	-	_	2	2	2	ch2	ON	OFF	
	harmonic tion THD (Fl	_AT)	ch1	1	1	ON	2	2	2	-	-	-	ch1	ON	OFF	BOOST: Set SW2-
			ch2	2	2	ON	-	-	-	2	2	2	ch2	ON	OFF	SW4 to 1.CUT:
	Output noise voltage ch1 V _{NO} (FLAT)		5	1	OFF	2	2	2	_	-	-	ch1	ON	OFF	SW4 to 3.	
			ch2	5	2	OFF	-	-	_	2	2	2	ch2	ON	OFF	<u>-</u>
Chan	Channel separation ch1		3	1	ON	2	2	2	_	_	_	ch1	ON	OFF		
CS ch2		ch2	4	2	ON	_	_		2	2	2	ch2	ON	OFF		
Hum	Hum rejection HR ch1		5	1	OFF	2	2	2	_	_	-	ch1	ON	ON		
			ch2	5	2	OFF	-	-	-	2	2	2	ch2	ON	ON	
Midpoint potential V _M ch1		6	3	OFF	-	-	-	-	-	_	ch1	ON	OFF			
			ch2	6	3	OFF	-	-	_	-	-	-	ch2	ON	OFF	

Typical Characteristics

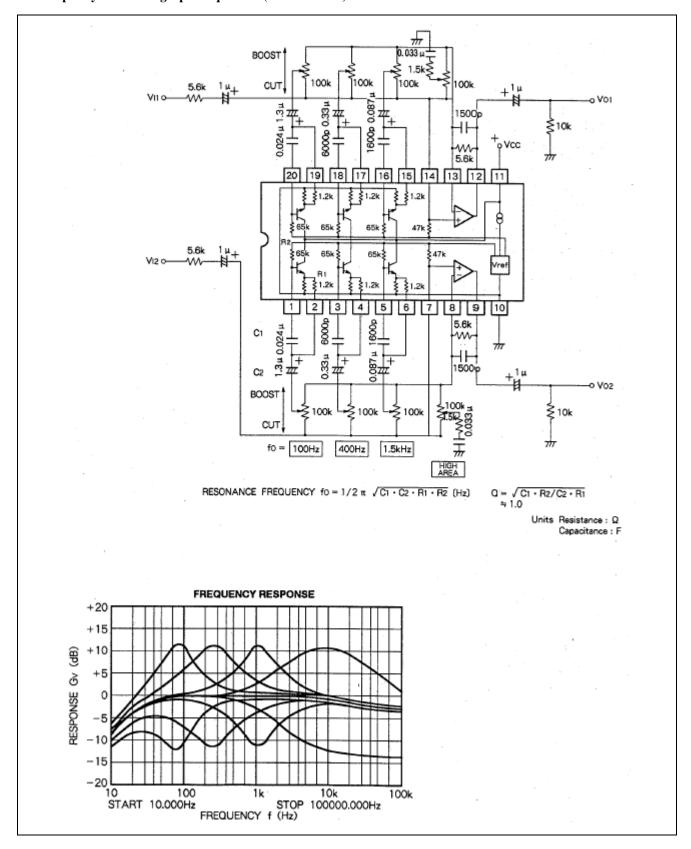


Application Example

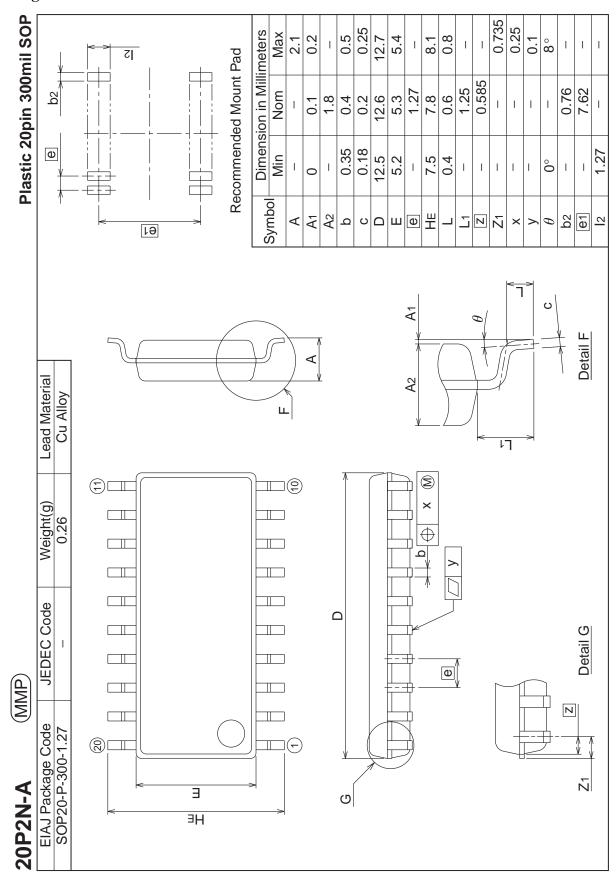
1. 3-Element graphic equalizer (Dual channel)



2. Simplicity 4-element graphic equalizer (Dual channel)



Package Dimensions



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