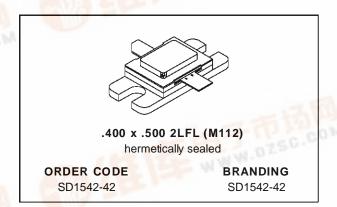


# SD1542-42

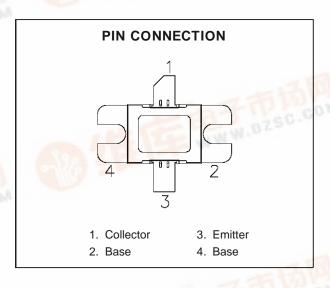
# RF & MICROWAVE TRANSISTORS AVIONICS APPLICATIONS

- DESIGNED FOR HIGH POWER PULSED IFF
- 600 WATTS (min.) IFF 1030 or 1090 MHz
- REFRACTORY GOLD METALLIZATION
- 6.0 dB MIN. GAIN
- LOW THERMAL RESISTANCE FOR RELIABILITY AND RUGGEDNESS
- 30:1 LOAD VSWR CAPABILITY AT SPECIFIED OPERATING CONDITIONS
- INPUT MATCHED, COMMON BASE CONFIGURATION



# **DESCRIPTION**

The SD1542-42 is a hermetically sealed, gold metallized, silicon NPN power transistor. The SD1542-42 is designed for applications requiring high peak power and low duty cycles such as IFF. The SD1542-42 is packaged in a hermetic metal/ceramic package with internal input matching, resulting in improved broadband performance and low thermal resistance.



#### **ABSOLUTE MAXIMUM RATINGS** $(T_{case} = 25^{\circ}C)$

Symbol	Parameter Value		Unit
Vcc	Collector-Supply Voltage*	55	V
Ic	Device Current* (T <sub>C</sub> ≤ 100°C)	45	А
P <sub>DISS</sub>	Power Dissipation*	1670	W
TJ	Junction Temperature	+200	°C
T <sub>STG</sub>	Storage Temperature	- 65 to +200	°C

#### THERMAL DATA

df.dzsc.com

<b>RTH(j-c)</b>	Junction-Case Thermal Resistance*	0.06	°C/W
# PE			

# **ELECTRICAL SPECIFICATIONS** $(T_{case} = 25^{\circ}C)$

# **STATIC**

Symbol	Test Conditions	Value			Unit		
		Min.	Тур.	Max.			
ВУсво	I <sub>C</sub> = 25 mA	$I_E = 0 \text{ mA}$		65	_		V
BV <sub>CER</sub>	I <sub>C</sub> = 25 mA	$R_{BE} = 10 \Omega$		65	_		V
BV <sub>EBO</sub>	I <sub>E</sub> = 10 mA	$I_C = 0 \text{ mA}$		3.5	_	_	V
Ices	V <sub>CE</sub> = 50 V	$V_{BE} = 0 V$		_	_	60	mA
h <sub>FE</sub>	V <sub>CE</sub> = 5 V	I <sub>C</sub> = 2 A		10	_	250	_

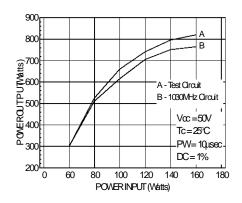
#### **DYNAMIC**

Symbol	Test Conditions			Value			
			Min.	Тур.	Max.	Unit	
Pout	f = 1090 MHz	$P_{IN} = 150 \text{ W}$	$V_{CC} = 50 V$	600	680	_	W
$\eta_{C}$	f = 1090 MHz	$P_{IN} = 150 \text{ W}$	$V_{CC} = 50 V$	35	40	_	%
G <sub>P</sub>	f = 1090 MHz	P <sub>IN</sub> = 150 W	Vcc = 50 V	6.0	6.6	_	dB

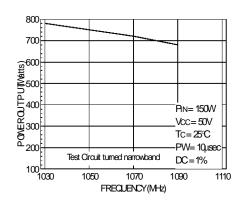
Note: Pulse Width =  $10\mu Sec$ , Duty Cycle = 1%

# **TYPICAL PERFORMANCE**

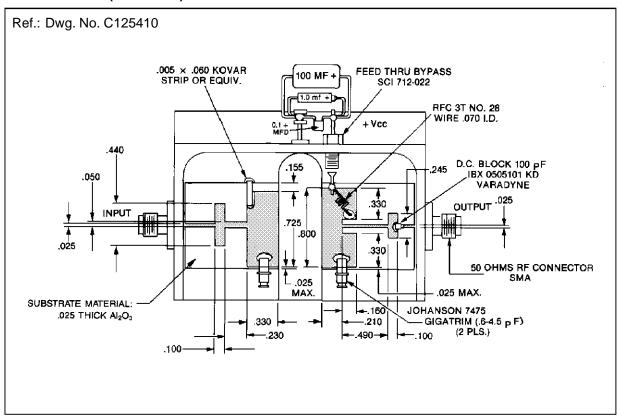
# **POWER OUTPUT vs POWER INPUT**



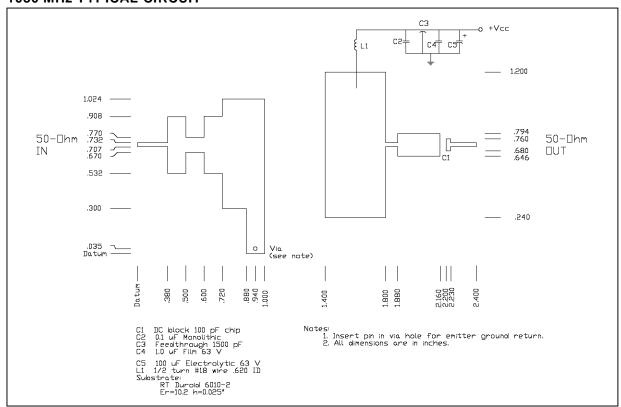
# **POWER OUTPUT vs FREQUENCY**



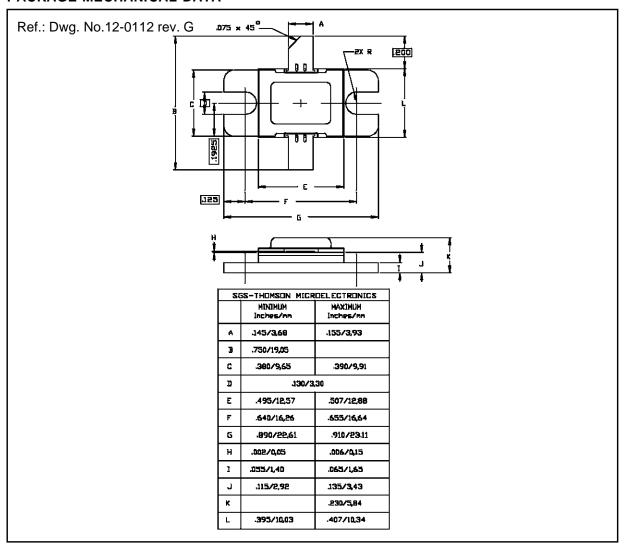
# **TEST CIRCUIT (1090 MHz)**



#### 1030 MHz TYPICAL CIRCUIT



#### PACKAGE MECHANICAL DATA



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