



# BTA12 BW/CW BTB12 BW/CW

## SNUBBERLESS TRIACS

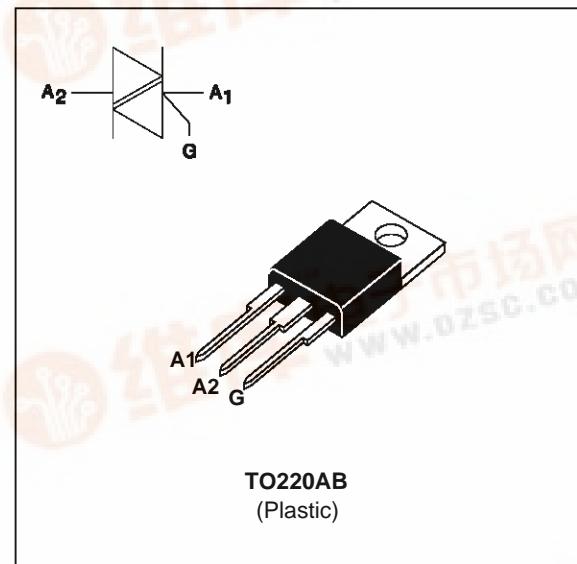
### FEATURES

- HIGH COMMUTATION :  $(dI/dt)c > 12A/ms$  without snubber
- HIGH SURGE CURRENT :  $I_{TSM} = 120A$
- $V_{DRM}$  UP TO 800V
- BTA Family :
  - INSULATING VOLTAGE = 2500V(RMS)
  - (UL RECOGNIZED : E81734)

### DESCRIPTION

The BTA/BTB12 BW/CW triac family are high performance glass passivated chips technology.

The SNUBBERLESS™ concept offer suppression of RC network and it is suitable for application such as phase control and static switching on inductive or resistive load.



### ABSOLUTE RATINGS (limiting values)

Symbol	Parameter			Value	Unit
$I_T(RMS)$	RMS on-state current (360° conduction angle)	BTA	$T_c = 85^\circ C$	12	A
		BTB	$T_c = 95^\circ C$		
$I_{TSM}$	Non repetitive surge peak on-state current ( $T_j$ initial = 25°C )	tp = 8.3 ms		126	A
		tp = 10 ms		120	
$I^{2t}$	$I^{2t}$ value	tp = 10 ms		72	A <sup>2</sup> s
$dl/dt$	Critical rate of rise of on-state current Gate supply : $I_G = 500mA$ $di_G/dt = 1A/\mu s$	Repetitive $F = 50$ Hz	20		$A/\mu s$
		Non Repetitive	100		
$T_{stg}$ $T_j$	Storage and operating junction temperature range			- 40 to + 150	$^\circ C$
				- 40 to + 125	$^\circ C$
$T_l$	Maximum lead temperature for soldering during 10 s at 4.5 mm from case	260		$^\circ C$	

Symbol	Parameter	BTA / BTB12... BW/CW				Unit
		400	600	700	800	
$V_{DRM}$ $V_{RRM}$	Repetitive peak off-state voltage $T_j = 125^\circ C$	400	600	700	800	V

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### THERMAL RESISTANCES

Symbol	Parameter		Value		Unit
R <sub>th</sub> (j-a)	Junction to ambient		60		°C/W
R <sub>th</sub> (j-c) DC	Junction to case for DC		BTA	3.3	°C/W
	BTB	2.7			
R <sub>th</sub> (j-c) AC	Junction to case for 360° conduction angle (F = 50 Hz)		BTA	2.5	°C/W
	BTB	2.0			

### GATE CHARACTERISTICS (maximum values)

P<sub>G</sub> (AV) = 1W    P<sub>GM</sub> = 10W (tp = 20 μs)    I<sub>GM</sub> = 4A (tp = 20 μs)    V<sub>GM</sub> = 16V (tp = 20 μs).

### ELECTRICAL CHARACTERISTICS

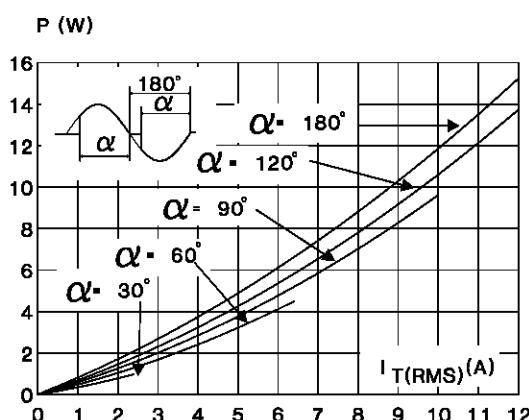
Symbol	Test Conditions	Quadrant	Suffix		Unit		
			BW	CW			
I <sub>GT</sub>	V <sub>D</sub> =12V (DC) R <sub>L</sub> =33Ω	T <sub>j</sub> =25°C	I-II-III	MIN	2		
				MAX	50		
V <sub>GT</sub>	V <sub>D</sub> =12V (DC) R <sub>L</sub> =33Ω	T <sub>j</sub> =25°C	I-II-III	MAX	1.5		
V <sub>GD</sub>	V <sub>D</sub> =V <sub>DRM</sub> R <sub>L</sub> =3.3kΩ	T <sub>j</sub> =125°C	I-II-III	MIN	0.2		
t <sub>gt</sub>	V <sub>D</sub> =V <sub>DRM</sub> I <sub>G</sub> = 500mA dI <sub>G</sub> /dt = 3A/μs	T <sub>j</sub> =25°C	I-II-III	TYP	2		
I <sub>L</sub>	I <sub>G</sub> =1.2 I <sub>GT</sub>	T <sub>j</sub> =25°C	I-III	TYP	40		
			II	TYP	80		
			I-III	MAX	-		
			II	MAX	50		
I <sub>H</sub> *	I <sub>T</sub> = 500mA gate open	T <sub>j</sub> =25°C		MAX	50		
V <sub>TM</sub> *	I <sub>TM</sub> = 17A tp= 380μs	T <sub>j</sub> =25°C		MAX	1.60		
I <sub>DRM</sub> I <sub>RRM</sub>	V <sub>DRM</sub> Rated V <sub>RRM</sub> Rated	T <sub>j</sub> =25°C		MAX	0.01		
		T <sub>j</sub> =125°C		MAX	2		
dV/dt *	Linear slope up to V <sub>D</sub> =67%V <sub>DRM</sub> gate open	T <sub>j</sub> =125°C		MIN	500		
				TYP	250		
(dI/dt) <sub>c</sub> *	Without snubber	T <sub>j</sub> =125°C		MIN	750		
				TYP	500		
				MIN	12		
				TYP	6.5		
				MIN	24		
				TYP	13		

\* For either polarity of electrode A2 voltage with reference to electrode A1.

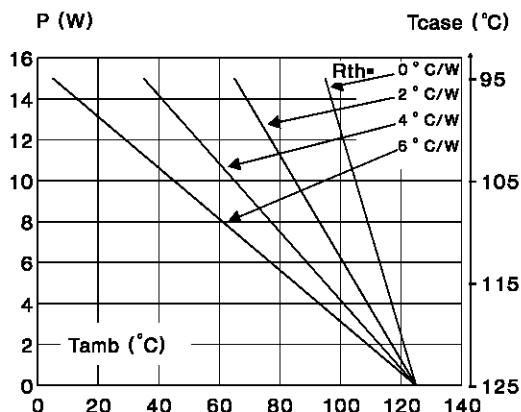
### ORDERING INFORMATION

Package	$I_T(\text{RMS})$	$V_{\text{DRM}} / V_{\text{RRM}}$	Sensitivity Specification	
			BW	CW
BTA (Insulated)	12	400	X	X
		600	X	X
		700	X	X
		800	X	X
BTB (Uninsulated)		400	X	X
		600	X	X
		700	X	X
		800	X	X

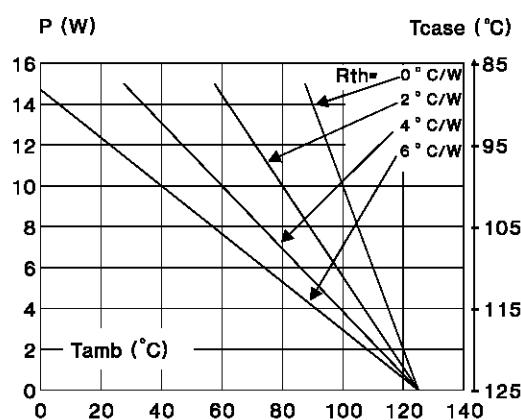
**Fig.1 :** Maximum RMS power dissipation versus RMS on-state current ( $F=50\text{Hz}$ ).  
(Curves are cut off by  $(dI/dt)_c$  limitation)



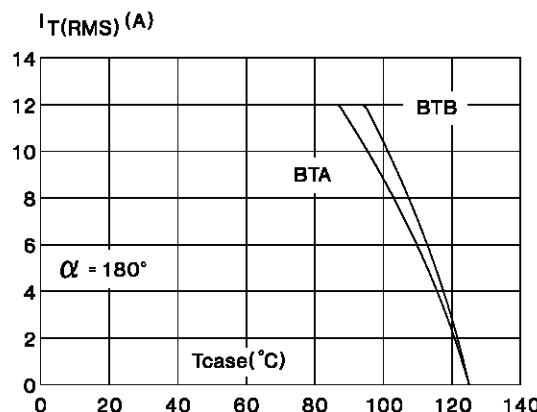
**Fig.3 :** Correlation between maximum RMS power dissipation and maximum allowable temperatures ( $T_{\text{amb}}$  and  $T_{\text{case}}$ ) for different thermal resistances heatsink + contact (BTB).



**Fig.2 :** Correlation between maximum RMS power dissipation and maximum allowable temperatures ( $T_{\text{amb}}$  and  $T_{\text{case}}$ ) for different thermal resistances heatsink + contact (BTA).

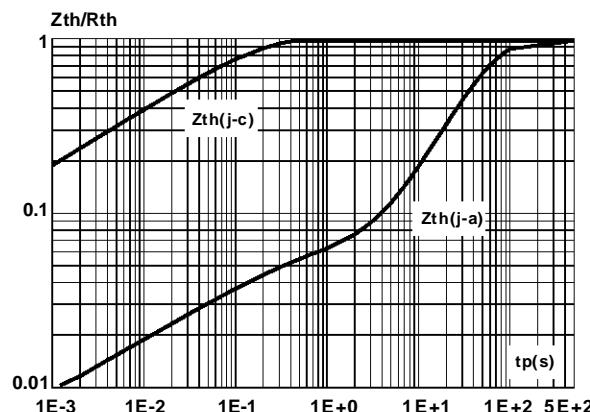


**Fig.4 :** RMS on-state current versus case temperature.

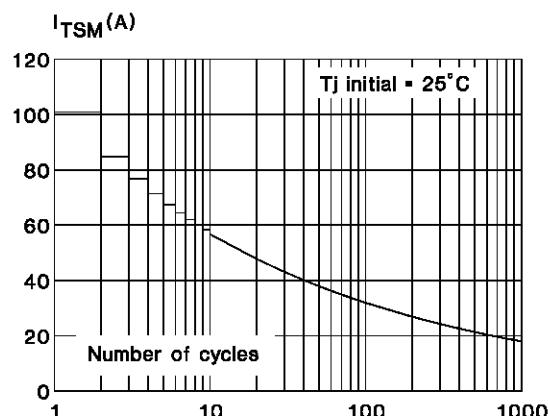


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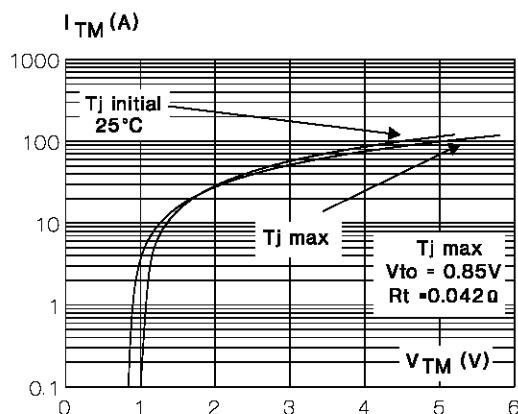
**Fig.5 :** Relative variation of thermal impedance versus pulse duration.



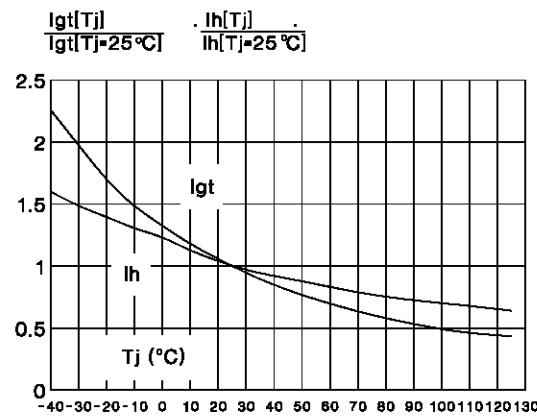
**Fig.7 :** Non Repetitive surge peak on-state current versus number of cycles.



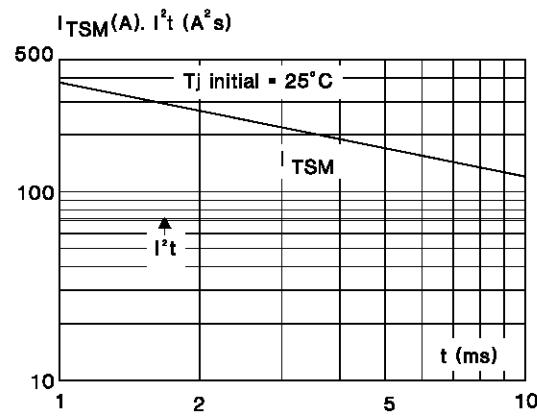
**Fig.9 :** On-state characteristics (maximum values).



**Fig.6 :** Relative variation of gate trigger current and holding current versus junction temperature.



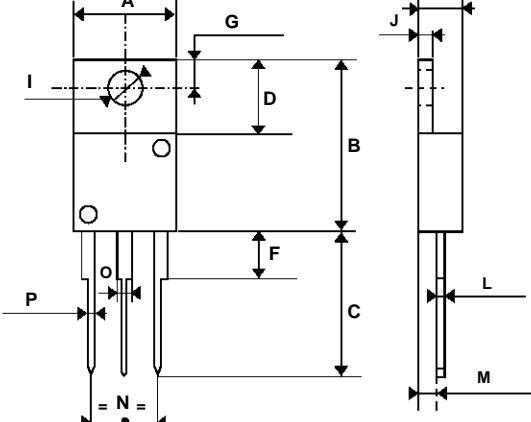
**Fig.8 :** Non repetitive surge peak on-state current for a sinusoidal pulse with width :  $t \leq 10\text{ms}$ , and corresponding value of  $I^2t$ .



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### PACKAGE MECHANICAL DATA

TO220AB Plastic



REF.	DIMENSIONS			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	10.20	10.50	0.401	0.413
B	14.23	15.87	0.560	0.625
C	12.70	14.70	0.500	0.579
D	5.85	6.85	0.230	0.270
F		4.50		0.178
G	2.54	3.00	0.100	0.119
H	4.48	4.82	0.176	0.190
I	3.55	4.00	0.140	0.158
J	1.15	1.39	0.045	0.055
L	0.35	0.65	0.013	0.026
M	2.10	2.70	0.082	0.107
N	4.58	5.58	0.18	0.22
O	0.80	1.20	0.031	0.048
P	0.64	0.96	0.025	0.038

Cooling method : C

Marking : type number

Weight : 2.3 g

Recommended torque value : 0.8 m.N.

Maximum torque value : 1 m.N.

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