

TOSHIBA**TA8428K(S)/F**

TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

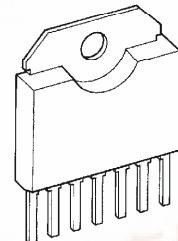
TA8428K (S), TA8428F**FULL BRIDGE DRIVER**

The TA8428K (S), TA8428F is Full Bridge Driver IC for Brush Motor Rotation Control.

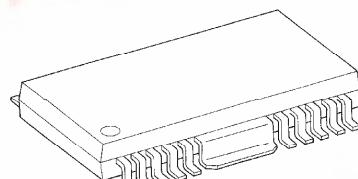
Forward Rotation, Reverse Rotation, Stop and Braking operations are available.

Thermal Shutdown and Short Current Protector are provided.

TA8428K (S)

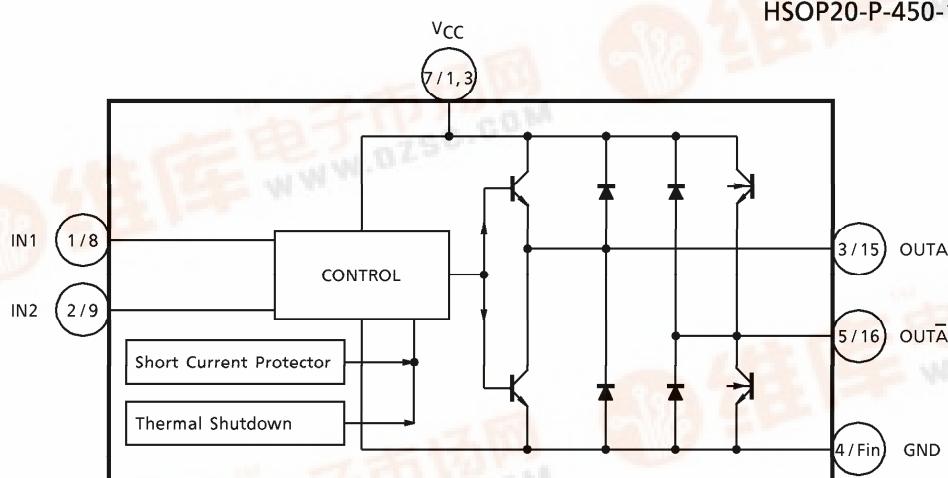


HSIP7-P-2.54



HSOP20-P-450-1.00

Weight
HSIP7-P-2.54 : 1.88 g (Typ.)
HSOP20-P-450-1.00 : 0.79 g (Typ.)

BLOCK DIAGRAM

TA8428K (S)
TA8428F

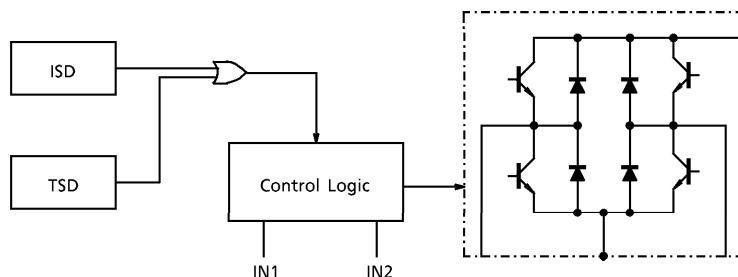
980910EBA2

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PIN FUNCTION

PIN No.		SYMBOL	FUNCTIONAL DESCRIPTION
K (S)	F		
1	8	IN1	TTL compatible control inputs.
2	9	IN2	(PNP type low active comparator inputs)
3	15	OUTA	Output terminals and free wheeling diodes are connected between each output to GND and V _{CC} .
4	Fin	GND	GND terminal
5	16	OUTĀ	Output terminals and free wheeling diodes are connected between each output to GND and V _{CC} .
6	Other pin	N.C	Non connection
7	1, 3	V _{CC}	Supply voltage terminal for control and motor drive.

TA8428K (S), TA8428F has 2 build-in protective functions which work independently. These circuit operations are as follows.



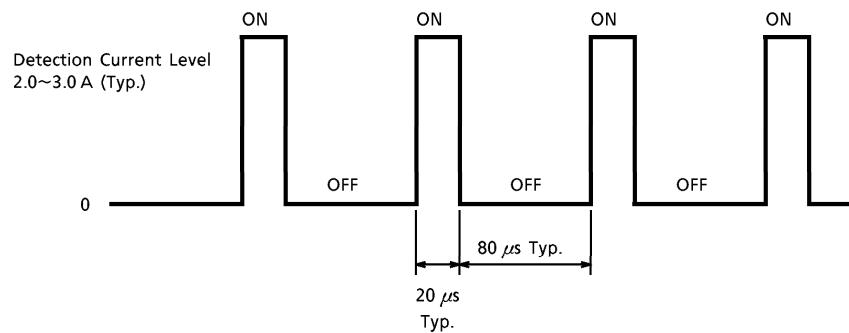
- Thermal shutdown (TSD)

If junction temperature of TA8428K (S), TA8428F is over the specified temperature (150°C Typ.) by excess power dissipation or abnormal ambient temperature change, thermal Shutdown circuit turn "ON" and output 4 transistors become High impedance. (All transistors turn "OFF")

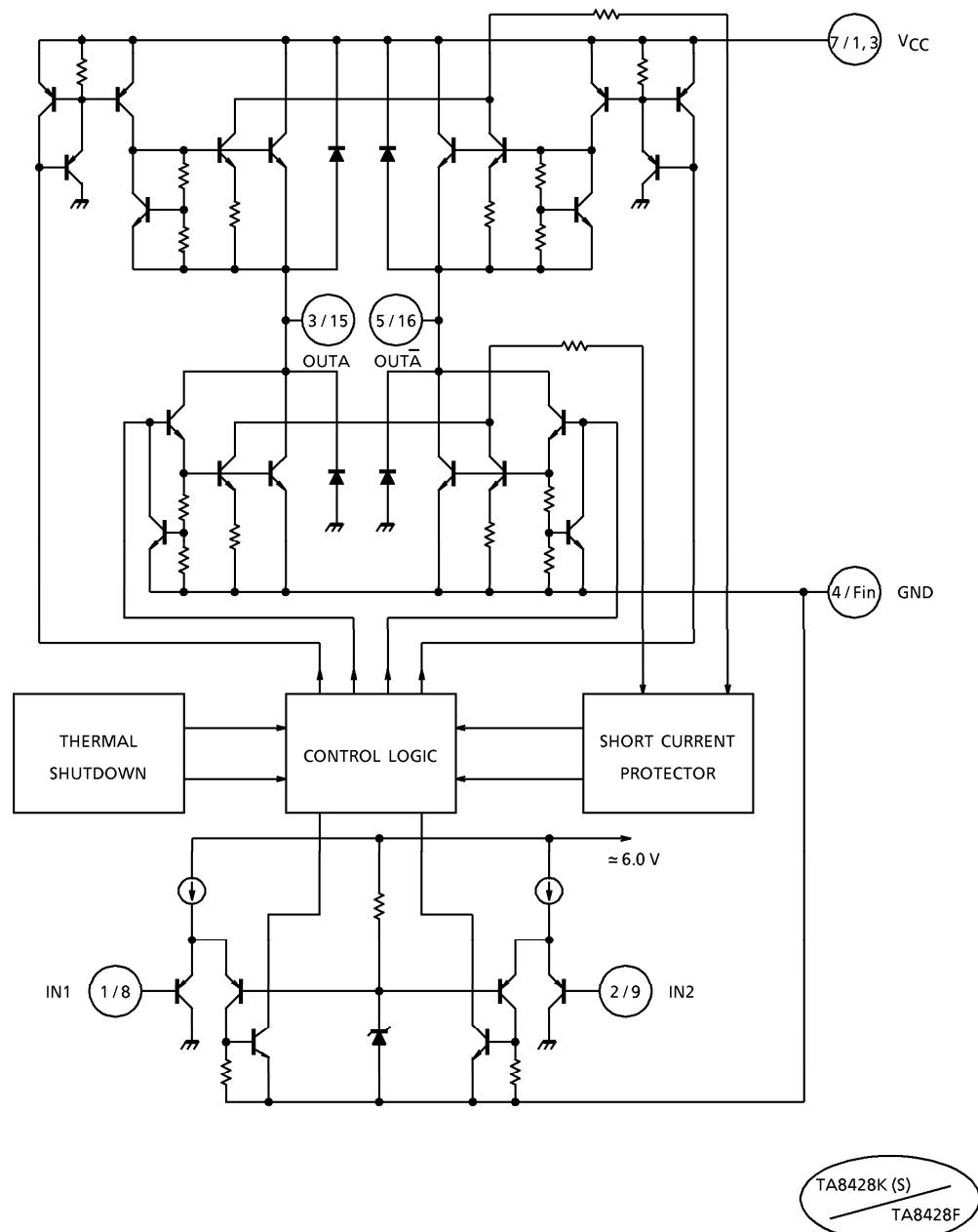
- Short current protector (ISD)

Short current protector circuit senses all output transistor current. If output transistor current is over the specified limiting current value (2.0~3.0 A Typ.), short current protector operates and all output transistors periodically turn "OFF" (High Impedance Mode) in a period of approximately 80 μ s.

This state is continued until the release of over current mode.



INTERNAL CIRCUIT



TA8428K (S)
TA8428F

MAXIMUM RATINGS ($T_a = 25^\circ\text{C}$)

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Supply Voltage		V_{CC}	30	V	
Input Voltage		V_{IN}	$-0.3 \sim V_{CC}$	V	
Output Current	K(S) type	PEAK	I_O (PEAK)	3.0 (Note 1)	
		AVE.	I_O (AVE.)	1.5	
	F type	PEAK	I_O (PEAK)	2.4 (Note 1)	
		AVE.	I_O (AVE.)	0.8	
Power Dissipation	K(S) type	P_D	1.25 (Note 2)	W	
			10.0 (Note 3)		
	F type	P_D	1.9 (Note 4)		
			2.5 (Note 5)		
Operating Temperature		T_{opr}	$-30 \sim 85$	$^\circ\text{C}$	
Storage Temperature		T_{stg}	$-55 \sim 150$	$^\circ\text{C}$	

(Note 1) : $t = 100 \text{ ms}$

(Note 2) : No heat sink

(Note 3) : $T_c = 85^\circ\text{C}$ (Note 4) : This value is obtained by $30 \times 30 \times 1.6\text{mm}$ PCB mounting occupied copper area in excess of 60%(Note 5) : This value is obtained by $50 \times 50 \times 1.6\text{mm}$ PCB mounting occupied copper area in excess of 60%ELECTRICAL CHARACTERISTICS ($V_{CC} = 24 \text{ V}$, $T_a = 25^\circ\text{C}$)

CHARACTERISTIC		SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Quiescent Current	I_{CC1}	1	Stop mode		—	8	15	mA	
	I_{CC2}		Forward / reverse mode		—	35	85		
	I_{CC3}		Brake mode		—	16	30		
Input Voltage	V_{IL}	2	—		—	—	0.8	V	
	V_{IH}		—		2.0	—	—		
Input Current	I_{IL}	2	$V_{IN} = \text{GND}$		—	—	50	μA	
	I_{IH}		$V_{IN} = V_{CC}$		—	—	10		
Output Saturation Voltage	K(S) type	V_{sat} (total)	3	$I_O = 1.5 \text{ A}$, $T_c = 25^\circ\text{C}$		—	2.2	2.9	
				$I_O = 0.8 \text{ A}$, $T_c = 25^\circ\text{C}$		—	1.8	2.5	
Output Leakage Current		I_{LU}	4	$V_L = 25 \text{ V}$	—	—	50	μA	
		I_{LL}			—	—	50		
Diode Forward Voltage	K(S) type	I_{LU}	4	$I_F = 1.5 \text{ A}$	—	2.6	—	V	
		I_{LL}			—	1.5	—		
	F type	I_{LU}		$I_F = 0.8 \text{ A}$	—	2.2	—		
		I_{LL}			—	1.2	—		
Thermal Shutdown Operating Temperature		T_{SD}	—	—	—	150	—	$^\circ\text{C}$	
Propagation Delay Time		t_{pLH}	2	—	—	1	—	μs	
		t_{pHL}	2	—	—	1	—		

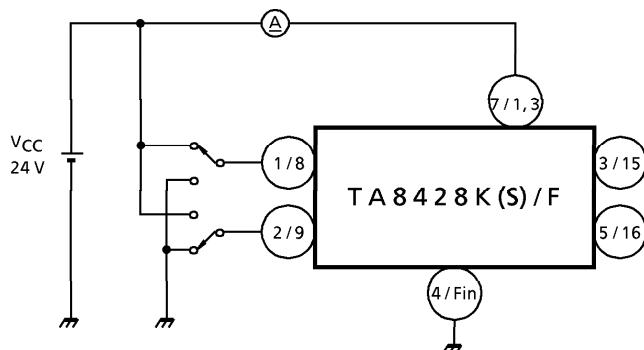
FUNCTION

INPUT		OUTPUT		MODE
IN1	IN2	OUTA	OUTĀ	
H	H	L	L	Brake
L	H	L	H	CW / CCW
H	L	H	L	CCW / CW
L	L	OFF (high impedance)		Stop

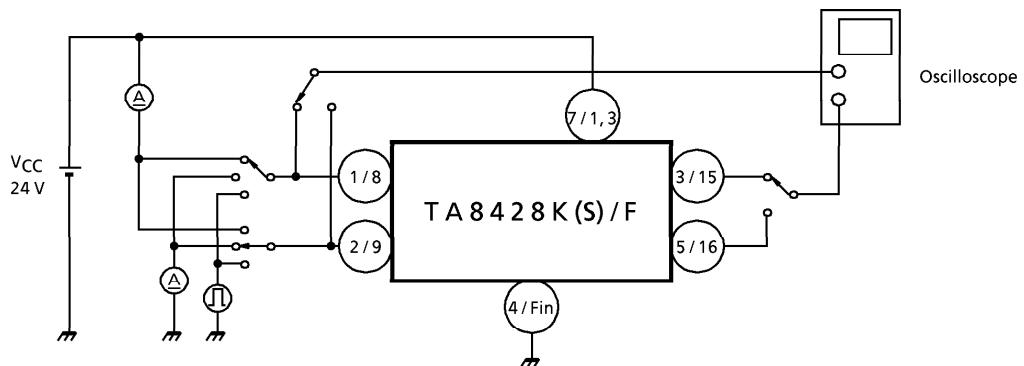
(Note) : PIN ⑥ is non connection.

(Note) : Heat fin is connected with GND with low impedance.

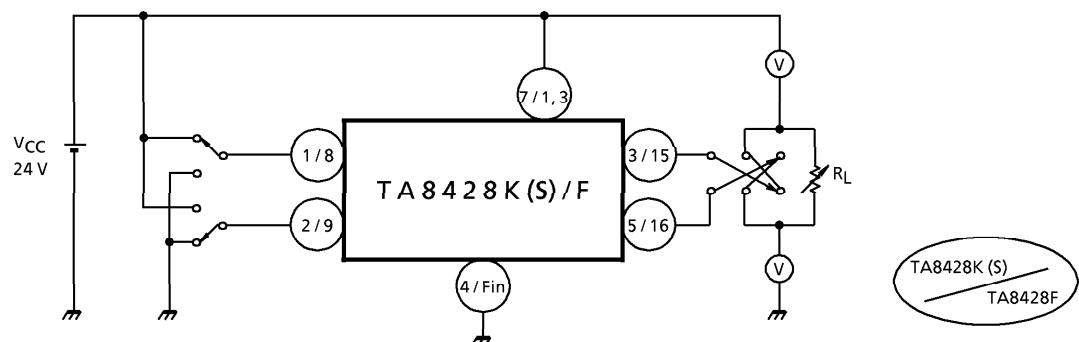
TEST CIRCUIT 1.

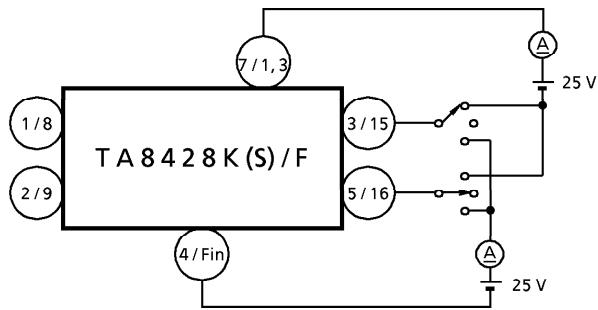
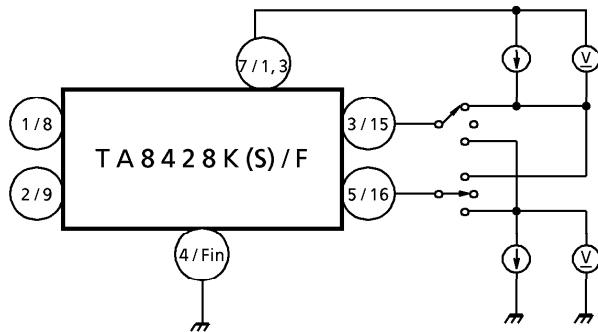
 I_{CC1} , I_{CC2} , I_{CC3} 

TEST CIRCUIT 2.

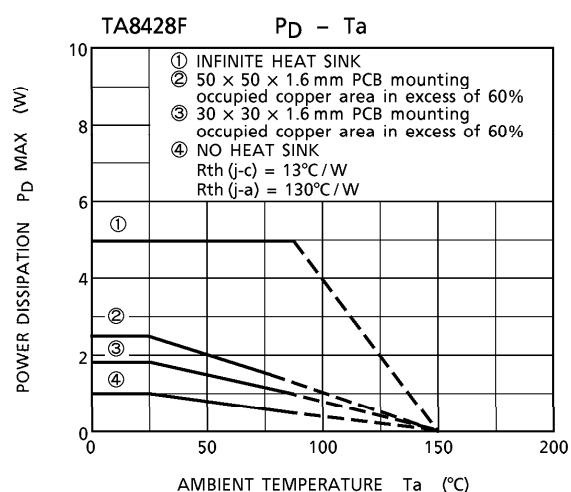
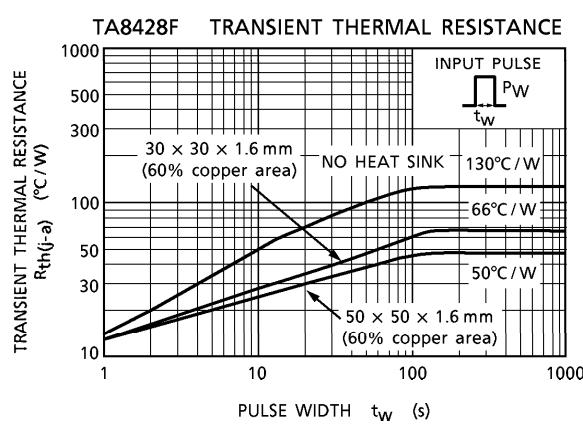
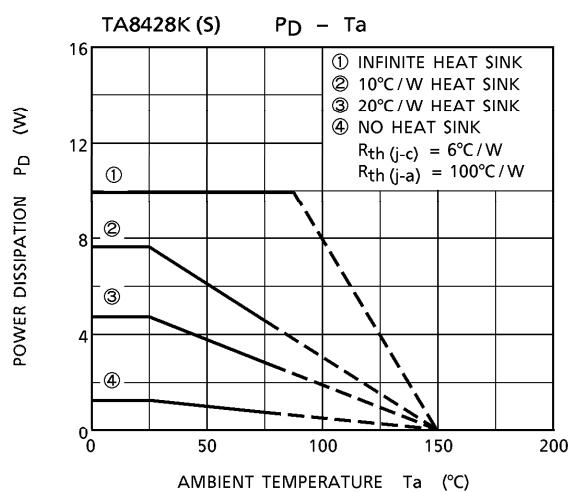
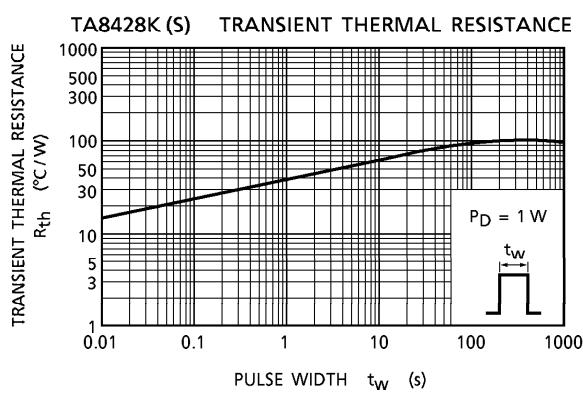
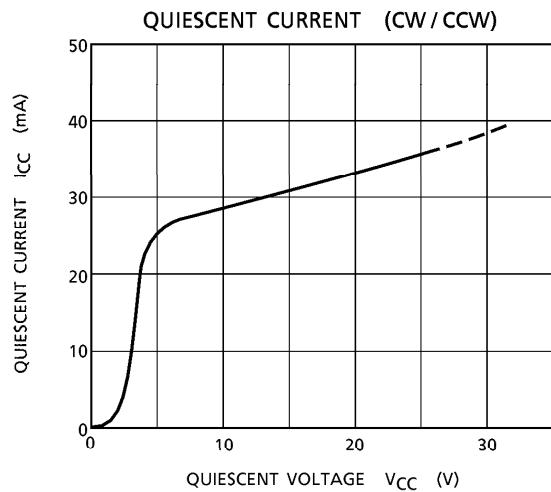
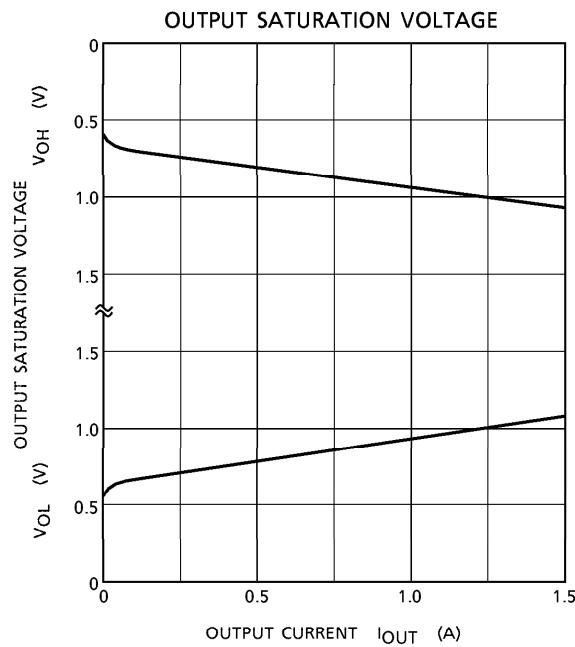
 V_{IL} , V_{IH} , I_{IL} , I_{IH} , t_{pLH} , t_{pHL} 

TEST CIRCUIT 3.

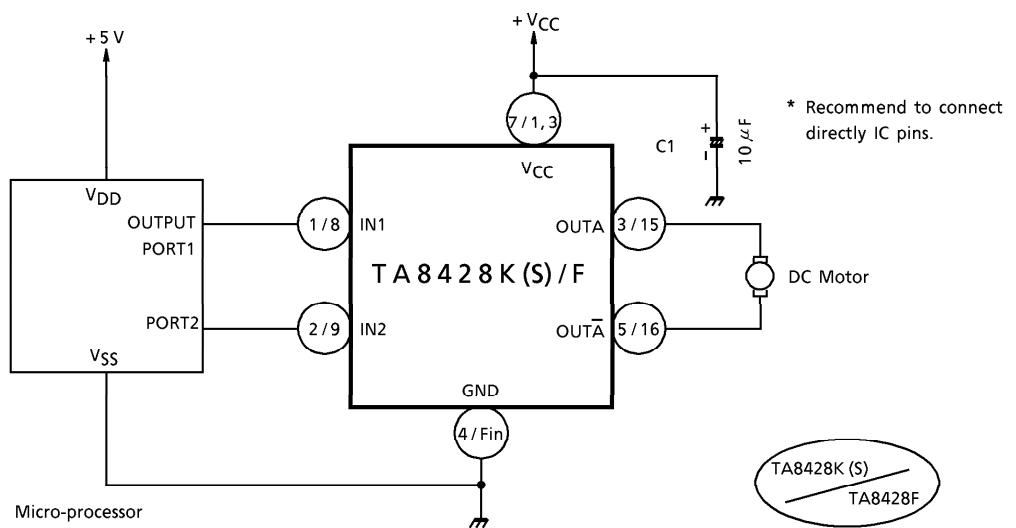
 V_{sat} 

TEST CIRCUIT 4. I_{LH}, I_{LL} **TEST CIRCUIT 5.** V_{FU}, V_{FL} 

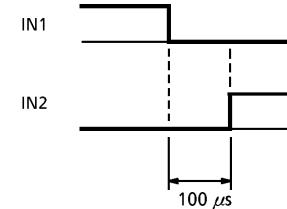
TA8428K (S)
/ TA8428F



APPLICATION CIRCUIT



(Note) : Recommend to take approximately $100 \mu s$ of input dead time for reliable operations.



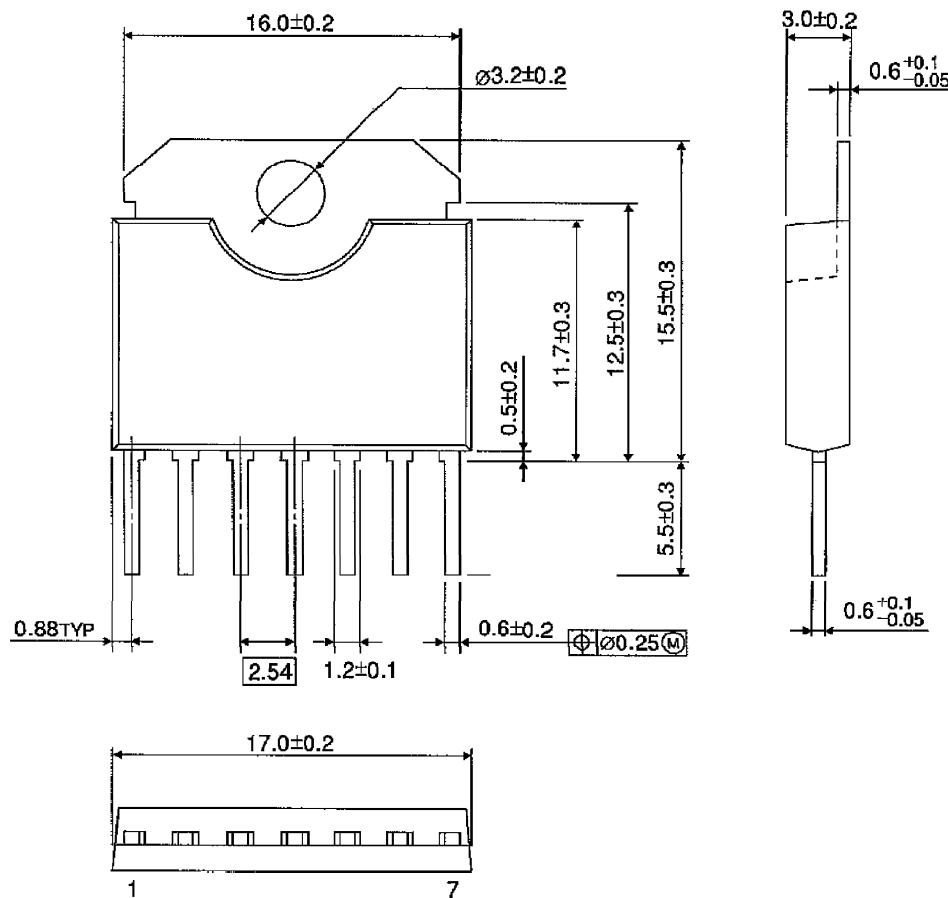
(Note) : Utmost care is necessary in the design of the output line, V_{CC} and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

(Note) : In case of mounted on radiators, do not use silicon rubber. (TA8428K(S))

(Note) : Connect and use 1 pin and 3 pin surely. (TA8428F)

PACKAGE DIMENSIONS
HSIP7-P-2.54

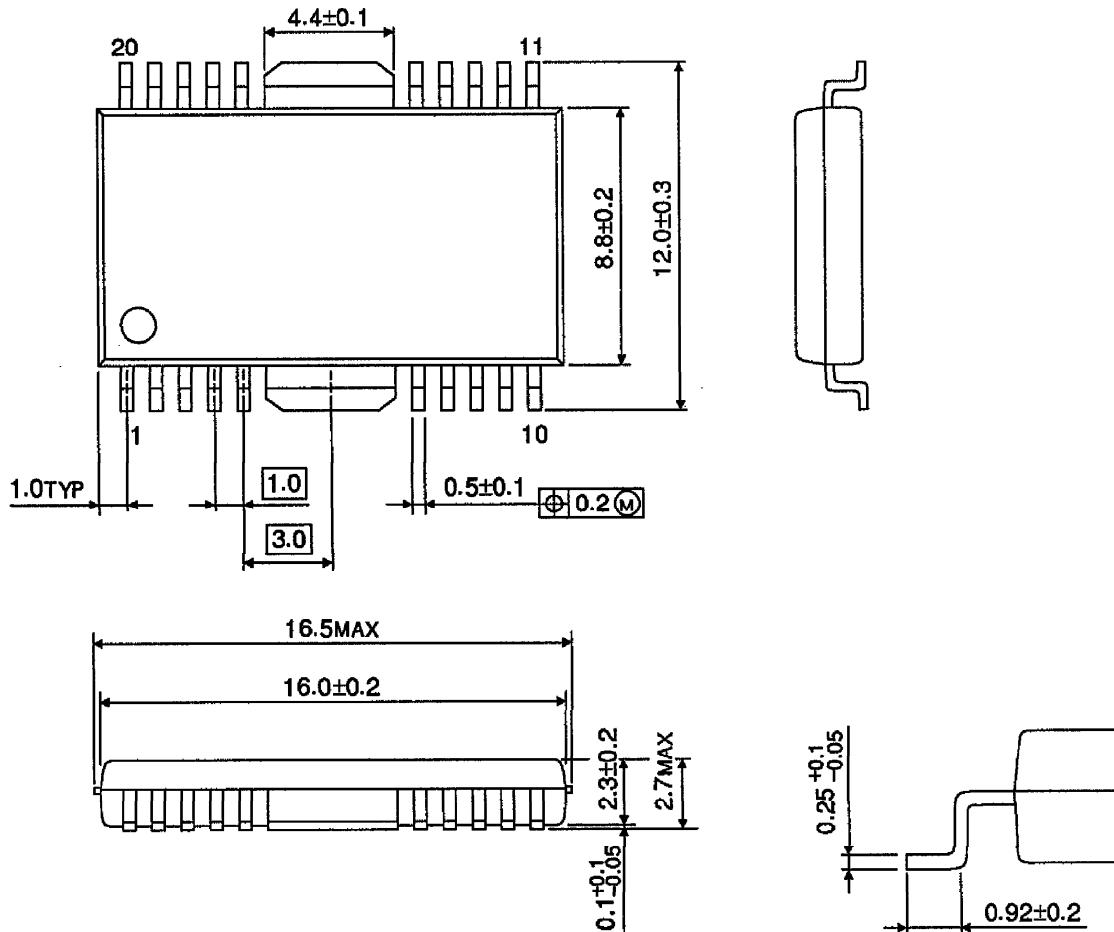
Unit : mm



Weight : 1.88 g (Typ.)

PACKAGE DIMENSIONS
HSOP20-P-450-1.00

Unit : mm



Weight :