Inverting 1.4MHz Switching Regulator in 5-Lead SOT-23

October 1998

FEATURES

- Better Regulation Than a Charge Pump
- Effective Output Impedance: 0.1Ω
- -5V at 150mA from a 5V Input
- Uses Tiny Capacitors and Inductor
- Internally Compensated
- Fixed Frequency 1.4MHz Operation
- Low Shutdown Current: <1µA
- Low V_{CFSAT} Switch: 300mV at 300mA
- 5-Lead SOT-23 Package

APPLICATIONS

- Digital Camera CCD Bias
- MR Head Bias
- LCD Bias
- Positive-to-Negative Conversion
- GaAs FET Bias

DESCRIPTION

The LT®1611 is the industry's first inverting 5-lead SOT-23 current mode DC/DC converter. Intended for use in small, low power applications, it operates from an input voltage as low as 1.1V and switches at 1.4MHz, allowing the use of tiny, low cost capacitors and inductors 2mm or less in height. Its small size and high switching frequency enable the complete DC/DC converter function to take up less than 0.25 square inches of PC board area. Capable of generating -5V at 150mA from a 5V supply or -5V at 100mA from a 3V supply, the LT1611 replaces nonregulated "charge pump" solutions in many applications.

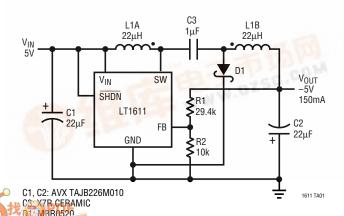
The LT1611 operates in a dual inductor inverting topology which filters the input side as well as the output side of the DC/DC converter. Fixed frequency switching ensures a clean output free from low frequency noise typically present with charge pump solutions. No load quiescent current of the LT1611 is 3mA, while in shutdown quiescent current drops to 0.5 μ A. The 36V switch allows V_{IN} to V_{OUT} differential of up to 33V.

The LT1611 is available in the 5-lead SOT-23 package.

LT, LTC and LT are registered trademarks of Linear Technology Corporation.

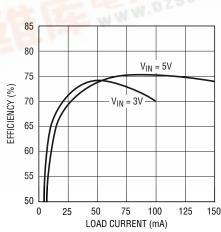
TYPICAL APPLICATION

5V to -5V, 150mA Inverting DC/DC Converter



S62-220 OR 2× MURATA LQH3C220 (UNCOUPLED)

Efficiency

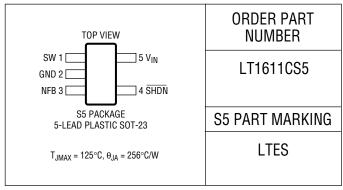


ABSOLUTE MAXIMUM RATINGS

(Note 1)

(
V _{IN} Voltage	10V
SW Voltage	
NFB Voltage	3V
Current into NFB Pin	±1mA
SHDN Voltage	10V
Maximum Junction Temperature	125°C
Operating Temperature Range	
Commercial	0°C to 70°C
Extended Commercial (Note 2)	40°C to 85°C
Storage Temperature Range	65°C to 150°C
Lead Temperature (Soldering, 10 sec)	300°C

PACKAGE/ORDER INFORMATION



Consult factory for Industrial and Military grade parts.

ELECTRICAL CHARACTERISTICS

 V_{IN} = 1.5V, $V_{\overline{SHDN}}$ = V_{IN} , T_A = 25°C, unless otherwise noted.

PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
Minimum Operating Voltage				0.9	1.1	V
Maximum Operating Voltage					10	V
NFB Pin Bias Current	V _{NFB} = −1.23V	•	-2.7	-4.7	-6.7	μΑ
Feedback Voltage		•	-1.205	-1.23	-1.255	V
Quiescent Current	V _{SHDN} = 1.5V, Not Switching			3	4.5	mA
Quiescent Current in Shutdown	$V_{\overline{SHDN}} = 0V, V_{\overline{IN}} = 2V$ $V_{\overline{SHDN}} = 0V, V_{\overline{IN}} = 5V$			0	0.5 1.0	μA μA
Reference Line Regulation	1.5V ≤ V _{IN} ≤ 10V			0.02	0.2	%/V
Switching Frequency		•	1.0	1.4	1.8	MHz
Maximum Duty Cycle		•	82	86		%
Switch Current Limit	(Note 3)		550	800		mA
Switch V _{CESAT}	I _{SW} = 300mA			300	350	mV
Switch Leakage Current	V _{SW} = 5V			0.01	1	μΑ
SHDN Input Voltage High			1			V
SHDN Input Voltage Low					0.3	V
SHDN Pin Bias Current	V _{SHDN} = 3V V _{SHDN} = 0V			25 0	50 0.1	μA μA

The ● denotes specifications which apply over the specified temperature range

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: C grade device specifications are guaranteed over the 0°C to 70°C temperature range. In addition, C grade device specifications are assured

over the -40°C to 85°C temperature range by design or correlation, but are not production tested.

Note 3: Current limit guaranteed by design and/or correlation to static test.

PIN FUNCTIONS

SW (Pin 1): Switch Pin. Minimize trace area at this pin to keep EMI down.

GND (Pin 2): Ground. Tie directly to local ground plane.

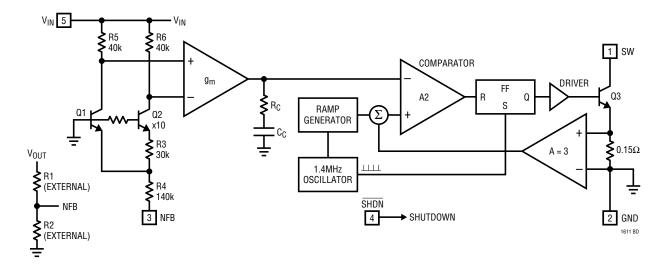
NFB (Pin 3): Negative Feedback Pin. Minimize trace area. Reference voltage is –1.23V. Connect resistive divider tap here. The suggested value for R2 is 10k. Set R1 and R2 according to:

R1 =
$$\frac{\left|V_{0UT}\right| - 1.23}{\frac{1.23}{B2} + \left(4.5 \cdot 10^{-6}\right)}$$

SHDN (**Pin 4**): Shutdown Pin. Tie to 1V or more to enable device. Ground to shut the device down.

V_{IN} (**Pin 5**): Input Supply Pin. Must be locally bypassed.

BLOCK DIAGRAM



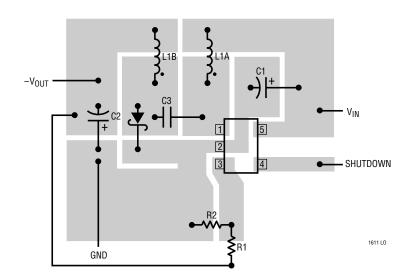
PACKAGE DESCRIPTION Dimensions in inches (millimeters) unless otherwise noted.

5-Lead Plastic SOT-23 (LTC DWG # 05-08-1633) 2.60 - 3.00(0.102 - 0.118)2.80 - 3.001.50 - 1.75(0.110 - 0.118) $\frac{0.00 - 0.15}{(0.00 - 0.006)}$ 0.90 - 1.45(0.059 - 0.069)(NOTE 3) $\overline{(0.035 - 0.057)}$ 0.10 - 0.600.35 - 0.500.90 - 1.30 $\overline{(0.004 - 0.024)}$ 0.09 - 0.20 0.95 $\overline{(0.035 - 0.051)}$ $\overline{(0.004 - 0.008)}$ (0.014 - 0.020)REF (0.037)FIVE PLACES (NOTE 2) (NOTE 2) (0.074)REF NOTE: REF 1. DIMENSIONS ARE IN MILLIMETERS S5 S0T-23 0797

S5 Package

- 2. DIMENSIONS ARE INCLUSIVE OF PLATING
- 3. DIMENSIONS ARE EXCLUSIVE OF MOLD FLASH AND METAL BURR
- 4. MOLD FLASH SHALL NOT EXCEED $0.254 \mathrm{mm}$
- 5. PACKAGE EIAJ REFERENCE IS SC-74A (EIAJ)

SUGGESTED LAYOUT



RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LT1307	Single Cell Micropower DC/DC with Low Battery Detector	3.3V/75mA from 1V; 600kHz Fixed Frequency
LT1316	Burst Mode [™] Operation DC/DC with Programmable Current Limit	1.5V Minimum V _{IN} ; Precise Control of Peak Switch Current
LT1317	2-Cell Micropower DC/DC with Low Battery Detector	3.3V/200mA from Two Cells; 600kHz Fixed Frequency
LT1370	500kHz High Efficiency DC/DC Converter	42V, 6A Internal Switch, Negative Feedback Regulation
LT1371	500kHz High Efficiency DC/DC Converter	42V, 3A Internal Switch, Negative Feedback Regulation
LT1610	Single Cell Micropower DC/DC	3V/30mA from 1V; 1.7MHz Fixed Frequency
LT1614	Inverting Mode Switching Regulator with Low-Battery Detector	-5V at 200mA from 5V Input in MSOP

Burst Mode is a trademark of Linear Technology Corporation.