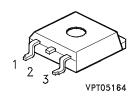
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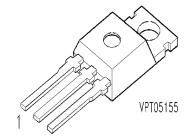
Target data sheet

SPPX2N60S5 SPBX2N60S5

Cool MOSa Power Transistor

- N-Channel
- Enhancement mode
- Ultra low gate charge
- Avalanche rated
- dv/dt rated
- 150°C operating temperature





1	2	3
G	D	S

Туре	V _{DS}	I _D	R _{DS(on)}	Marking	Package	Ordering Code
SPPX2N60S5	600 V	11.3 A	380 m Ω	X2N60S5	P-TO220-3-1	-
SPBX2N60S5					P-TO263-3-2	-

Maximum Ratings, at $T_i = 25$ °C, unless otherwise specified

Parameter	Symbol	Value	Unit
Drain source voltage	V _{DSS}	600	V
Continuous drain current	I _D		А
$T_{\rm C}$ = 25 °C		11.3	
$T_{\rm C} = 100 {}^{\circ}{\rm C}$		7.1	
Pulsed drain current	I _{D puls}	22.6	
$T_{\rm C}$ = 25 °C			
Avalanche energy, single pulse	E _{AS}	340	mJ
$I_{\rm D} = 11.3 \text{ A}, \ V_{\rm DD} = 50 \text{ V}, \ R_{\rm GS} = 25 \ \Omega$			
Avalanche current (periodic, limited by T_{jmax})	I _{AR}	tbd	А
Avalanche energy (10 kHz, limited by T_{jmax})	E _{AR}	tbd	mJ
Reverse diode dv/dt	d <i>v</i> /d <i>t</i>	6	KV/µs
$I_{S} = 11.3 \text{ A}, \ V_{DS} < V_{DSS}, \ di/dt = 100 \ A/\mu s,$			
T _{jmax} = 150 °C			
Gate source voltage	V_{GS}	±20	V
Power dissipation, $T_C = 25 ^{\circ}C$	P _{tot}	125	W
Operating temperature	T _j	-55+150	°C
Storage temperature	$T_{ m stg}$	-55 + 150	
IEC climatic category; DIN IEC 68-1		55/150/56	

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Electrical Characteristics

Parameter	Symbol	Values			Unit
at T_i = 25 °C, unless otherwise specified		min.	typ.	max.	
Thermal Characteristics	•		•	•	•
Thermal resistance, junction - case	R_{thJC}	-	-	1	K/W
Thermal resistance, junction - ambient	R_{thJA}	-	62	-	
(Leaded and through-hole packages)					
SMD version, device on PCB:	R_{thJA}				
@ min. footprint		-	tbd	-	
@ 6 cm ² cooling area ¹⁾		-	35	_	
Static Characteristics					
Drain- source breakdown voltage	V _{(BR)DSS}	600	_	-	V
$V_{GS} = 0 \text{ V}, I_{D} = 0.25 \text{ mA}$					
Gate threshold voltage, $V_{GS} = V_{DS}$	V _{GS(th)}				
$I_{\rm D} = 0.5 \; {\rm mA}, \; T_{\rm j} = 25 \; {\rm ^{\circ}C}$		3.5	4.5	5.5	
$I_{\rm D} = 0.5 \; {\rm mA}, \; T_{\rm j} = 150 \; {\rm ^{\circ}C}$		tbd	-	-	
Zero gate voltage drain current, V _{DS} =V _{DSS}	I _{DSS}				μΑ
$V_{GS} = 0 \text{ V}, T_j = -40 ^{\circ}\text{C}$		-	_	0.1	
$V_{GS} = 0 \text{ V}, T_j = 25 ^{\circ}\text{C}$		-	0.5	1	
$V_{GS} = 0 \text{ V}, T_{j} = 150 \text{ °C}$		-	_	tbd	
Gate-source leakage current	I _{GSS}	-	10	100	nA
$V_{GS} = 20 \text{ V}, \ V_{DS} = 0 \text{ V}$					
Drain-Source on-state resistance	R _{DS(on)}	-	tbd	380	mΩ
$V_{GS} = 10 \text{ V}, I_D = 7.1 \text{ A}$					

¹ Device on 50mm*50mm*1.5mm epoxy PCB FR4 with 6 cm2 (one layer, 70μm thick) copper area for drain connection. PCB is vertical without blown air.

Electrical Characteristics

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Parameter	Symbol	Values			Unit
at $T_j = 25$ °C, unless otherwise specified		min.	typ.	max.	1
Characteristics		•	•	,	•
Transconductance	<i>g</i> fs	-	tbd	-	S
$V_{DS} \ge 2 * I_D * R_{DS(on)max}$, $I_D = 7.1 A$					
Input capacitance	C_{iss}	-	1500	tbd	pF
$V_{GS} = 0 \text{ V}, \ V_{DS} = 25 \text{ V}, \ f = 1 \text{ MHz}$					
Output capacitance	C _{oss}	_	960	tbd	
$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$					
Reverse transfer capacitance	C _{rss}	-	50	tbd	
$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$					
Turn-on delay time	$t_{d(on)}$	-	tbd	tbd	ns
$V_{\text{DD}} = 350 \text{ V}, \ V_{\text{GS}} = 10 \text{ V}, \ I_{\text{D}} = 11.3 \text{ A},$					
$R_{\rm G} = 6.8 \Omega$					
Rise time	t _r	-	tbd	-	
$V_{\text{DD}} = 350 \text{ V}, \ V_{\text{GS}} = 10 \text{ V}, \ I_{\text{D}} = 11.3 \text{ A},$					
$R_{\rm G} = 6.8 \ \Omega$					
Turn-off delay time	t _{d(off)}	-	tbd	tbd	
$V_{\text{DD}} = 350 \text{ V}, \ V_{\text{GS}} = 10 \text{ V}, \ I_{\text{D}} = 11.3 \text{ A},$					
$R_{\rm G} = 6.8 \ \Omega$					
Fall time	t _f	-	tbd	-	
$V_{\text{DD}} = 350 \text{ V}, \ V_{\text{GS}} = 10 \text{ V}, \ I_{\text{D}} = 11.3 \text{ A},$					
$R_{\rm G} = 6.8 \ \Omega$					

Electrical Characteristics

Parameter	Symbol	Values		Unit	
at $T_j = 25$ °C, unless otherwise specified		min.	typ.	max.	
Gate Charge Characteristics			•	•	
Gate-source charge	Q _{gs}	-	tbd	-	nC
$I_{D} = 11.3 \text{ A}, \ V_{DD} = 400 \text{ V}$					
Gate-drain charge	Q _{gd}	-	tbd	-	
$I_D = 11.3 \text{ A}, \ V_{DD} = 400 \text{ V}$					
Total gate charge	Q _G	-	50	tbd	
$V_{\rm DD} = 400 \text{ V}, I_{\rm D} = 11.3 \text{ A}, V_{\rm GS} = 0 \text{ to } 10 \text{ V}$					

Reverse Diode

Continuous source current $T_{\rm C}$ = 25 °C	Is	-	-	11.3	А
Pulsed source current	/ _{SM}	-	-	22.6	
$T_{\rm C}$ = 25 °C					
Inverse diode forward voltage	V _{SD}	-	tbd	1.2	V
$V_{GS} = 0 \text{ V}, I_{F} = 11.3 \text{ A}$					
Reverse recovery time	t _{rr}	-	tbd	-	ns
$V_{R} = 100 \text{ V}, I_{F}=I_{S}, di_{F}/dt = 100 \text{ A/}\mu\text{s}$					
Reverse recovery charge	Q _{rr}	-	tbd	-	μC
$V_{R} = 100 \text{ V}, I_{F}=I_{S}, di_{F}/dt = 100 \text{ A/}\mu\text{s}$					

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