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SGD512/D
Rev. 3, Oct-2003

ON Semiconductor

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MiniGate™ Logic from ON Semiconductor
Quick Reference Card

		One-Gates					Two-Gates	Three-Gates
Device Funct	Description	HC MC74HC1GYYDXXX	VHC (AHC) MC74VHC1GYYDXXX	VHCT (AHCT) MC74VHC1GTYYDXXX	LCX (LVC) NL17SZYYXXXX	Ultra LVLCX NLSVYYYXXXX	LCX (LVC) NL27WZYYXXXX	LCX (LVC) NL37WZYYXXXX
00	2-Input NAND	DTT1, DFT2	DTT1, DFT2	DTT1, DFT2	DFT2, XV5T2	XV5T2	US	–
01	2-Input NAND OD*	–	DTT1, DFT2	–	–	–	–	–
02	2-Input NOR	DTT1, DFT2	DTT1, DFT2	DTT1, DFT2	DFT2, XV5T2	XV5T2	US	–
03	2-Input NOR OD*	–	DTT1, DFT2	–	–	–	–	–
04	Inverter	DTT1, DFT2	DTT1, DFT2	DTT1, DFT2	DFT2, XV5T2	XV5T2	DTT1, DFT2	US
U04	Inverter, Unbuffered	DTT1, DFT2	DTT1, DFT2	–	DFT2, XV5T2	–	DTT1, DFT2	–
05	Inverter OD*	–	DTT1, DFT2	–	See "06"	–	See "06"	See "06"
06	Inverter OD*	–	–	–	DFT2, XV5T2	–	DTT1, DFT2	US
07	Buffer OD*	–	DTT1, DFT2	–	DFT2, XV5T2	–	DTT1, DFT2	US
08	2-Input AND	DTT1, DFT2	DTT1, DFT2	DTT1, DFT2	DFT2, XV5T2	XV5T2	US	–
09	2-Input AND OD*	–	DTT1, DFT2	–	–	–	–	–
14	Inverter-Schmitt	DTT1, DFT2	DTT1, DFT2	DTT1, DFT2	DFT2, XV5T2	–	DTT1, DFT2	US
16	Buffer	–	See "50"	–	DFT2, XV5T2	XV5T2	DTT1, DFT2	US
17	Buffer-Schmitt	–	–	–	DFT2, XV5T2	–	DTT1, DFT2	US
18	1-of-2 Demux, 3-State	–	–	–	–	–	DFT2**	–
19	1-of-2 Demux	–	–	–	–	–	DFT2**	–
32	2-Input OR	DTT1, DFT2	DTT1, DFT2	DTT1, DFT2	DFT2, XV5T2	XV5T2	US	–
34	Buffer	–	–	–	See "16"	–	See "16"	See "16"
50	Buffer	–	DTT1, DFT2	DTT1, DFT2	See "16"	–	See "16"	See "16"
66	Analog Switch	–	DTT1, DFT2	DTT1, DFT2	–	–	NLAS323US	–
70	Buffer	–	See "1G50"	See "1GT50"	See "16"	–	See "16"	See "16"
74	D Flip-Flop	–	–	–	US	–	See "SZ74"	–
86	2-Input XOR	–	DTT1, DFT2	DTT1, DFT2	DFT2	–	US	–
125	3-State Non-Inv Buffer, Low Enable	–	DTT1, DFT2	DTT1, DFT2	DFT2, XV5T2	–	US	–
126	3-State Non-Inv Buffer, High Enable	–	DTT1, DFT2	DTT1, DFT2	DFT2, XV5T2	–	US	–
132	2-Input NAND, Schmitt	–	DTT1, DFT2	–	–	–	–	–
135	2-Input NAND, Schmitt OD*	–	DTT1, DFT2	–	–	–	–	–

DTT1 = TSOP-5, -6 (SOT-23); **DFT2** = SC-88, SC-88A (SC-70, SOT-353, SOT-363); **XV5T2** = SOT-553; **US** = Ultra-Small 8
* Open Drain

For up-to-date technical data sheets or additional information on these devices, please visit our website at www.onsemi.com/minigates. A CDROM containing the complete technical library for our MiniGate portfolio, including data sheets and application notes, is available from our Literature Distribution Center at 1-800-344-3860 (USA/Canada); or +1 303-675-2175; or via email at orderlit@onsemi.com. Request document number **MINIGATECD/D**.

Analog Switches
from ON Semiconductor

Function	Device Number	Package(s)	Comments
1-Single Pole, Single Throw Switch	MC74VHC1G66	DTT1, DFT2	–
1-Single Pole, Single Throw Switch	MC74VHC1GT66	DTT1, DFT2	–
1-Single Pole, Single Throw Switch	NLAS4501	DTT1, DFT2	–
1-Single Pole, Single Throw Switch	NLAST4501	DTT1, DFT2	–
2-Single Pole, Single Throw Switches	NLAS323	US	–
2-Single Pole, Single Throw Switches	NLAS324	US	–
2-Single Pole, Single Throw Switches	NLAS325	US	–
2:1 Mux with Enable	NLAS1053	US	–
1-Single Pole, Dual Throw Switch	NLAS4599	DTT1, DFT2	–
1-Single Pole, Dual Throw Switch	NLAST4599	DTT1, DFT2	–
1-Single Pole, Dual Throw Switch	NLASB3157	DFT2	–
2-Dual Pole, Dual Throw Switches	NLAS44599	MNR2, DTR2	–
2-Dual Pole, Dual Throw Switches	NLAST44599	MNR2, DTR2	–
2-Single Pole, Dual Throw Switches	NLAS4684	MNR2, FCT, MR2	Ultra Low R$_{DS(ON)}$
2-Single Pole, Dual Throw Switches	NLAS4685	MNR2, FCT, MR2	Ultra Low R$_{DS(ON)}$
2-Single Pole, Single Throw Switches	NLAS2066	US	Analog OVT
2-Single Pole, Single Throw Switches	NL7WB66	US	–

DTT1 = TSOP-5, -6 (SOT-23); **DFT2** = SC-88, SC-88A (SC-70, SOT-353, SOT-363); **US** = Ultra-Small 8; **MNR2** = Quad Flat No-Lead
FCT = Flip-Chip; **MR2** = Micro10; **OVT** = Over-Voltage Tolerant

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