# **Low Voltage Reference**

A precision band–gap voltage reference designed for critical instrumentation and D/A converter applications. This unit is designed to work with D/A converters, up to 12 bits in accuracy, or as a reference for power supply applications.

- Output Voltage:  $2.5 \text{ V} \pm 25 \text{ mV}$
- Input Voltage Range: 4.5 V to 40 V
- Quiescent Current: 1.2 mA Typical
- Output Current: 10 mA
- Temperature Coefficient: 10 ppm/°C Typical
- Guaranteed Temperature Drift Specification
- Equivalent to AD580
- Standard 8-Pin DIP, and 8-Pin SOIC Package

#### **Typical Applications**

- Voltage Reference for 8 to 12 Bit D/A Converters
- Low T<sub>C</sub> Zener Replacement
- High Stability Current Reference
- Voltmeter System Reference
- Pb–Free Package is Available

#### **MAXIMUM RATINGS** ( $T_A = 25^{\circ}C$ , unless otherwise noted.)

Rating	Symbol	Value	Unit
Input Voltage	VI	40	V
Storage Temperature	T <sub>stg</sub>	-65 to 150	°C
Junction Temperature	TJ	+175	°C
Operating Ambient Temperature Range MC1403B MC1403	Τ <sub>Α</sub>	-40 to +85 0 to +70	°C °C

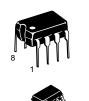
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



## ON Semiconductor®

http://onsemi.com

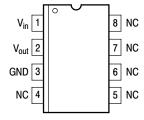
# PRECISION LOW VOLTAGE REFERENCE



PDIP-8 P1 SUFFIX CASE 626

> SOIC-8 D SUFFIX CASE 751

### **PIN CONNECTIONS**



#### **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 6 of this data sheet.

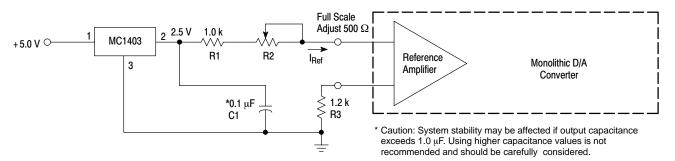


Figure 1. A Reference for Monolithic D/A Converters

#### Providing the Reference Current for ON Semiconductor Monolithic D/A Converters

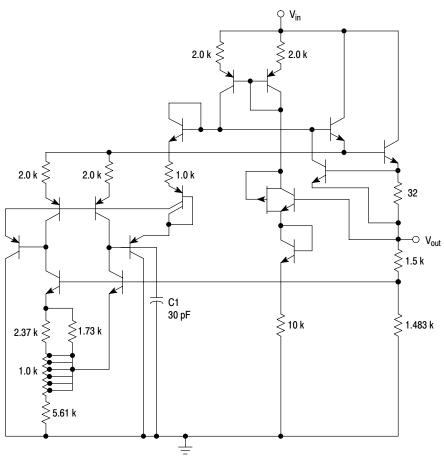
The MC1403 makes an ideal reference for many monolithic D/A converters, requiring a stable current reference of nominally 2.0 mA. This can be easily obtained from the MC1403 with the addition of a series resistor, R1. A variable resistor, R2, is recommended to provide means for full– scale adjust on the D/A converter. The resistor R3 improves temperature performance by matching the impedance on both inputs of the D/A reference amplifier. The capacitor decouples any noise present on the reference line. It is essential if the D/A converter is located any appreciable distance from the reference.

A single MC1403 reference can provide the required current input for up to five of the monolithic D/A converters.

Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (I <sub>O</sub> = 0 mA)	V <sub>out</sub>	2.475	2.5	2.525	V
Temperature Coefficient of Output Voltage* MC1403	$\Delta V_O / \Delta T$	-	10	40	ppm/°C
Output Voltage Change* (Over specified temperature range) MC1403 0 to +70°C MC1403B -40 to +85°C	ΔV <sub>O</sub>	-		7.0 12.5	mV
Line Regulation (I <sub>O</sub> = 0 mA) (15 V $\leq$ V <sub>I</sub> $\leq$ 40 V) (4.5 V $\leq$ V <sub>I</sub> $\leq$ 15 V)	Reg <sub>line</sub>	-	1.2 0.6	4.5 3.0	mV
Load Regulation (0 mA < I <sub>O</sub> < 10 mA)	Reg <sub>load</sub>	-	-	10	mV
Quiescent Current (I <sub>O</sub> = 0 mA)	la	-	1.2	1.5	mA

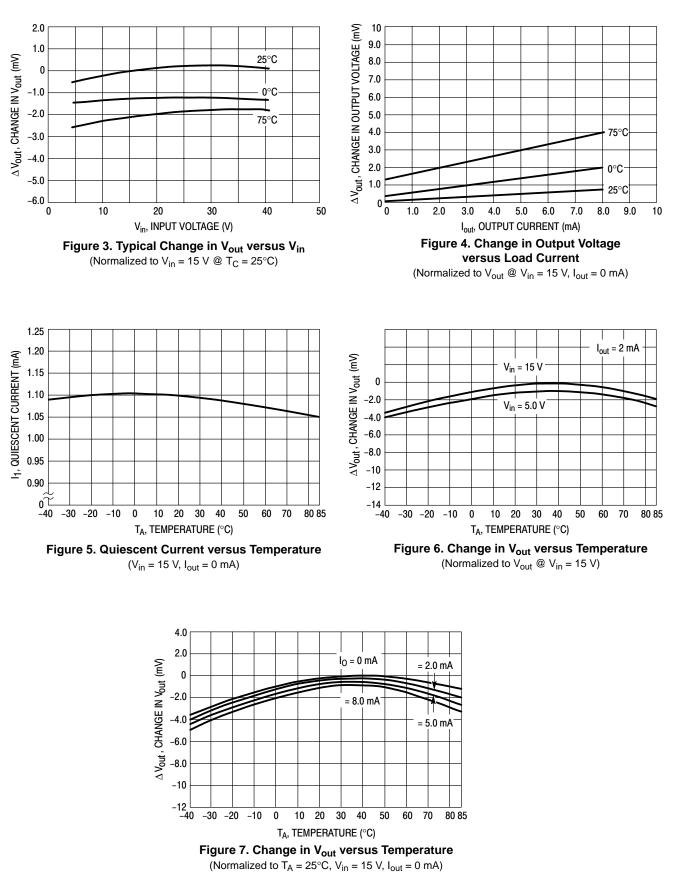
#### ELECTRICAL CHARACTERISTICS (Vin = 15 V, TA = 25°C, unless otherwise noted.)

\*Guaranteed but not tested.



This device contains 15 active transistors. Figure 2. MC1403, B Schematic

MC1403, B



#### 3–1/2–Digit Voltmeter – Common Anode Displays, Flashing Overrange

An example of a 3-1/2-digit voltmeter using the MC14433 is shown in the circuit diagram of Figure 8. The reference voltage for the system uses an MC1403 2.5 V reference IC. The full scale potentiometer can calibrate for a full scale of 199.9 mV or 1.999 V. When switching from 2.0 V to 200 mV operation, R<sub>I</sub> is also changed, as shown on the diagram.

When using  $R_C$  equal to 300 k $\Omega$ , the clock frequency for the system is about 66 kHz. The resulting conversion time is approximately 250 ms.

When the input is overrange, the display flashes on and off. The flashing rate is one-half the conversion rate. This

is done by dividing the EOC pulse rate by 2 with 1/2 MC14013B flip–flop and blanking the display using the blanking input of the MC14543B.

The display uses an LED display with common anode digit lines driven with an MC14543B decoder and an MC1413 LED driver. The MC1413 contains 7 Darlington transistor drivers and resistors to drive the segments of the display. The digit drive is provided by four MPS-A12 Darlington transistors operating in an emitter-follower configuration. The MC14543B, MC14013B and LED displays are referenced to V<sub>EE</sub> via Pin 13 of the MC14433. This places the full power supply voltage across the display. The current for the display may be adjusted by the value of the segment resistors shown as 150  $\Omega$  in Figure 8.

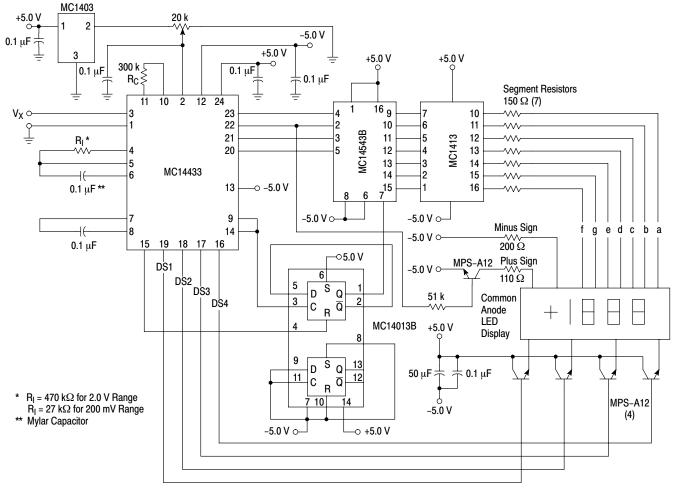


Figure 8. 3–1/2–Digit Voltmeter

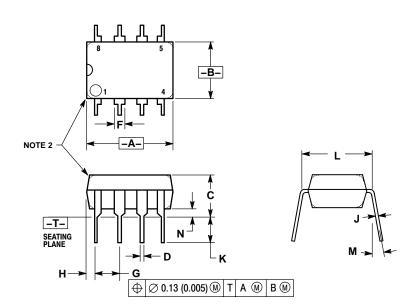
#### **ORDERING INFORMATION**

Device	Device Package Operating Temperature Range		Shipping <sup>†</sup>	
MC1403D	SOIC-8		98 Units/Rail	
MC1403DG	SOIC-8 (Pb-Free)		98 Units/Rail	
MC1403DR2	SOIC-8	1 [	2500 Tape/Reel	
MC1403DR2G	SOIC-8 (Pb-Free)	T <sub>A</sub> = 0° to +70°C	2500 Tape/Reel	
MC1403P1	PDIP-8	1 [	1000 Units/Rail	
MC1403P1G	PDIP-8 (Pb-Free)		1000 Units/Tubes	
MC1403BD	SOIC-8		98 Units/Rail	
MC1403BDG	SOIC-8 (Pb-Free)		98 Units/Rail	
MC1403BDR2	SOIC-8	$T_A = -40^\circ$ to $+85^\circ$ C	2500 Tape/Reel	
MC1403BP1	PDIP-8-8	1 F	1000 Units/Rail	
MC1403BP1G	PDIP-8-8 (Pb-Free)	1	1000 Units/Rail	

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## PACKAGE DIMENSIONS

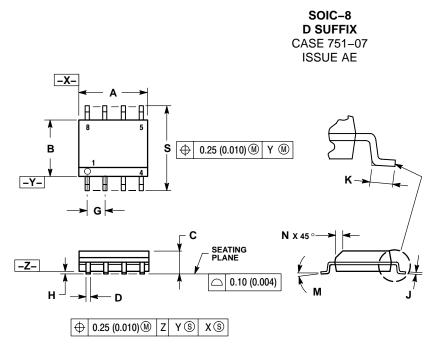
PDIP-8 P1 SUFFIX CASE 626-05 ISSUE L



NOTES: 1. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL. 2. PACKAGE CONTOUR OPTIONAL (ROUND OR SQUARE CORNERS). 3. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN MAX		
Α	9.40	10.16	0.370	0.400	
В	6.10	6.60	0.240	0.260	
C	3.94	4.45	0.155	0.175	
D	0.38	0.51	0.015	0.020	
F	1.02	1.78	0.040	0.070	
G	2.54 BSC		0.100 BSC		
Н	0.76	1.27	0.030	0.050	
J	0.20	0.30	0.008	0.012	
K	2.92	3.43	0.115	0.135	
L	7.62 BSC		0.300 BSC		
Μ		10°		10°	
N	0.76	1.01	0.030	0.040	

#### PACKAGE DIMENSIONS

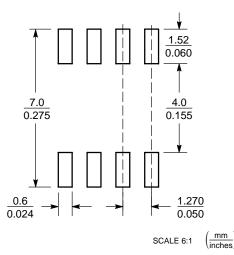


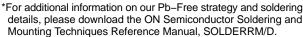
NOTES:

- 1. DIMENSIONING AND TOLERANCING PER
- ANSI Y14.5M, 1982. CONTROLLING DIMENSION: MILLIMETER. DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION. 2 3.
- MAXIMUM MOLD PROTRUSION 0.15 (0.006) 4. PER SIDE
- DIMENSION D DOES NOT INCLUDE DAMBAR 5. PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.
- 751–01 THRU 751–06 ARE OBSOLETE. NEW STANDARD IS 751–07. 6.

	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	4.80	5.00	0.189	0.197	
В	3.80	4.00	0.150	0.157	
С	1.35	1.75	0.053	0.069	
D	0.33	0.51	0.013	0.020	
G	1.27 BSC		0.050 BSC		
н	0.10	0.25	0.004	0.010	
J	0.19	0.25	0.007	0.010	
ĸ	0.40	1.27	0.016	0.050	
м	0 °	8 °	0 °	8 °	
Ν	0.25	0.50	0.010	0.020	
S	5.80	6.20	0.228	0.244	

#### SOLDERING FOOTPRINT\*





ON Semiconductor and in the registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications Intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### PUBLICATION ORDERING INFORMATION

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 61312, Phoenix, Arizona 85082-1312 USA Phone: 480-829-7710 or 800-344-3860 Toll Free USA/Canada Fax: 480-829-7709 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800–282–9855 Toll Free USA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center 2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051 Phone: 81-3-5773-3850

ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative