DTC124TM / DTC124TE / DTC124TUA / DTC124TKA / DTC124TSA

Transistors

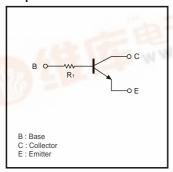
Digital transistors (built-in resistor)

DTC124TM / DTC124TE / DTC124TUA / DTC124TKA / DTC124TSA

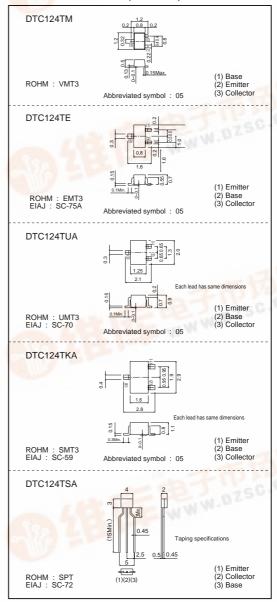
Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- The bias resistors consist of thin-film resistors with complete of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making device design easy.

●Equivalent circuit



External dimensions (Unit : mm)





● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol		Unit				
Parameter		М	Е	UA	KA	SA	Unit
Collector-base voltage	Vсво		V				
Collector-emitter voltage	Vceo		٧				
Emitter-base voltage	Vево		٧				
Collector current	Ic		mA				
Collector power dissipation	Pc	15	50	2	00	300	mW
Junction temperature	Tj		°C				
Storage temperature	Tstg		°C				

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	50	_	_	V	Ic=50μA
Collector-emitter breakdown voltage	BVceo	50	_	_	V	Ic=1mA
Emitter-base breakdown voltage	ВУЕВО	5	_	_	V	Iε=50μA
Collector cutoff current	Ісво	_	_	0.5	μА	Vcb=50V
Emitter cutoff current	ІЕВО	_	_	0.5	μА	V _{EB} =4V
Collector-emitter saturation voltage	VCE(sat)	_	_	0.3	V	Ic/I _B =10mA/1mA
DC current transfer ratio	hfe	100	250	600	_	VcE=5V, Ic=1mA
Input resistance	R ₁	15.4	22	28.6	kΩ	-
Transition frequency	f⊤	_	250	_	MHz	VcE=10V, IE=-5mA, f=100MHz *

^{*}Transition frequency of the device

Packaging specifications

	Package	VMT3	EMT3	UMT3	SMT3	SST3
	Packaging type	Taping	Taping	Taping	Taping	Taping
	Code	T2L	TL	T106	T146	TP
Туре	Basic ordering unit (pieces)	8000	3000	3000	3000	5000
DTC	124TM	0	_	-	_	_
DTC	124TE	_	0	_	_	_
DTC	124TUA	_	_	0	_	-
DTC	124TKA	_	-	-	0	_
DTC	124TSA	_	_	-	_	0

•Electrical characteristic curves

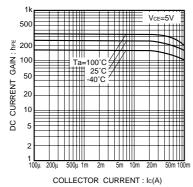


Fig.1 DC current gain vs. collector current

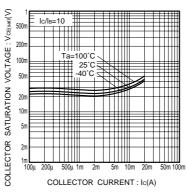


Fig.2 Collector-emitter saturation voltage vs. collector current

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