

MITSUBISHI SEMICONDUCTOR <GaAs FET>

MGFC36V5867

5.8 ~ 6.75GHz BAND 4W INTERNALLY MATCHED GaAs FET

DESCRIPTION

The MGFC36V5867 device is an internally impedance-matched GaAs power FET especially designed for use in 5.8 ~ 6.75GHz band amplifiers. The hermetically sealed metal-ceramic package guarantees high reliability.

FEATURES

- Class A operation
- Internally matched to 50(ohm) system
- High output power
 - P1dB = 36dBm (TYP.) @ f=5.8 ~ 6.75 GHz
- High power gain
 - GLP = 10 dB (TYP.) @ f=5.8 ~ 6.75 GHz

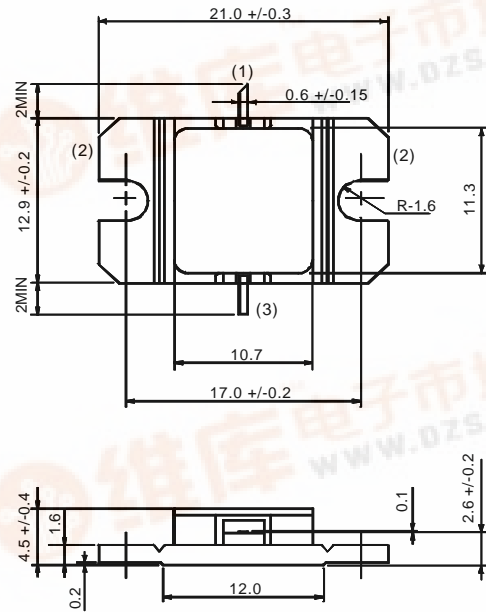
APPLICATION

VSAT

RECOMMENDED BIAS CONDITIONS

- VDS = 10 (V)
- ID=1.2(A)
- RG=100 (ohm)

OUTLINE DRAWING Unit : millimeters



GF-8

- (1) GATE
- (2) SOURCE (FLANGE)
- (3) DRAIN

ABSOLUTE MAXIMUM RATINGS (Ta=25deg.C)

Symbol	Parameter	Ratings	Unit
VGDO	Gate to drain voltage	-15	V
VGSO	Gate to source voltage	-15	V
ID	Drain current	3.75	A
IGR	Reverse gate current	-10	mA
IGF	Forward gate current	21	mA
PT *1	Total power dissipation	25	W
Tch	Channel temperature	175	deg.C
Tstg	Storage temperature	-65 / +175	deg.C

*1 : Tc=25deg.C

< Keep safety first in your circuit designs! >

Mitsubishi Electric Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (1) placement of substitutive, auxiliary circuits, (2) use of non-flammable material or (3) prevention against any malfunction or mishap.

ELECTRICAL CHARACTERISTICS (Ta=25deg.C)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
IDSS	Saturated drain current	VDS=3V, VGS=0V	-	-	3.75	A
gm	Transconductance	VDS=3V, ID=1.1A	-	1	-	S
VGS(off)	Pinch-off voltage	VDS=3V, ID=10mA	-	-	-4.5	V
P1dB	Output power at 1dB gain	VDS=10V, ID(RF off)=1.2A. f=5.8 ~ 6.75GHz	35.0	36.0	-	dBm
GLP	Linear power gain		8.5	10.0	-	dB
ID	Drain Current		-	-	1.8	A
P.A.E.	Power added efficiency		-	30	-	%
Rth(ch-c)	Thermal resistance	*1 delta Vf method	-	5	6	deg.C/W

*1 : Channel-case



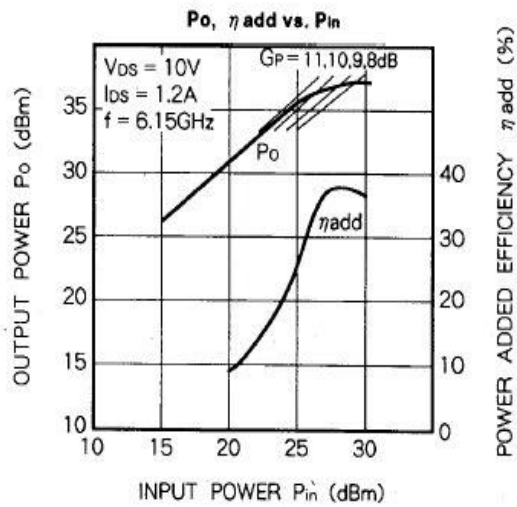
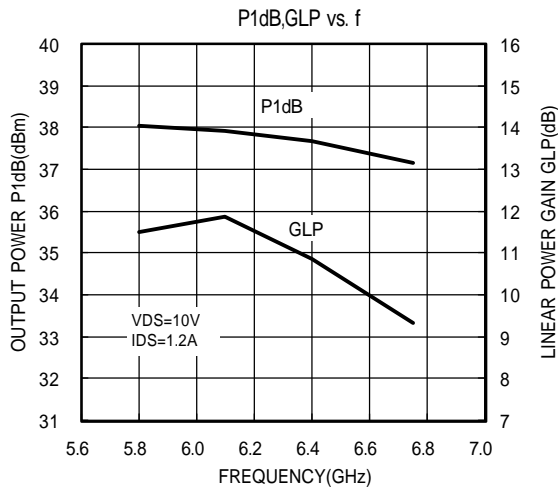
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TYPICAL CHARACTERISTICS (Ta=25deg.C)



S parameters (Ta=25deg.C , VDS=10(V),IDS=1.2(A))

f (GHz)	S-Parameters (TYP.)							
	S11		S21		S12		S22	
	Magn.	Angle(deg)	Magn.	Angle(deg)	Magn.	Angle(deg)	Magn.	Angle(deg)
5.80	0.48	155	3.409	-29	0.07	-76	0.33	179
5.90	0.47	136	3.426	-42	0.07	-91	0.29	164
6.00	0.46	116	3.472	-55	0.07	-104	0.26	147
6.10	0.44	98	3.494	-70	0.07	-117	0.24	132
6.20	0.42	79	3.465	-84	0.08	-132	0.21	114
6.30	0.40	60	3.446	-98	0.08	-145	0.20	96
6.40	0.39	39	3.397	-112	0.08	-157	0.19	77
6.50	0.37	17	3.356	-126	0.08	-172	0.18	57
6.60	0.37	-7	3.297	-141	0.08	173	0.18	34
6.70	0.38	-33	3.221	-156	0.08	161	0.18	12
6.80	0.41	-58	3.116	-171	0.08	146	0.19	-12



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