



## 双路 SPDT 模拟开关， 可处理超摆幅信号

### 概述

### 特性

MAX4850/MAX4850H/MAX4852/MAX4852H 系列是双路 SPDT (单刀/双掷) 开关，工作在 +2V 至 +5.5V 单电源，可以处理大于电源摆幅的信号。这些开关具有 3.5Ω 或 3.5Ω/7Ω 的低导通电阻与低导通电容，可理想用于音频与数据信号的切换。

MAX4850/MAX4850H 配置为两个 SPDT 开关，具有两个比较器，用于耳机检测或静音/传送按键功能。MAX4852 配置为两个 SPDT 开关，不具备比较器，电源电流低至 1μA。

对于超摆幅应用，这些器件提供信号导通或高阻两种选择。对于 MAX4850/MAX4852，即使在超出正电源摆幅的情况下，信号 (高达 5.5V) 也可以不失真地通过开关。对于 MAX4850H/MAX4852H，当输入信号超出电源摆幅时，开关输入变为高阻状态。

MAX4850/MAX4850H/MAX4852/MAX4852H 提供节省空间的 (3mm x 3mm)、16 引脚 TQFN 封装，工作在 -40°C 至 +85°C 扩展级温度范围。

- ◆ 符合 USB 2.0 全速 (12MB) 和 USB 1.1 信号开关要求
- ◆ 可切换大于  $V_{CC}$  的信号
- ◆ 0.1ns 偏差
- ◆ 3.5Ω/7Ω 导通电阻
- ◆ -3dB 带宽: 135MHz
- ◆ +2V 至 +5.5V 电源范围
- ◆ 与 1.8V 逻辑兼容
- ◆ 低电源电流
  - 1μA (MAX4852)
  - 5μA (MAX4850)
  - 10μA (MAX4850H/MAX4852H)
- ◆ 提供节省空间 (3mm x 3mm) 的 16 引脚 TQFN 封装

### 应用

- USB 开关
- 音频信号切换
- 蜂窝电话
- 笔记本电脑
- PDA 及其它手持式设备

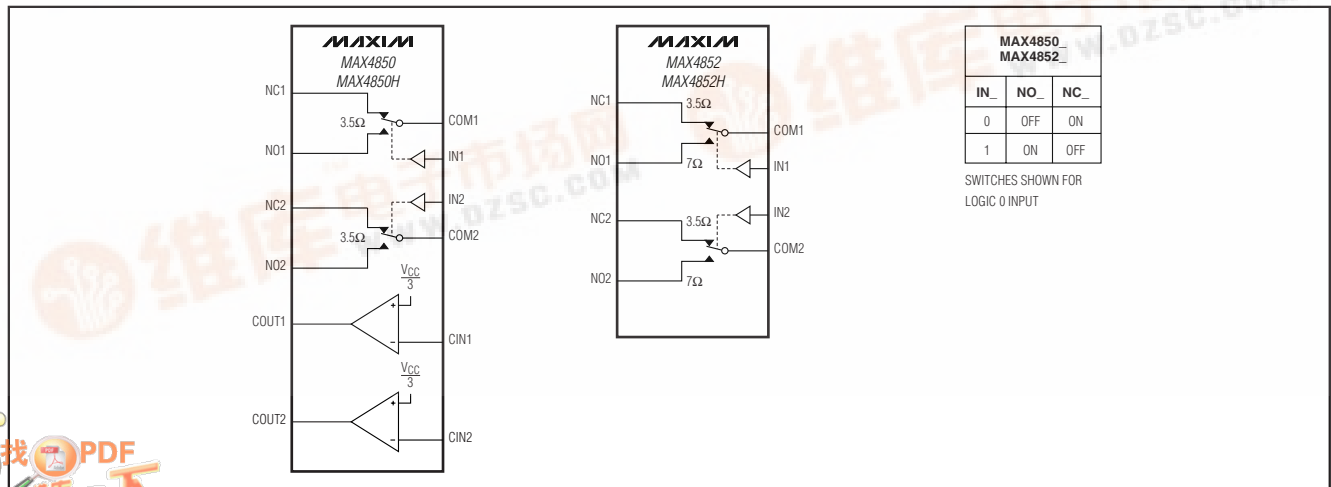
### 订购信息

PART	TEMP RANGE	PIN-PACKAGE	TOP MARK
MAX4850ETE	-40°C to +85°C	16 TQFN-EP*	ABU
MAX4850HETE	-40°C to +85°C	16 TQFN-EP*	ABV
MAX4852ETE	-40°C to +85°C	16 TQFN-EP*	ABZ
MAX4852HETE	-40°C to +85°C	16 TQFN-EP*	ACA

\*EP = 裸露焊盘。

引脚配置和选择指南在数据资料的最后部分给出。

### 方框图/真值表



MAX4850/MAX4850H/MAX4852/MAX4852H

# 双路 SPDT 模拟开关, 可处理超摆幅信号

## ABSOLUTE MAXIMUM RATINGS

$V_{CC}$ , $IN_{-}$ , $CIN_{-}$ , $COM_{-}$ , $NO_{-}$ , NC <sub>-</sub> to GND (Note 1).....	-0.3V to +6.0V
$COUT_{-}$ .....	-0.3V to ( $V_{CC}$ + 0.3V)
$COUT_{-}$ Continuous Current.....	±20mA
Closed Switch Continuous Current $COM_{-}$ , $NO_{-}$ , $NC_{-}$ 3.5Ω Switch .....	±100mA
7Ω Switch .....	±50mA
Peak Current $COM_{-}$ , $NO_{-}$ , $NC_{-}$ (pulsed at 1ms, 50% duty cycle) 3.5Ω Switch .....	±200mA
7Ω Switch .....	±100mA

Peak Current $COM_{-}$ , $NO_{-}$ , $NC_{-}$ (pulsed at 1ms, 10% duty cycle) 3.5Ω Switch .....	±240mA
7Ω Switch .....	±120mA
Continuous Power Dissipation ( $T_A$ = +70°C) 16-Pin TQFN (derate 20.8mW/°C above +70°C).....	1667mW
Operating Temperature Range .....	-40°C to +85°C
Junction Temperature.....	+150°C
Storage Temperature Range .....	-65°C to +150°C
Lead Temperature (soldering, 10s).....	+300°C

**Note 1:** Signals on  $IN_{-}$ ,  $NO_{-}$ ,  $NC_{-}$ , or  $COM_{-}$  below GND are clamped by internal diodes. Limit forward-diode current to maximum current rating.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## ELECTRICAL CHARACTERISTICS

( $V_{CC}$  = +2.7V to +5.5V,  $T_A$  = -40°C to +85°C, unless otherwise noted. Typical values are at  $V_{CC}$  = +3.0V,  $T_A$  = +25°C, unless otherwise noted.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Supply Voltage	$V_{CC}$		2.0		5.5	V
Supply Current	$I_{CC}$	$V_{CC}$ = 5.5V, $V_{IN_{-}}$ = 0V or $V_{CC}$	MAX4850	5	10	μA
			MAX4850H/ MAX4852H	10	20	
			MAX4852		1	
<b>ANALOG SWITCH (3.5Ω Switch)</b>						
Analog Signal Range	$V_{NO_{-}}$ , $V_{NC_{-}}$ , $V_{COM_{-}}$		0		5.5	V
On-Resistance (Note 3)	$R_{ON}$	$V_{CC}$ = 3V, $I_{COM_{-}}$ = 10mA, $V_{NC_{-}}$ or $V_{NO_{-}}$ = 0 to 5.5V (MAX485 <sub>-</sub> ) or $V_{CC}$ (MAX485 <sub>H</sub> )	$T_A$ = +25°C	3.5	4.5	Ω
			$T_A$ = -40°C to +85°C		5	
On-Resistance Match Between Channels (Notes 3, 4)	$\Delta R_{ON}$	$V_{CC}$ = 3V, $I_{COM}$ = 10mA, $V_{NC_{-}}$ or $V_{NO_{-}}$ = 1.5V	$T_A$ = +25°C	0.1	0.2	Ω
			$T_A$ = -40°C to +85°C		0.25	
On-Resistance Flatness (Note 5)	$R_{FLAT}$	$V_{CC}$ = 3V, $I_{COM_{-}}$ = 10mA, $V_{NC_{-}}$ or $V_{NO_{-}}$ = 1V, 2V, 3V	$T_A$ = +25°C	1.2	1.8	Ω
			$T_A$ = -40°C to +85°C		2	
NO <sub>-</sub> /NC <sub>-</sub> Off-Leakage Current	$I_{OFF}$	$V_{CC}$ = 5.5V, $V_{NC_{-}}$ or $V_{NO_{-}}$ = 1V or 4.5V, $V_{COM_{-}}$ = 4.5V or 1V	$T_A$ = +25°C	-2	+2	nA
			$T_A$ = -40°C to +85°C	-10	+10	
COM <sub>-</sub> On-Leakage Current	$I_{ON}$	$V_{CC}$ = 5.5V; $V_{NC_{-}}$ or $V_{NO_{-}}$ = 1V, 4.5V, or floating; $V_{COM_{-}}$ = 1V, 4.5V, or floating	$T_A$ = +25°C	-2	+2	nA
			$T_A$ = -40°C to +85°C	-12.5	+12.5	
-3dB Bandwidth	BW	Signal = 0dBm, $R_L$ = 50Ω, $C_L$ = 5pF (Figure 5)		100		MHz
NO <sub>-</sub> /NC <sub>-</sub> Off-Capacitance	$C_{OFF}$	f = 1MHz (Figure 6)		20		pF

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### ELECTRICAL CHARACTERISTICS (continued)

( $V_{CC} = +2.7V$  to  $+5.5V$ ,  $T_A = -40^\circ C$  to  $+85^\circ C$ , unless otherwise noted. Typical values are at  $V_{CC} = +3.0V$ ,  $T_A = +25^\circ C$ , unless otherwise noted.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
COM On-Capacitance	$C_{ON}$	$f = 1MHz$ (Figure 6)		60		$\mu F$
<b>ANALOG SWITCH (7<math>\Omega</math> Switch)</b>						
Analog Signal Range	$V_{NO\_}$ , $V_{NC\_}$ , $V_{COM\_}$		0		5.5	V
On-Resistance	$R_{ON}$	$V_{CC} = 3V$ , $I_{COM\_} = 10mA$ , $V_{NC\_}$ or $V_{NO\_} = 0$ to $5.5V$ (MAX4852) or $V_{CC}$ (MAX4852H)	$T_A = +25^\circ C$	7	9	$\Omega$
			$T_A = -40^\circ C$ to $+85^\circ C$		10	
On-Resistance Match Between Channels (Notes 3, 4)	$\Delta R_{ON}$	$V_{CC} = 3V$ , $I_{COM\_} = 10mA$ , $V_{NC\_}$ or $V_{NO\_} = 1.5V$	$T_A = +25^\circ C$	0.2	0.4	$\Omega$
			$T_A = -40^\circ C$ to $+85^\circ C$		0.5	
On-Resistance Flatness (Note 5)	$R_{FLAT}$	$V_{CC} = 3V$ , $I_{COM\_} = 10mA$ , $V_{NC\_}$ or $V_{NO\_} = 1V, 2V, 3V$	$T_A = +25^\circ C$	2.5	3.75	$\Omega$
			$T_A = -40^\circ C$ to $+85^\circ C$		4.0	
NO_/NC_ Off-Leakage Current	$I_{OFF}$	$V_{CC} = 5.5V$ , $V_{NC\_}$ or $V_{NO\_} = 1V$ or $4.5V$ , $V_{COM\_} = 4.5V$ or $1V$	$T_A = +25^\circ C$	-2	+2	nA
			$T_A = -40^\circ C$ to $+85^\circ C$	-10	+10	
COM_ On-Leakage Current	$I_{ON}$	$V_{CC} = 5.5V$ ; $V_{NC\_}$ or $V_{NO\_} = 1V, 4.5V$ , or floating; $V_{COM\_} = 1V, 4.5V$ , or floating	$T_A = +25^\circ C$	-2	+2	nA
			$T_A = -40^\circ C$ to $+85^\circ C$	-12.5	+12.5	
-3dB Bandwidth	BW	Signal = 0dBm, $R_L = 50\Omega$ , $C_L = 5pF$ (Figure 5)		135		MHz
NO_/NC_ Off-Capacitance	$C_{OFF}$	$f = 1MHz$ (Figure 6)		12		$\mu F$
COM On-Capacitance	$C_{ON}$	$f = 1MHz$ (Figure 6)		50		$\mu F$
<b>DYNAMIC CHARACTERISTICS</b>						
Signal Over-Rail to High-Z Switching Time	$t_{HIZ}$	MAX4850H/MAX4852H, $V_{NO\_}$ or $V_{NC\_} = V_{CC}$ to $(V_{CC} + 0.5V)$ , $V_{CC} < 5V$ (Figure 1)		0.5	1	$\mu s$
High-Z to Low-Z Switching Time	$t_{HIZB}$	MAX4850H/MAX4852H, $V_{NO\_}$ or $V_{NC\_} = (V_{CC} + 0.5V)$ to $V_{CC}$ , $V_{CC} < 5V$ (Figure 1)		0.5	1	$\mu s$
Skew (Note 3)	$t_{SKEW}$	$R_S = 39\Omega$ , $C_L = 50pF$ (Figure 2)		0.1	1	ns
Propagation Delay (Note 3)	$t_{PD}$	$R_S = 39\Omega$ , $C_L = 50pF$ (Figure 2)		0.9	2	ns
Turn-On Time	$t_{ON}$	$V_{CC} = 3V$ , $V_{NO\_}$ or $V_{NC\_} = 1.5V$ , $R_L = 300\Omega$ , $C_L = 50pF$ (Figure 1)	$T_A = +25^\circ C$	40	60	ns
			$T_A = -40^\circ C$ to $+85^\circ C$		100	
Turn-Off Time	$t_{OFF}$	$V_{CC} = 3V$ , $V_{NO\_}$ or $V_{NC\_} = 1.5V$ , $R_L = 300\Omega$ , $C_L = 50pF$ (Figure 1)	$T_A = +25^\circ C$	30	40	ns
			$T_A = -40^\circ C$ to $+85^\circ C$		60	

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### ELECTRICAL CHARACTERISTICS (continued)

( $V_{CC} = +2.7V$  to  $+5.5V$ ,  $T_A = -40^\circ C$  to  $+85^\circ C$ , unless otherwise noted. Typical values are at  $V_{CC} = +3.0V$ ,  $T_A = +25^\circ C$ , unless otherwise noted.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Break-Before-Make Time Delay (Note 3)	$t_D$	$V_{CC} = 3V$ , $V_{NO\_}$ or $V_{NC\_} = 1.5V$ , $R_L = 300\Omega$ , $C_L = 50pF$ (Figure 3)	$T_A = +25^\circ C$	15		ns
			$T_A = -40^\circ C$ to $+85^\circ C$	2		
Charge Injection	Q	$V_{COM\_} = 1.5V$ , $R_S = 0\Omega$ , $C_L = 1nF$ (Figure 4)	8			pC
Off-Isolation (Note 6)	$V_{ISO}$	$f = 100kHz$ , $V_{COM\_} = 1V_{RMS}$ , $R_L = 50\Omega$ , $C_L = 5pF$ (Figure 5)	-80			dB
Crosstalk	$V_{CT}$	$f = 1MHz$ , $V_{COM\_} = 1V_{RMS}$ , $R_L = 50\Omega$ , $C_L = 5pF$ (Figure 5)	-95			dB
Total Harmonic Distortion	THD	$f = 20Hz$ to $20kHz$ , $V_{COM\_} = 1V + 2V_{P-P}$ , $R_L = 600\Omega$	0.04			%
<b>DIGITAL I/O (IN<sub>-</sub>)</b>						
Input-Logic High Voltage	$V_{IH}$	$V_{CC} = 2V$ to $3.6V$	1.4			V
		$V_{CC} = 3.6V$ to $5.5V$	1.8			
Input-Logic Low Voltage	$V_{IL}$	$V_{CC} = 2V$ to $3.6V$			0.5	V
		$V_{CC} = 3.6V$ to $5.5V$			0.8	
Input Leakage Current	$I_{IN}$	$V_{IN\_} = 0$ or $5.5V$	-0.5		+0.5	$\mu A$
<b>COMPARATOR</b>						
Comparator Range			0		5.5	V
Comparator Threshold	$V_{TH}$	$V_{CC} = 2V$ to $5.5V$ , falling input	$0.3 \times V_{CC}$	$0.33 \times V_{CC}$	$0.36 \times V_{CC}$	V
Comparator Hysteresis		$V_{CC} = 2V$ to $5.5V$	50			mV
Comparator Output High Voltage		$I_{SOURCE} = 1mA$	$V_{CC} - 0.4V$			V
Comparator Output Low Voltage		$I_{SINK} = 1mA$			0.4	V
Comparator Switching Time		Rising input (Figure 7)	2.5			$\mu s$
		Falling input (Figure 7)	0.5			

**Note 2:** Specifications are 100% tested at  $T_A = +85^\circ C$  only, and guaranteed by design and characterization over the specified temperature range.

**Note 3:** Guaranteed by design and characterization; not production tested.

**Note 4:**  $\Delta R_{ON} = R_{ON(MAX)} - R_{ON(MIN)}$ .

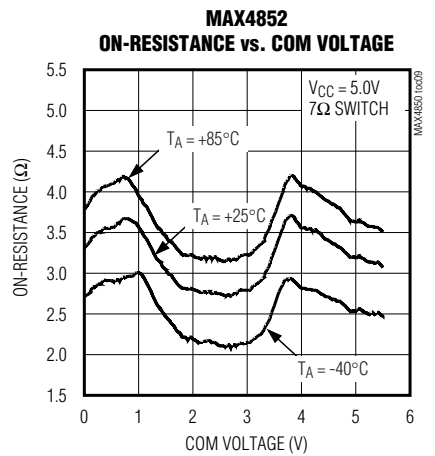
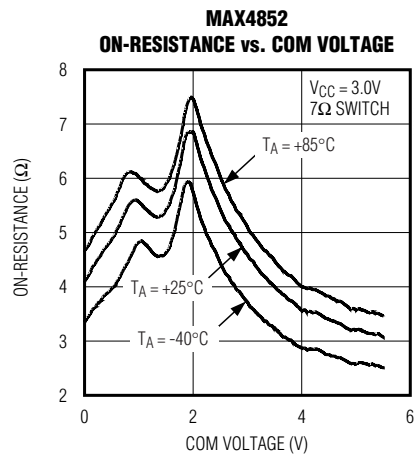
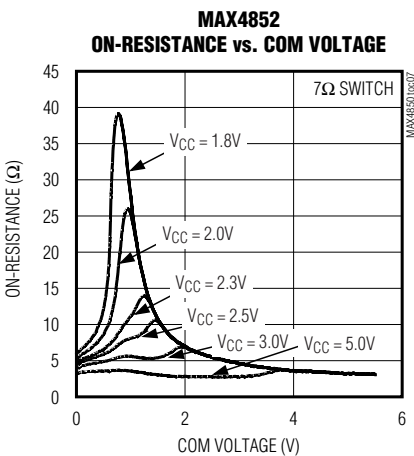
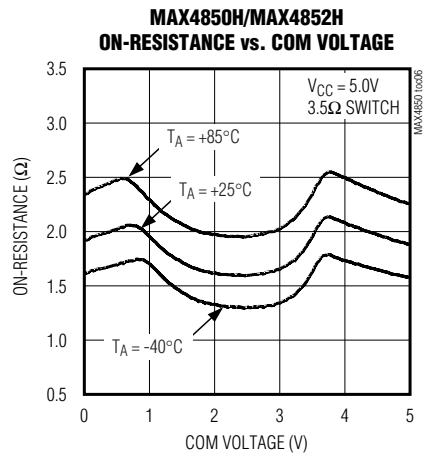
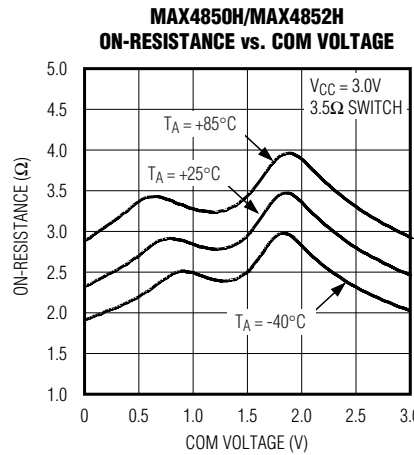
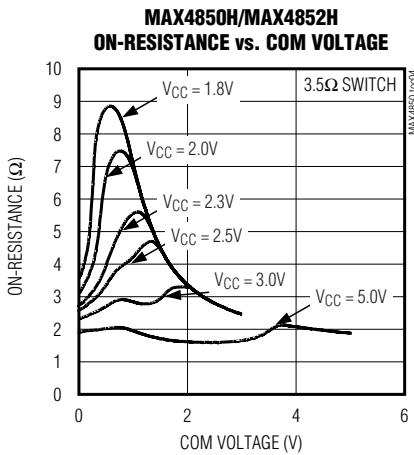
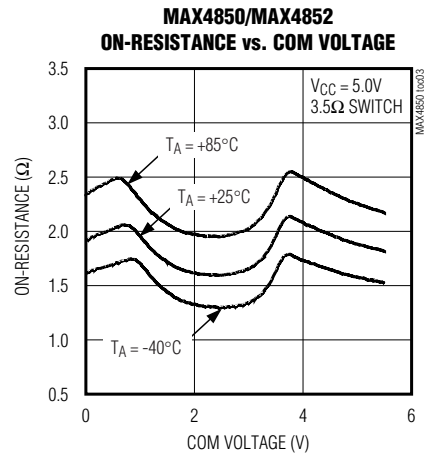
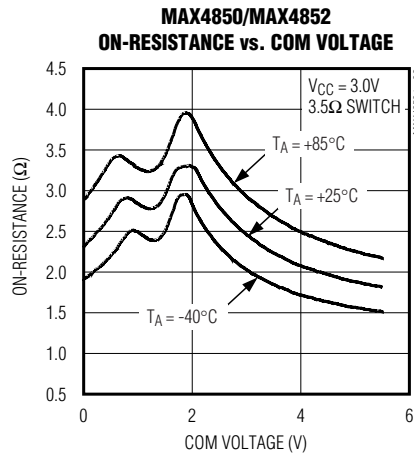
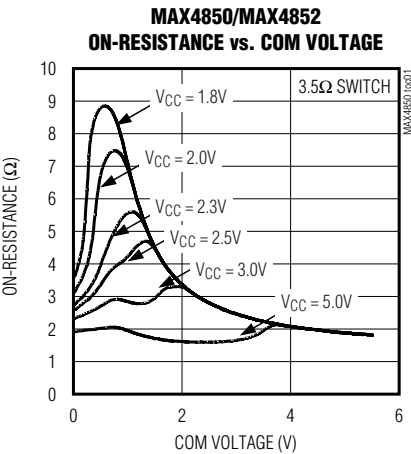
**Note 5:** Flatness is defined as the difference between the maximum and minimum value of on-resistance as measured over the specified analog signal ranges.

**Note 6:** Off-isolation =  $20 \log_{10}(V_{COM\_} / V_{NO\_})$ ,  $V_{COM\_}$  = output,  $V_{NO\_}$  = input to off switch.

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典型工作特性

( $V_{CC} = 3.0V$ ,  $T_A = +25^\circ C$ , unless otherwise noted.)

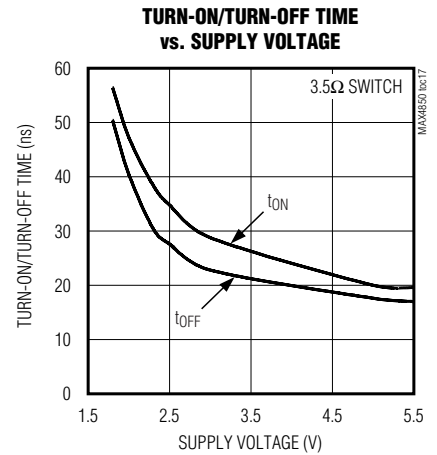
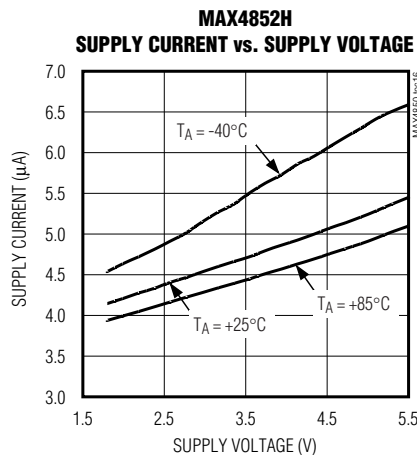
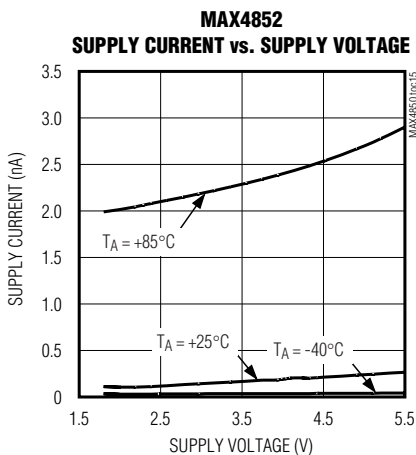
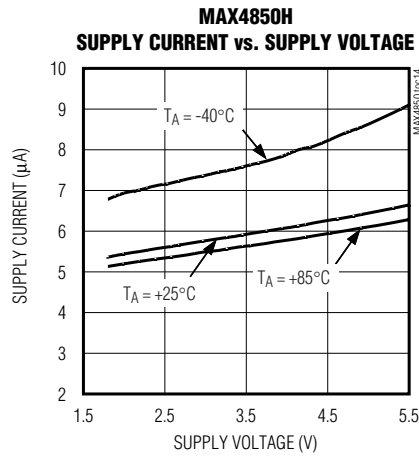
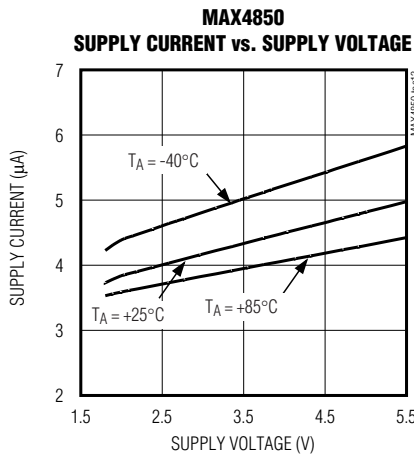
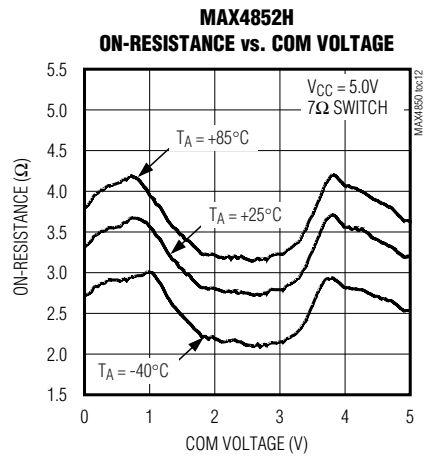
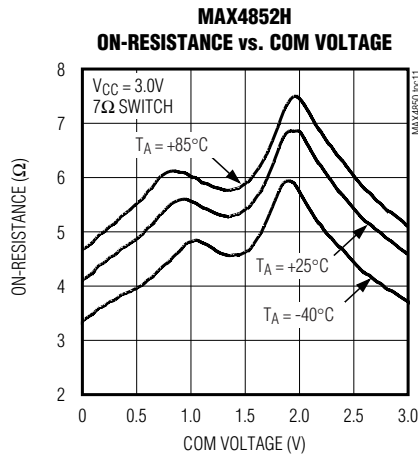
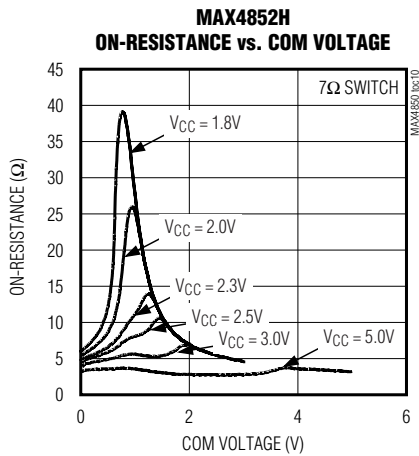


MAX4850/MAX4850H/MAX4852/MAX4852H

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典型工作特性(续)

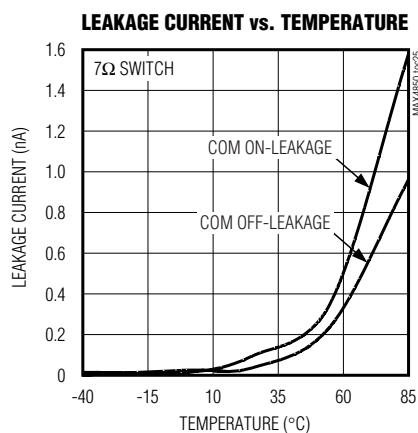
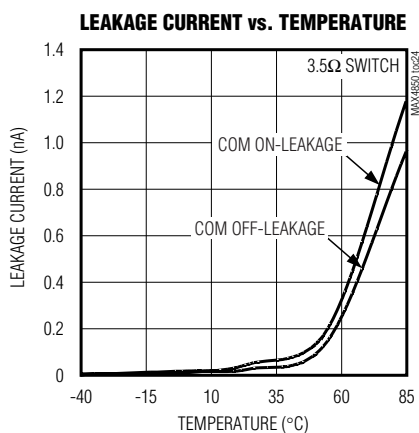
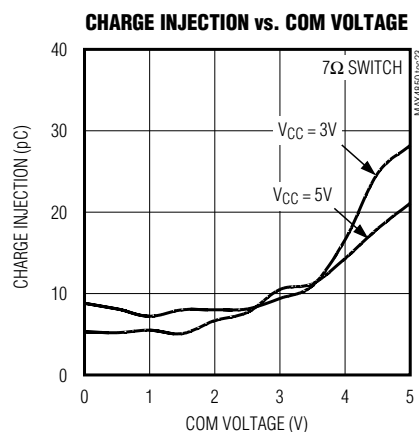
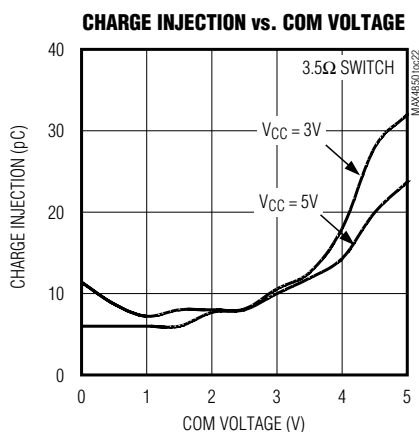
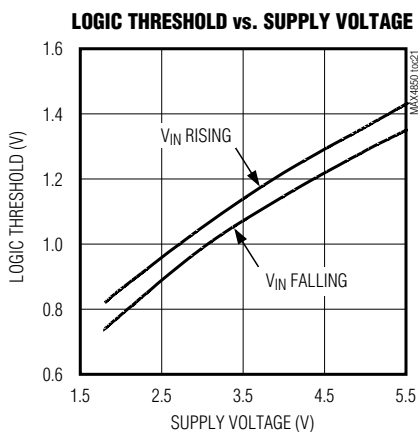
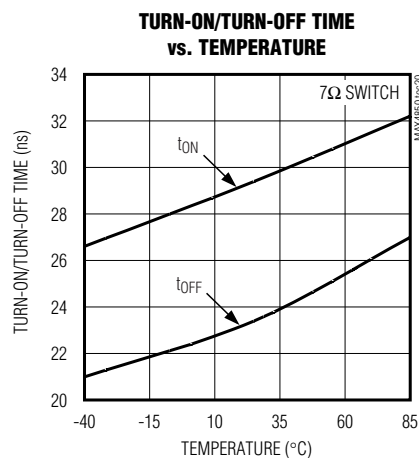
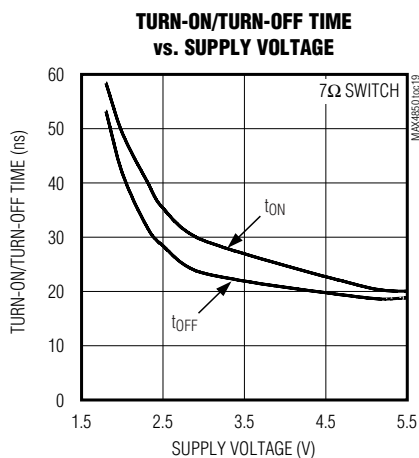
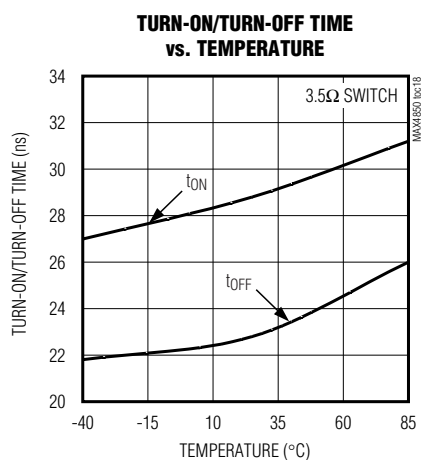
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典型工作特性(续)

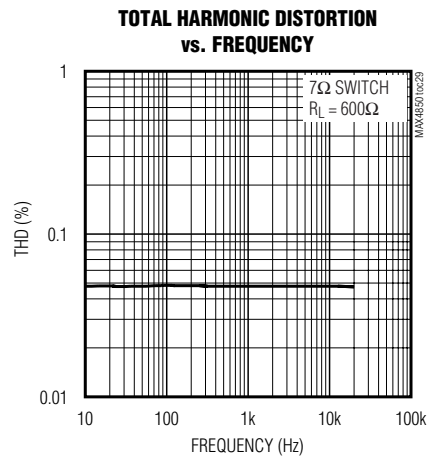
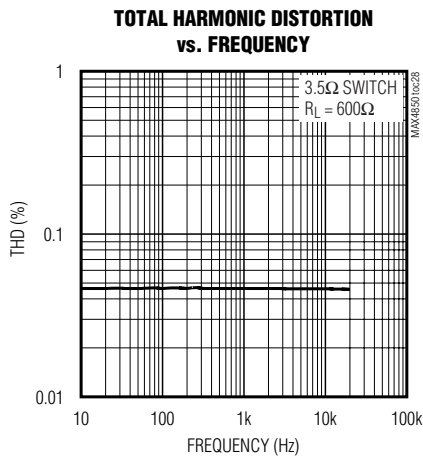
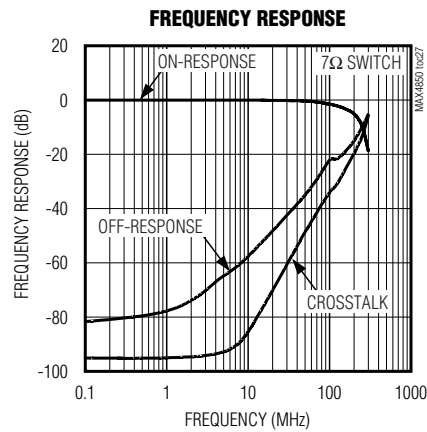
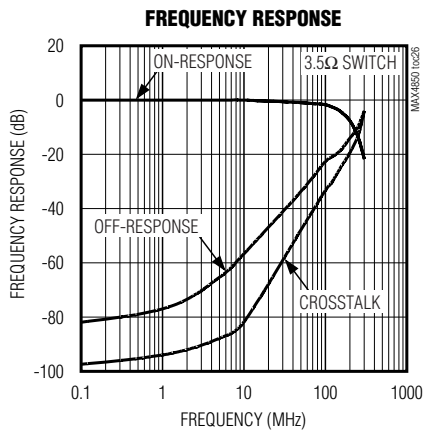
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(VCC = 3.0V, TA = +25°C, unless otherwise noted.)

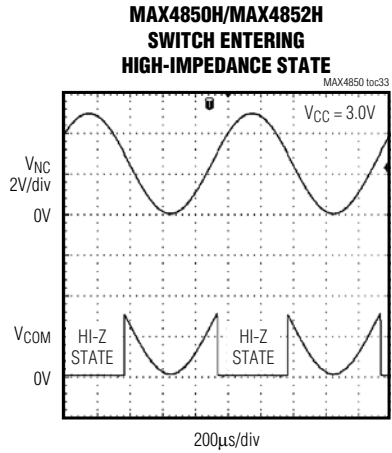
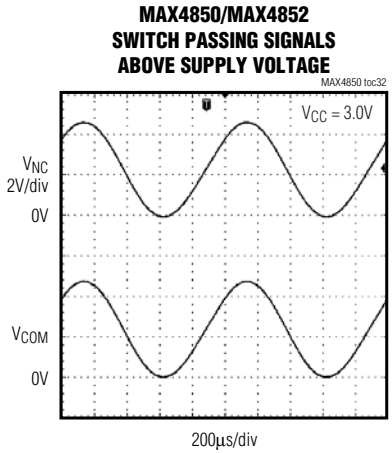
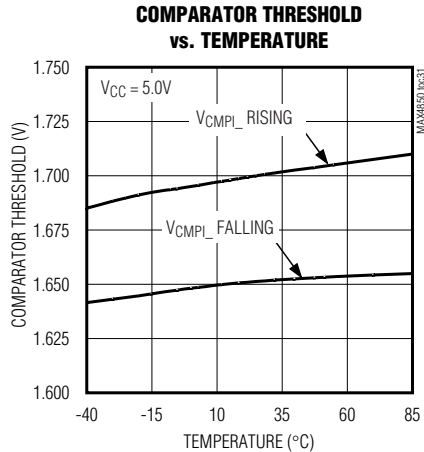
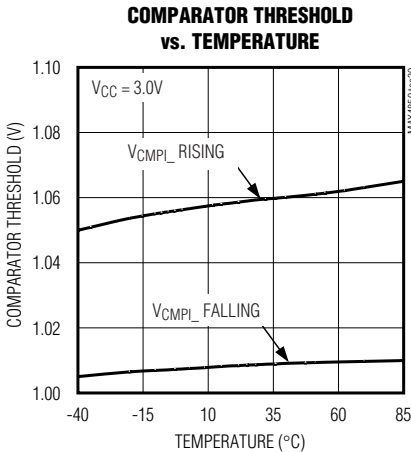




# 双路 SPDT 模拟开关， 可处理超摆幅信号

典型工作特性(续)

( $V_{CC} = 3.0V$ ,  $T_A = +25^\circ C$ , unless otherwise noted.)



MAX4850/MAX4850H/MAX4852/MAX4852H

## 双路 SPDT 模拟开关， 可处理超摆幅信号

引脚说明

### MAX4850/MAX4850H

引脚	名称	功能
1, 8	N.C.	未接。内部无连接。
2	CIN1	比较器 1 反相输入
3	CIN2	比较器 2 反相输入
4	COM1	模拟开关 1 的公共端
5	NO1	模拟开关 1 的常开端
6	GND	地
7	NC2	模拟开关 2 的常闭端
9	IN2	模拟开关 2 的数字控制输入。IN2 为逻辑低时接通 COM2 和 NC2，IN2 为逻辑高时接通 COM2 和 NO2。
10	COM2	模拟开关 2 的公共端
11	COU1	比较器 1 的输出
12	NO2	模拟开关 2 的常开端
13	COU2	比较器 2 的输出
14	V <sub>CC</sub>	电源电压。通过一个尽可能靠近该引脚的 0.01 $\mu$ F 电容旁路至 GND。
15	IN1	模拟开关 1 的数字控制输入。IN1 为逻辑低时接通 COM1 和 NC1，IN1 为逻辑高时接通 COM1 和 NO1。
16	NC1	模拟开关 1 的常闭端
EP	—	裸露焊盘。接至 PC 板地层。

### MAX4852/MAX4852H

引脚	名称	功能
1, 2, 3, 8, 11, 13	N.C.	未连。内部无连接。
4	COM1	模拟开关 1 的公共端
5	NO1	模拟开关 1 的常开端
6	GND	地
7	NC2	模拟开关 2 的常闭端
9	IN2	模拟开关 2 的数字控制输入。IN2 为逻辑低时接通 COM2 和 NC2，IN2 为逻辑高时接通 COM2 和 NO2。
10	COM2	模拟开关 2 的公共端
12	NO2	模拟开关 2 的常开端
14	V <sub>CC</sub>	电源电压。通过一个尽可能靠近该引脚的 0.01 $\mu$ F 电容旁路至 GND。
15	IN1	模拟开关 1 的数字控制输入。IN1 为逻辑低时接通 COM1 和 NC1，IN1 为逻辑高时接通 COM1 和 NO1。
16	NC1	模拟开关 1 的常闭端
EP	—	裸露焊盘。接至 PC 板地层。

## 双路 SPDT 模拟开关， 可处理超摆幅信号

### 详细说明

### 模拟信号电平

MAX4850/MAX4850H/MAX4852/MAX4852H 是低导通电阻、低电压、工作在 +2V 至 +5.5V 单电源的模拟开关，完全规范于电源标称值为 3.0V 的应用。这些器件具有超摆幅信号处理能力，在电源电压降至 2.0V 时，信号仍然允许高达 5.5V。这些器件配置为双路 SPDT 开关。

当模拟输入信号在整个电源电压范围内变化时，这些开关的导通电阻变化极小(见典型工作特性)。这些开关是双向的，因此 NO\_、NC\_ 和 COM\_ 既可做输入也可做输出。

### 比较器

这些开关具有 50pF 的低导通电容，能够满足 USB 2.0 全速/1.1 应用中 12Mbps 数据信号的开关要求。MAX485\_\_ 设计用来切换 USB 的 D+ 和 D- 信号，并能保证在输入信号的 50% 和输出信号的 50% 处测量的偏差小于 1ns (见图 2)。

比较器的同相输入端内部设置为  $V_{CC}/3$ 。当反相端 (CIN\_) 低于该门限值时 ( $V_{CC}/3$ )，比较器的输出 (COUT\_) 变高。当 CIN\_ 上升到超过  $V_{CC}/3$  时，COUT\_ 变低。

MAX4850\_ 具有一个比较器，可用于耳机或静音检测。比较器的门限值由内部设定，约为  $V_{CC}$  的 1/3。

比较器的门限值可用于耳机检测，这是因为典型情况下，耳机音频信号是偏置到  $V_{CC}/2$  的。

### 供电顺序

### 应用信息

**警告：** 不要超过极限参数，因为超过列出的极限参数可能引起器件永久损坏。

### 数字控制输入

逻辑输入 (IN\_) 可接受高达 +5.5V 的电压，即使电源电压低于这个电平。例如， $V_{CC}$  电源为 +3.3V 时，IN\_ 输入可低至 GND，最高可达 +5.5V，这样就允许多种逻辑电平共存于同一系统中。满摆幅驱动 IN\_ 可使功耗降至最低。对于 +2V 电源电压，逻辑门限值为 0.5V (低) 和 1.4V (高)；对于 +5V 电源电压，逻辑门限值是 0.8V (低) 和 1.8V (高)。

对于所有 CMOS 器件，推荐使用正确的供电顺序。总是在加模拟信号之前先加  $V_{CC}$ ，特别是在模拟信号没有限流的情况下。

# 双路 SPDT 模拟开关， 可处理超摆幅信号

测试电路/时序图

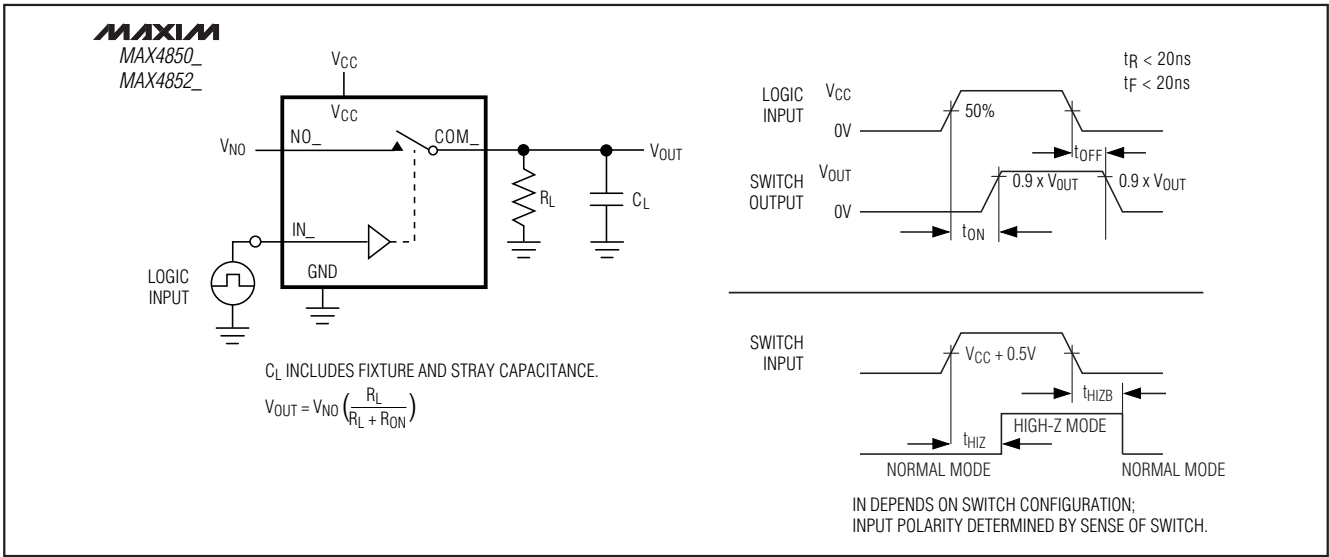


图 1. 开关时间

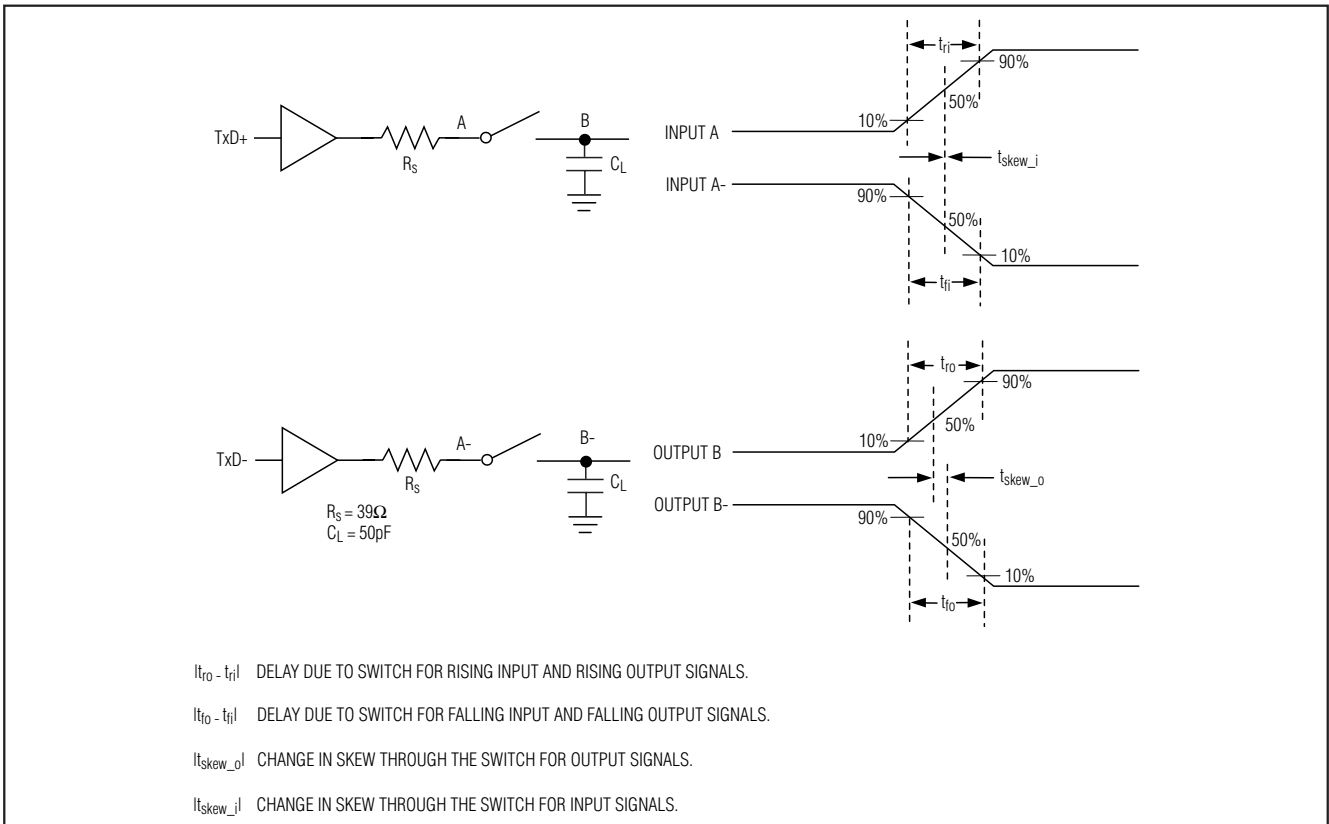


图 2. 输入/输出偏差时序图

## 双路 SPDT 模拟开关， 可处理超摆幅信号

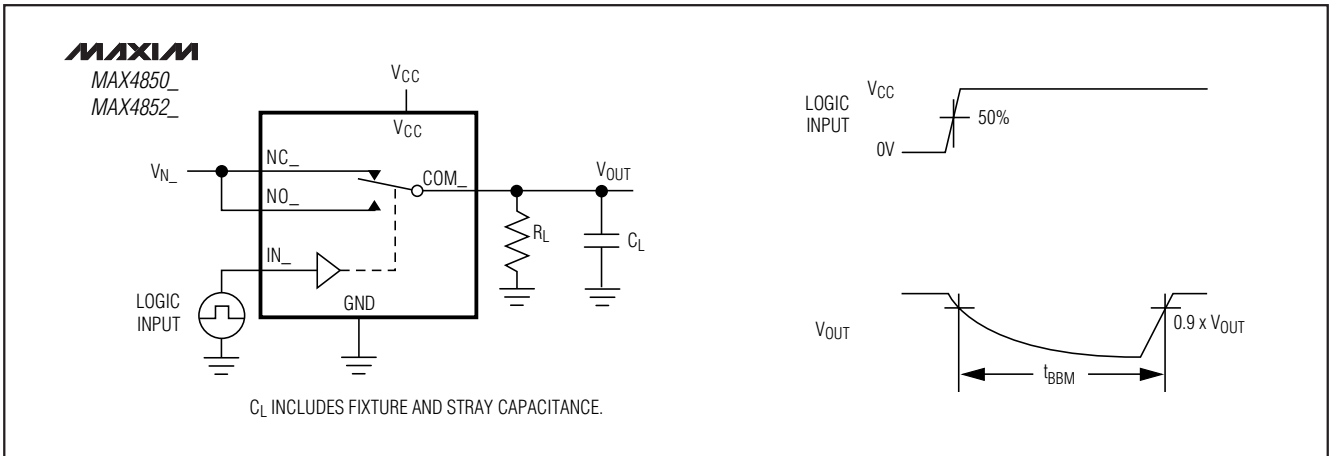


图 3. 先断后合的间隔

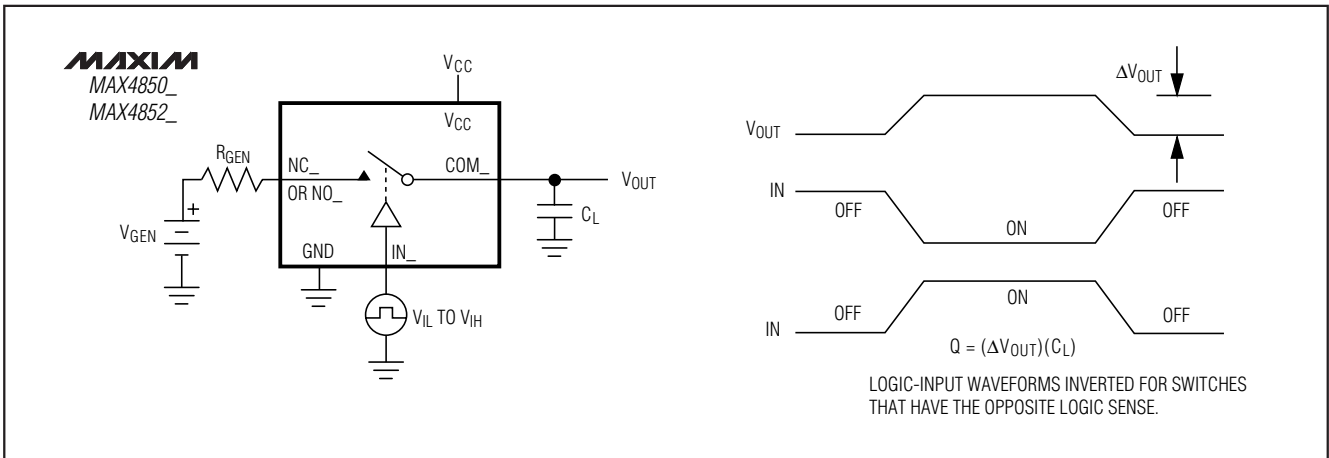


图 4. 电荷注入

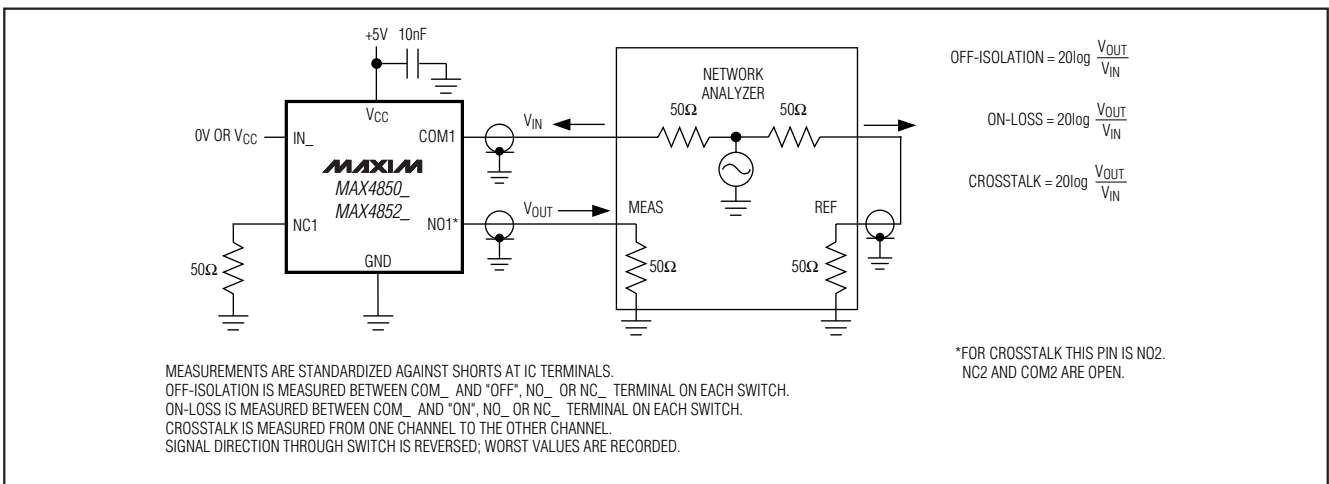


图 5. 导通损耗、关断隔离和串扰

双路 SPDT 模拟开关，  
可处理超摆幅信号

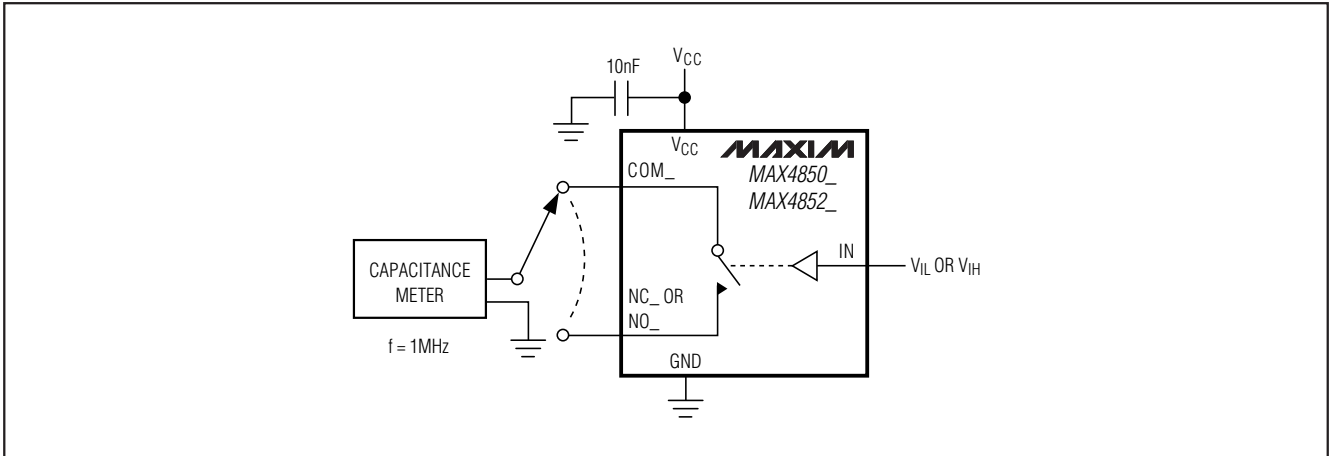


图 6. 通道关/开电容

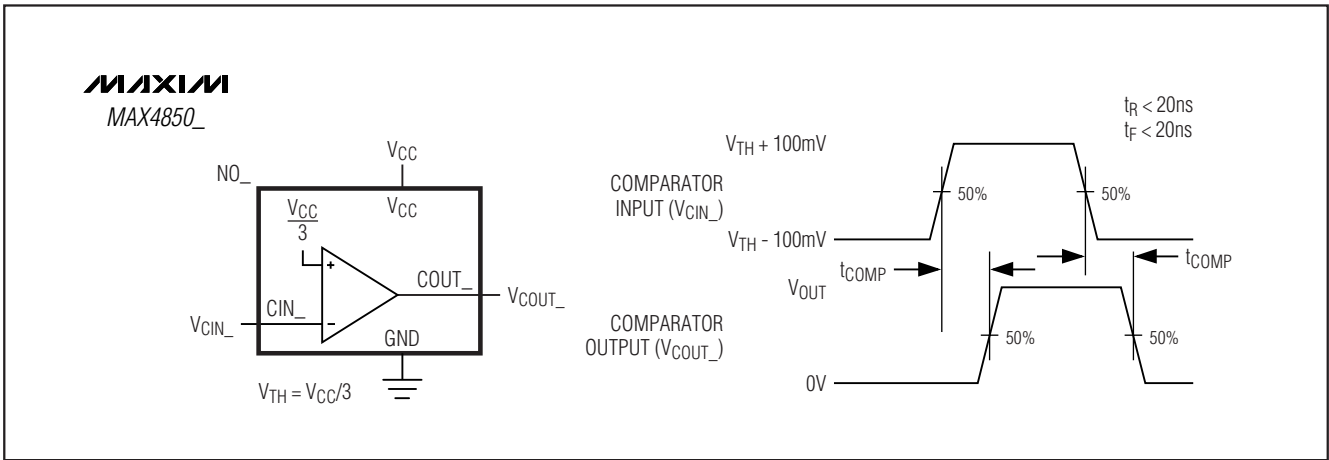
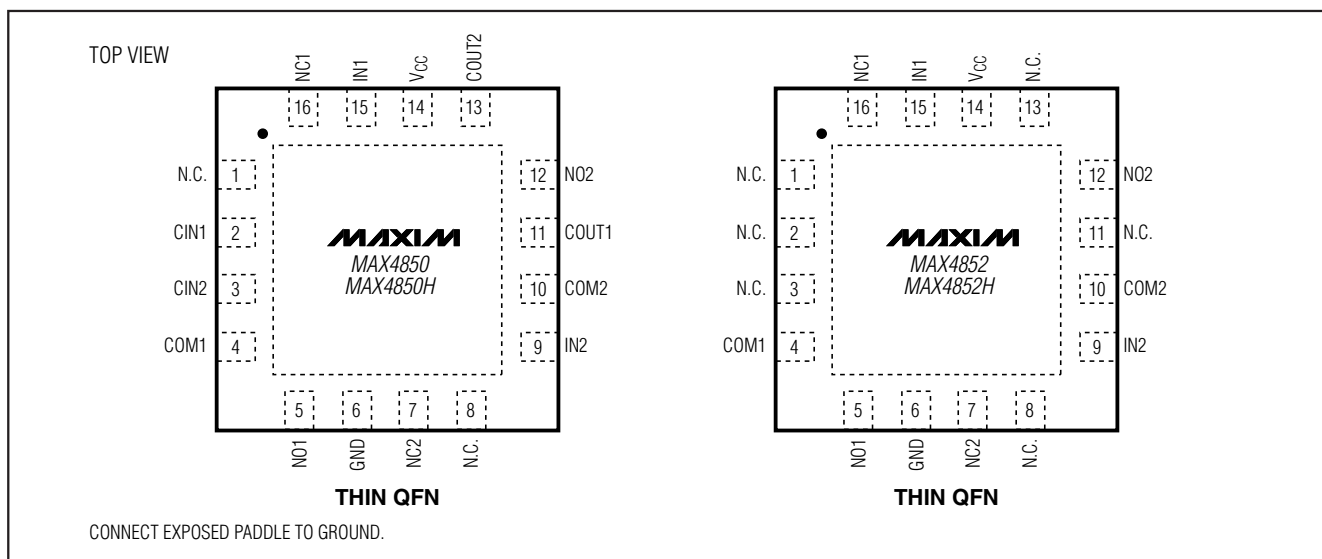


图 7. 比较器切换时间

# 双路 SPDT 模拟开关, 可处理超摆幅信号

引脚配置



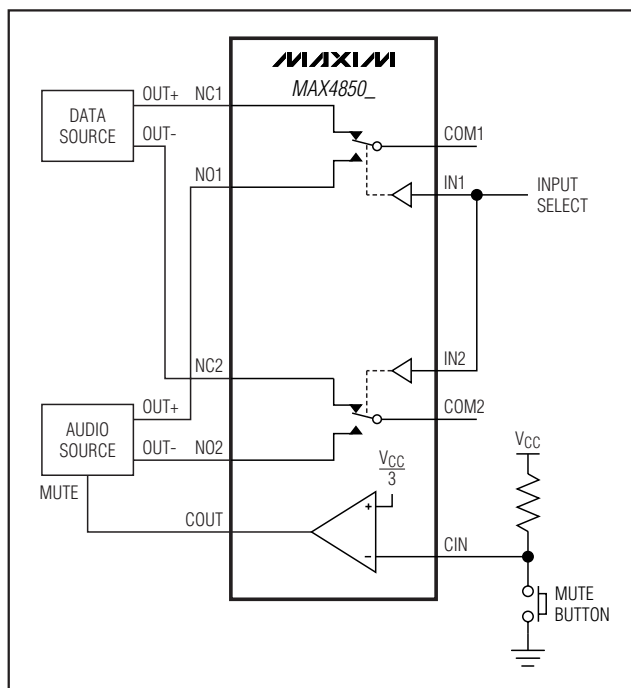
## 选择指南

PART	R <sub>ON</sub> NC/NO (Ω)	COMPARATORS	OVER-RAIL HANDLING
MAX4850	3.5/3.5	2	Input signal passes through the switch
MAX4850H	3.5/3.5	2	High-impedance switch input
MAX4852	3.5/7	—	Input signal passes through the switch
MAX4852H	3.5/7	—	High-impedance switch input

## 芯片信息

TRANSISTOR COUNT: 735  
PROCESS: CMOS

## 典型工作电路

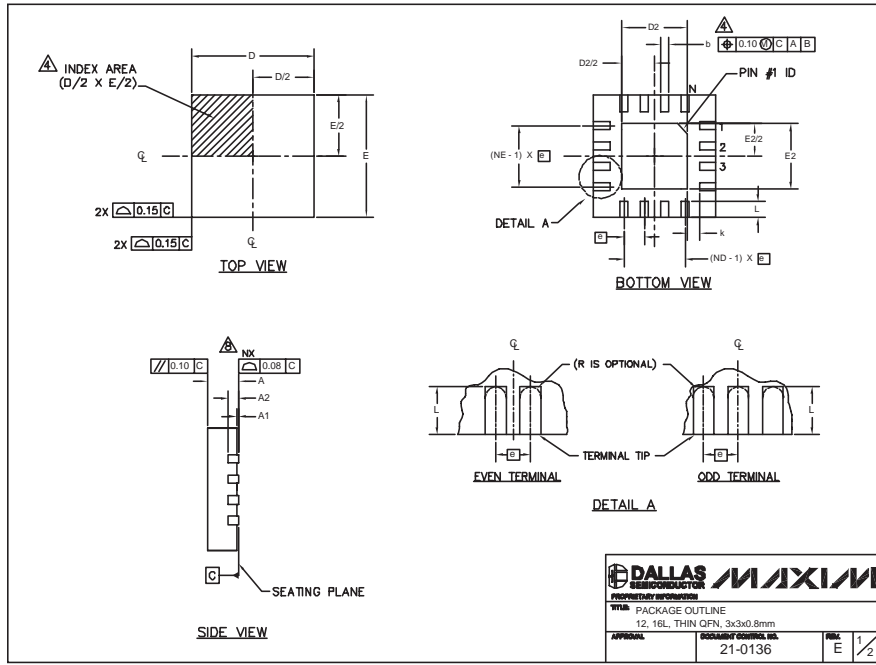


MAX4850/MAX4850H/MAX4852/MAX4852H

# 双路 SPDT 模拟开关， 可处理超摆幅信号

封装信息

(本数据资料提供的封装图可能不是最近的规格，如需最近的封装外型信息，请查询 [www.maxim-ic.com.cn/packages](http://www.maxim-ic.com.cn/packages)。)



PKG	12L 3x3			16L 3x3		
REF.	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	0.70	0.75	0.80	0.70	0.75	0.80
b	0.20	0.26	0.30	0.20	0.26	0.30
D	2.90	3.00	3.10	2.90	3.00	3.10
E	2.90	3.00	3.10	2.90	3.00	3.10
e	0.50 BSC			0.50 BSC		
L	0.45	0.65	0.65	0.30	0.40	0.60
N	12			16		
ND	3			4		
NE	3			4		
A1	0	0.02	0.05	0	0.02	0.05
A2	0.20 REF			0.20 REF		
k	0.25	-	-	0.25	-	-

PKG CODES	D2			E2			PIN ID	JEDEC	DOWN BOND ALLOWED
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.			
T1233-1	0.96	1.10	1.25	0.96	1.10	1.25	0.35 x 45°	WEED-1	NO
T1233-3	0.96	1.10	1.25	0.96	1.10	1.25	0.35 x 45°	WEED-1	YES
T1833-1	0.85	1.10	1.25	0.85	1.10	1.25	0.35 x 45°	WEED-2	NO
T1833-2	0.85	1.10	1.25	0.96	1.10	1.25	0.35 x 45°	WEED-2	YES
T1833F-3	0.85	0.80	0.85	0.85	0.80	0.85	0.225 x 45°	WEED-2	N/A
T1833-4	0.85	1.10	1.25	0.96	1.10	1.25	0.35 x 45°	WEED-2	NO

NOTES:

- DIMENSIONING & TOLERANCING CONFORM TO ASME Y14.5M-1994.
- ALL DIMENSIONS ARE IN MILLIMETERS. ANGLES ARE IN DEGREES.
- N IS THE TOTAL NUMBER OF TERMINALS.
- THE TERMINAL #1 IDENTIFIER AND TERMINAL NUMBERING CONVENTION SHALL CONFORM TO JEDEC 95-1 SFP-012. DETAILS OF TERMINAL #1 IDENTIFIER ARE OPTIONAL, BUT MUST BE LOCATED WITHIN THE ZONE INDICATED. THE TERMINAL #1 IDENTIFIER MAY BE EITHER A MOLD OR MARKED FEATURE.
- DIMENSION b APPLIES TO METALLIZED TERMINAL AND IS MEASURED BETWEEN 0.20 mm AND 0.25 mm FROM TERMINAL TIP.
- ND AND NE REFER TO THE NUMBER OF TERMINALS ON EACH D AND E SIDE RESPECTIVELY.
- DEPOPULATION IS POSSIBLE IN A SYMMETRICAL FASHION.
- COPLANARITY APPLIES TO THE EXPOSED HEAT SINK SLUG AS WELL AS THE TERMINALS.
- DRAWING CONFORMS TO JEDEC MO220 REVISION C.

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