General Description

The MAX1722/MAX1723/MAX1724 compact, high-efficiency, step-up DC-DC converters are available in tiny, 5pin thin SOT23 packages. They feature an extremely low 1.5µA quiescent supply current to ensure the highest possible light-load efficiency. Optimized for operation from one to two alkaline or nickel-metal-hydride (NiMH) cells, or a single Li+ cell, these devices are ideal for applications where extremely low quiescent current and ultra-small size are critical.

Built-in synchronous rectification significantly improves efficiency and reduces size and cost by eliminating the need for an external Schottky diode. All three devices feature a 0.5Ω N-channel power switch. The MAX1722/MAX1724 also feature proprietary noise-reduction circuitry, which suppresses electromagnetic interference (EMI) caused by the inductor in many step-up applications. The family offers different combinations of fixed or adjustable outputs, shutdown, and EMI reduction (see Selector Guide).

Pagers
Remote Controls
Remote Wireless Transmitters
Personal Medical Devices
Digital Still Cameras

Applications

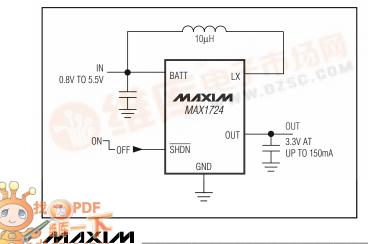
Single-Cell Battery-Powered Devices Low-Power Hand-Held Instruments

MP3 Players

D.WWW

Personal Digital Assistants (PDA)

Typical Operating Circuit



- ♦ Up to 90% Efficiency
- No External Diode or FETs Needed
- 1.5µA Quiescent Supply Current
- ♦ 0.1µA Logic-Controlled Shutdown
- ±1% Output Voltage Accuracy
- Fixed Output Voltage (MAX1724) or Adjustable Output Voltage (MAX1722/MAX1723)

专业PCB打样工厂,24小时加急出货

- Up to 150mA Output Current
- ♦ 0.8V to 5.5V Input Voltage Range
- ♦ 0.91V Guaranteed Startup (MAX1722/MAX1724)
- Internal EMI Suppression (MAX1722/MAX1724)
- Thin SOT23-5 Package (1.1mm max Height)

Ordering Information

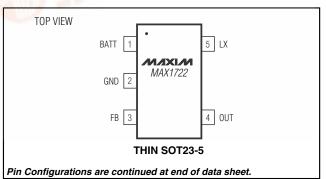
Features

PART	TEMP. RANGE	PIN- PACKAGE	TOP MARK
MAX1722EZK-T	-40°C to +85°C	5 SOT23	ADQF
MAX1723EZK-T	-40°C to +85°C	5 SOT23	ADQG
MAX1724EZK27-T	-40°C to +85°C	5 SOT23	ADQH
MAX1724EZK30-T	-40°C to +85°C	5 SOT23	ADQI
MAX1724EZK33-T	-40°C to +85°C	5 SOT23	ADQJ
MAX1724EZK50-T	-40°C to +85°C	5 SOT23	ADQK

Selector Guide

PART	OUTPUT (V)	SHDN	LX DAMPING
MAX1722EZK	Adjustable	No	Yes
MAX1723EZK	Adjustable	Yes	No
MAX1724EZK27	Fixed 2.7	Yes	Yes
MAX1724EZK30	Fixed 3.0	Yes	Yes
MAX1724EZK33	Fixed 3.3	Yes	Yes
MAX1724EZK50	Fixed 5.0	Yes	Yes

Pin Configurations



_ Maxim Integrated Products 1

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at

ABSOLUTE MAXIMUM RATINGS

OUT, SHDN, BATT, LX to GND	-0.3V to +6V
FB to GND	
OUT, LX Current	1Å
Continuous Power Dissipation (T _A =	+70°C)
5-Pin Thin SOT23 (derate 7.1mW	/°C above +70°C)571mW

Operating Temperature Range	40°C to +85°C
Junction Temperature	+150°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering, 10s)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{BATT} = 1.2V, V_{OUT} = 3.3V (MAX1722/MAX1723), V_{OUT} = V_{OUT(NOM)} (MAX1724), SHDN = OUT, R_L = \infty, T_A = 0°C to +85°C, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)$

PARAMETER	SYMBOL	CONE	ITIONS	MIN	ТҮР	MAX	UNITS
Minimum Input Voltage		MAX1722/MAX1724			0.8		V
	Max	T 05°C	MAX1722/MAX1724	0.91		5.5	V
Operating Input Voltage	VIN	$T_A = +25^{\circ}C$	MAX1723 (Note 2)	1.2		5.5	v
Minimum Ctartum Innut Valtage		T _A = +25°C,	MAX1722/MAX1724		0.83	0.91	V
Minimum Startup Input Voltage		$R_L = 3k\Omega$	MAX1723 (Note 2)		0.87	1.2	v
		MAX1724EZK27	$T_A = +25^{\circ}C$	2.673	2.7	2.727	
		WIAA 1724EZRZ7	$T_A = 0^{\circ}C \text{ to } +85^{\circ}C$	2.633		2.767	
		MAX1724EZK30	$T_A = +25^{\circ}C$	2.970	3.0	3.030	
Output Voltage	Vaur	WAAT/24EZROU	$T_A = 0^{\circ}C \text{ to } +85^{\circ}C$	2.925		3.075	V
Oulput voltage	VOUT	MAX1724EZK33	$T_A = +25^{\circ}C$	3.267	3.3	3.333	v
		WAAT/24EZR33	$T_A = 0^{\circ}C \text{ to } +85^{\circ}C$	3.218		3.383	1
		MAX1724EZK50	$T_A = +25^{\circ}C$	4.950	5.0	5.050	
		WAAT/24EZROU	$T_A = 0^{\circ}C \text{ to } +85^{\circ}C$	4.875		5.125	
Output Voltage Range	Vout	MAX1722/MAX1723		2		5.5	V
Feedback Voltage	VFB	MAX1722/MAX1723	$T_A = +25^{\circ}C$	1.223	1.235	1.247	V
reeuback vollage	V FB	WIAA 1722/WIAA 1723	$T_A = 0^{\circ}C \text{ to } +85^{\circ}C$	1.210		1.260	
Feedback Bias Current	I _{FB}	MAX1722/MAX1723	$T_A = +25^{\circ}C$		1.5	20	nA
reeuback blas Current	I IFB	WIAA 1722/WIAA 1723	$T_A = +85^{\circ}C$		2.2		
N-Channel On-Resistance	RDS(ON)	V _{OUT} forced to 3.3V			0.5	1.0	Ω
P-Channel On-Resistance	R _{DS(ON)}	V _{OUT} forced to 3.3V			1.0	2.0	Ω
N-Channel Switch Current Limit	ILIM	V _{OUT} forced to 3.3V		400	500	600	mA
Switch Maximum On-Time	ton			3.5	5	6.5	μs
Synchronous Rectifier Zero- Crossing Current		V _{OUT} forced to 3.3V		5	20	35	mA
Quiescent Current into OUT		(Notes 3, 4)			1.5	3.6	μA
Shutdown Current into OUT		MAX1723/MAX1724	$T_A = +25^{\circ}C$		0.01	0.5	μA
		(Notes 3, 4)	$T_A = +85^{\circ}C$		0.1		μΑ
Quiescent Current into BATT		MAX1722/MAX1724	$T_A = +25^{\circ}C$		0.001	0.5	μA
		(Note 4)	$T_A = +85^{\circ}C$		0.01		μΑ

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{BATT} = 1.2V, V_{OUT} = 3.3V (MAX1722/MAX1723), V_{OUT} = V_{OUT(NOM)} (MAX1724), \overline{SHDN} = OUT, R_L = \infty, T_A = 0^{\circ}C \text{ to } +85^{\circ}C,$ unless otherwise noted. Typical values are at T_A = +25^{\circ}C.) (Note 1)

PARAMETER	SYMBOL	COND	ITIONS	MIN	TYP	MAX	UNITS
Shutdown Current into BATT		MAX1724 (Note 4)	T _A = +25°C		0.001	0.5	μA
Shutdown Current into BATT		MAX 17 24 (NOLE 4)	$T_A = +85^{\circ}C$		0.01		μΑ
SHDN Voltage Threshold	VIL	MAX1723/MAX1724		75	400		mV
SHDN Voltage Mieshold	VIH	MAX1723/MAX1724			500	800	
SHDN Input Bias Current		MAX1723/MAX1724,	$T_A = +25^{\circ}C$		2	100	nA
SHDN input bias Current		$V_{\overline{SHDN}} = 5.5V$	T _A = +85°C		7		

ELECTRICAL CHARACTERISTICS

 $(V_{BATT} = 1.2V, V_{OUT} = 3.3V \text{ (MAX1722/MAX1723)}, V_{OUT} = V_{OUT(NOM)} \text{ (MAX1724)}, \overline{SHDN} = OUT, R_L = \infty, T_A = -40^{\circ}C \text{ to } +85^{\circ}C, \text{ unless otherwise noted.}) \text{ (Note 1)}$

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
		MAX1724EZK27	2.633		2.767	
		MAX1724EZK30	2.925		3.075	V
Output Voltage	Vout	MAX1724EZK33	3.218		3.383	
		MAX1724EZK50	4.875		5.125]
Output Voltage Range	Vout	MAX1722/MAX1723	2		5.5	V
Feedback Voltage	V _{FB}	MAX1722/MAX1723	1.200		1.270	V
N-Channel On-Resistance	R _{DS(ON)}	V _{OUT} forced to 3.3V			1.0	Ω
P-Channel On-Resistance	R _{DS(ON)}	V _{OUT} forced to 3.3V			2.0	Ω
N-Channel Switch Current Limit	ILIM	V _{OUT} forced to 3.3V	400		620	mA
Switch Maximum On-Time	ton		3.5		6.5	μs
Synchronous Rectifier Zero- Crossing Current		V _{OUT} forced to 3.3V	5		35	mA
Quiescent Current into OUT		(Notes 3,4)			3.6	μA
	VIL	MAX1723/MAX1724	75			
SHDN Voltage Threshold	VIH	MAX1723/MAX1724			800	mV

Note 1: Limits are 100% production tested at $T_A = +25^{\circ}$ C. Limits over the operating temperature range are guaranteed by design. **Note 2:** Guaranteed with the addition of a Schottky MBR0520L external diode between LX and OUT when using the MAX1723

with only one cell, and assumes a 0.3V voltage drop across the Schottky diode (see Figure 3). **Note 3:** Supply current is measured with an ammeter between the output and OUT pin. This current correlates directly with actual battery supply current, but is reduced in value according to the step-up ratio and efficiency.

Note 4: V_{OUT} forced to the following conditions to inhibit switching: V_{OUT} = 1.05 × V_{OUT(NOM)} (MAX1724), V_{OUT} = 3.465V (MAX1722/MAX1723).



Typical Operating Characteristics (Figure 3 (MAX1723), Figure 7 (MAX1722), Figure 8 (MAX1724), V_{BATT} = V_{IN} = 1.5V, L = 10μH, C_{IN} = 10μF, C_{OUT} = 10μF, T_A = +25°C, unless otherwise noted.) **EFFICIENCY vs. LOAD CURRENT EFFICIENCY vs. LOAD CURRENT EFFICIENCY vs. LOAD CURRENT** (V_{OUT} = 3.3V) $(V_{OUT} = 5.0V)$ $(V_{OUT} = 2.5V)$ 100 100 100 $V_{IN} = 3.3V$ $V_{IN} = 4.0V$ $V_{IN} = 2.0V$ $V_{IN} = 2.5V$ 1711 НШ 90 90 90 $V_{IN} = 2.0V$ $V_{IN} = 2.0V$ EFFICIENCY (%) EFFICIENCY (%) (%) 80 80 80 Vin = .5V EFFICIENCY 70 70 70 $V_{IN} = 1.0V$ 60 60 60 $V_{IN} = 1.0V$ = 1.5V V_{IN} : 11111 VIN = 1.0\ L = D01606 $V_{IN} = 1.5V$ L = D01606 L = D01606 50 50 50 0.01 0.1 100 1000 0.01 100 10 0.01 01 1000 1 0.1 1 10 100 1000 1 10 LOAD CURRENT (mA) LOAD CURRENT (mA) LOAD CURRENT (mA) MAXIMUM OUTPUT CURRENT **STARTUP VOLTAGE QUIESCENT CURRENT INTO OUT** vs. INPUT VOLTAGE vs. LOAD CURRENT vs. OUTPUT VOLTAGE 2.0 200 2.4 RESISTIVE LOAD NO LOAD 1.8 2.2 $V_{OUT} = 5.0V$ $V_{OUT} = 2.5V$ 160 1.6 2.0 STARTUP VOLTAGE (V) (1.4 1.8 (MMX) (MAX) (mA) 120 80 1.6 $V_{OUT} = 5.0V$ 1.4 1.2 $V_{OUT} = 3.3V$ 1.0 g 0.4 40 0.8 0.2 0 0.6 0 0.01 1.0 1.5 2.0 2.5 3.0 3.5 4.0 4.5 5.0 5.5 0 2 3 4 5 0.1 1 10 100 1 INPUT VOLTAGE (V) LOAD CURRENT (mA) OUTPUT VOLTAGE (V) **STARTUP VOLTAGE vs.** SWITCHING WAVEFORMS TEMPERATURE 1.2 NO LOAD llχ 1.0 500mA/div STARTUP VOLTAGE (V) 0.8 VOUT 50mV/div 0.6 0.4 VIX 2V/div

85

MAX1722/MAX1723/MAX1724

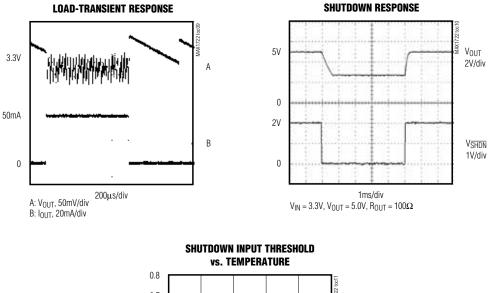
1µs/div

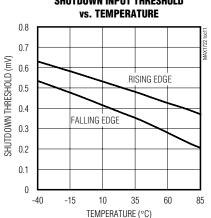
 $I_{OUT} = 50 \text{mA}, V_{OUT} = 5.0 \text{V}, V_{IN} = 3.3 \text{V}$

0.2 0 -40 -15 10 35 60 TEMPERATURE (°C)

Typical Operating Characteristics (continued)

(Figure 3 (MAX1723), Figure 7 (MAX1722), Figure 8 (MAX1724), $V_{BATT} = V_{IN} = 1.5V$, $L = 10\mu$ H, $C_{IN} = 10\mu$ F, $C_{OUT} = 10\mu$ F, $T_A = +25^{\circ}$ C, unless otherwise noted.)





Pin Description

	PIN		PIN		PIN		NAME	FUNCTION
MAX1722	MAX1723	MAX1724		FUNCTION				
1	—	1	BATT Battery Input and Damping Switch Connection					
_	1	3	SHDN	Shutdown Input. Drive high for normal operation. Drive low for shutdown.				
2	2	2	GND	Ground				
3	3	_	FB	Feedback Input to Set Output Voltage. Use a resistor-divider network to adjust the output voltage. See <i>Setting the Output Voltage</i> section.				
4	4	4	OUT	Power Output. OUT also provides bootstrap power to the IC.				
5	5	5	LX	Internal N-channel MOSFET Switch Drain and P-Channel Synchronous Rectifier Drain				

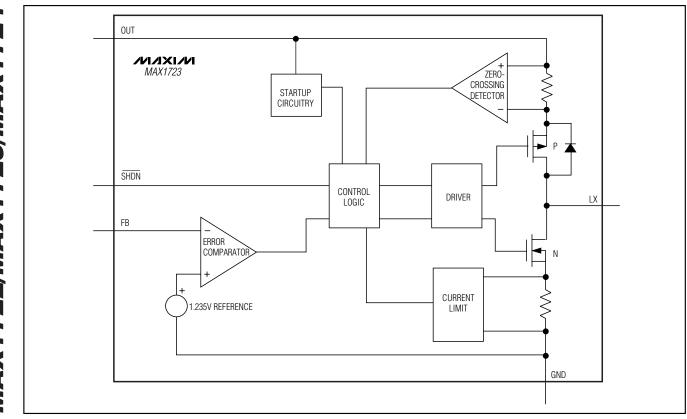


Figure 1. MAX1723 Simplified Functional Diagram

Detailed Description

The MAX1722/MAX1723/MAX1724 compact, high-efficiency, step-up DC-DC converters are guaranteed to start up with voltages as low as 0.91V and operate with an input voltage down to 0.8V. Consuming only 1.5µA of quiescent current, these devices include a built-in synchronous rectifier that reduces cost by eliminating the need for an external diode and improves overall efficiencv by minimizing losses in the circuit (see Synchronous Rectification section). The MAX1722/MAX1724 feature a clamp circuit that reduces EMI due to inductor ringing. The MAX1723/MAX1724 feature an active-low shutdown that reduces quiescent supply current to 0.1µA. The MAX1722/MAX1723 have an adjustable output voltage, while the MAX1724 is available with four fixed-output voltage options (see Selector Guide). Figure 1 is the MAX1723 simplified functional diagram and Figure 2 is the MAX1724 simplified functional diagram.

PFM Control Scheme

A forced discontinuous, current-limited, pulse-frequencymodulation (PFM) control scheme is a key feature of the MAX1722/MAX1723/MAX1724. This scheme provides ultra-low quiescent current and high efficiency over a wide output current range. There is no oscillator; the inductor current is limited by the 0.5A N-channel current limit or by the 5µs switch maximum on-time. Following each on cycle, the inductor current must ramp to zero before another cycle may start. When the error comparator senses that the output has fallen below the regulation threshold, another cycle begins.

Synchronous Rectification

The internal synchronous rectifier eliminates the need for an external Schottky diode, thus reducing cost and board space. While the inductor discharges, the Pchannel MOSFET turns on and shunts the MOSFET body diode. As a result, the rectifier voltage drop is significantly reduced, improving efficiency without the addition of external components.

Low-Voltage Startup Circuit

The MAX1722/MAX1723/MAX1724 contain a low-voltage startup circuit to control DC-DC operation until the output voltage exceeds 1.5V (typ). The minimum start-



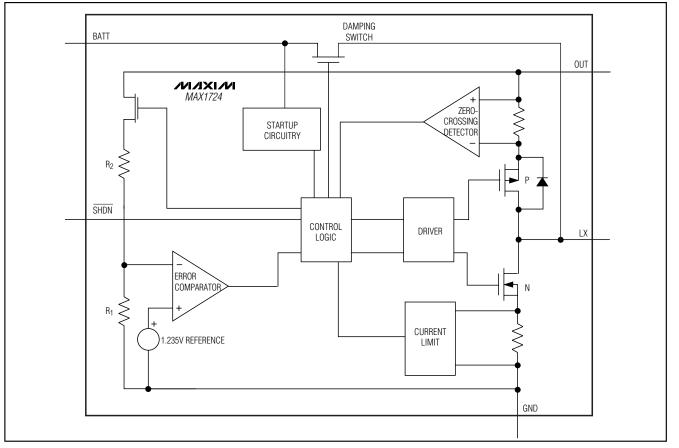


Figure 2. MAX1724 Simplified Functional Diagram

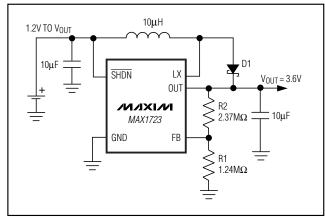


Figure 3. MAX1723 Single-Cell Operation

up voltage is a function of load current (see *Typical Operating Characteristics*). This circuit is powered from the BATT pin for the MAX1722/MAX1724, guaranteeing startup at input voltages as low as 0.91V. The MAX1723

lacks a BATT pin; therefore, this circuit is powered through the OUT pin. Adding a Schottky diode in parallel with the P-channel synchronous rectifier allows for startup voltages as low as 1.2V for the MAX1723 (Figure 3). The external Schottky diode is not needed for input voltages greater than 1.8V. Once started, the output maintains the load as the battery voltage decreases below the startup voltage.

Shutdown (MAX1723/MAX1724)

The MAX1723/MAX1724 enter shutdown when the SHDN pin is driven low. During shutdown, the body diode of the P-channel MOSFET allows current to flow from the battery to the output. V_{OUT} falls to approximately V_{IN} - 0.6V and LX remains high impedance. Shutdown can be pulled as high as 6V, regardless of the voltage at BATT or OUT. For normal operation, connect SHDN to the input.

MAX1722/MAX1723/MAX1724

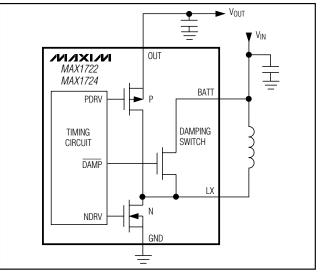


Figure 4. Simplified Diagram of Damping Switch

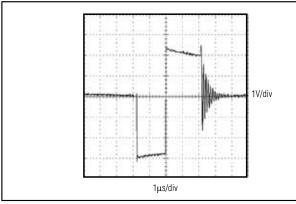
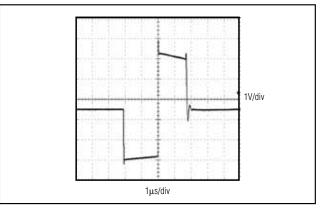


Figure 5. LX Ringing Without Damping Switch (MAX1723)

BATT/Damping Switch (MAX1722/MAX1724)

The MAX1722/MAX1724 include an internal damping switch (Figure 4) to minimize ringing at LX and reduce EMI. When the energy in the inductor is insufficient to supply current to the output, the capacitance and inductance at LX form a resonant circuit that causes ringing. The damping switch supplies a path to quickly dissipate this energy, suppressing the ringing at LX. This does not reduce the output ripple, but does reduce EMI with minimal impact on efficiency. Figures 5 and 6 show the LX node voltage waveform without and with the damping switch, respectively.





Design Procedure

Setting the Output Voltage (MAX1722/MAX1723)

The output voltage can be adjusted from 2V to 5.5V using external resistors R1 and R2 (Figure 7). Since FB leakage is 20nA (max), select feedback resistor R1 in the 100k Ω to 1M Ω range. Calculate R2 as follows:

$$R2 = R1 \left(\frac{V_{OUT}}{V_{FB}} - 1 \right)$$

where $V_{FB} = 1.235V$.



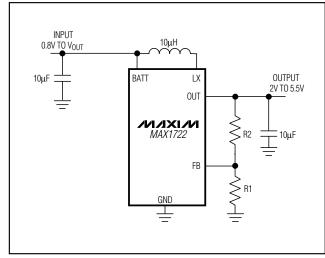


Figure 7. Adjustable Output Circuit

Inductor Selection

The control scheme of the MAX1722/MAX1723/ MAX1724 permits flexibility in choosing an inductor. A 10 μ H inductor value performs well in most applications. Smaller inductance values typically offer smaller physical size for a given series resistance, allowing the smallest overall circuit dimensions. Circuits using larger inductance values may start up at lower battery voltages, provide higher efficiency, and exhibit less ripple, but they may reduce the maximum output current. This occurs when the inductance is sufficiently large to prevent the maximum current limit (ILIM) from being reached before the maximum on-time (ton(MAX)) expires.

Table 1. Suggested Inductors andSuppliers

MANUFACTURER	INDUCTOR	PHONE WEBSITE
Coilcraft	DO1608 Series DO1606 Series	847-639-2361 www.coilcraft.com
Murata	LQH4C Series	770-436-1300 www.murata.com
Sumida	CDRH4D18 Series CR32 Series CMD4D06 Series	847-545-6700 www.sumida.com
Sumitomo/ Daidoo Electronics	CXLD140 Series	+81 (06) 6355-5733 www.daidoo.co.jp
Toko	3DF Type D412F Type	847-297-0070 www.toko.com

For maximum output current, choose the inductor value so that the controller reaches the current-limit before the maximum on-time is triggered:

$$L < \frac{V_{BATT} t_{ON(MAX)}}{I_{LIM}}$$

where the maximum on-time is typically 5 μ s, and the current limit (I_{LIM}) is typically 500mA (see *Electrical Characteristics* table).

For larger inductor values, determine the peak inductor current (IPEAK) by:

 $I_{\text{PEAK}} = \frac{V_{\text{BATT}} t_{\text{ON}(\text{MAX})}}{V_{\text{BATT}} t_{\text{ON}(\text{MAX})}}$

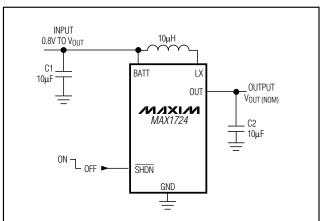


Figure 8. MAX1724 Standard Application Circuit

The inductor's incremental saturation current rating should be greater than the peak switching current. However, it is generally acceptable to bias the inductor into saturation by as much as 20%, although this will slightly reduce efficiency. Table 1 lists suggested inductors and suppliers.

Maximum Output Current

The maximum output current depends on the peak inductor current, the input voltage, the output voltage, and the overall efficiency (η) :

$$I_{OUT(MAX)} = \frac{1}{2} I_{PEAK} \left(\frac{V_{BATT}}{V_{OUT}} \right) \eta$$

MANUFACTURER	CAPACITOR VALUE	DESCRIPTION	PHONE WEBSITE
	1μF to 10μF	X7R Ceramic	
AVX	10μF to 330μF	TAJ Tantalum Series TPS Tantalum Series	843-448-9411 www.avxcorp.com
	1μF to 22μF	X5R/X7R Ceramic	
Kemet	10μF to 330μF	T494 Tantalum Series	864-963-6300 www.kemet.com
	68μF to 330μF	T520 Tantalum Series	www.kemel.com
Sanyo	33µF to 330µF	TPC Polymer Series	408-749-9714 www.secc.co.jp
Taiyo Yuden	33µF to 330µF	X5R/X7R Ceramic	800-368-2496 www.t-yuden.org
ТDК	1μF to 10μF	X7R Ceramic	847-803-6100 www.tdk.com
Vishay Sprague	10μF to 330μF	594D Tantalum Series 595D Tantalum Series	203-452-5664 www.vishay.com

Table 2. Suggested Surface-Mount Capacitors and Manufacturers (C1 and C2)

For most applications, the peak inductor current equals the current limit. However, for applications using large inductor values or low input voltages, the maximum ontime limits the peak inductor current (see *Inductor Selection* section).

Capacitor Selection

Choose input and output capacitors to supply the input and output peak currents with acceptable voltage ripple. The input filter capacitor (C_{IN}) reduces peak currents drawn from the battery and improves efficiency. Low equivalent series resistance (ESR) capacitors are recommended. Ceramic capacitors have the lowest ESR, but low ESR tantalum or polymer capacitors offer a good balance between cost and performance.

Output voltage ripple has two components: variations in the charge stored in the output capacitor with each LX pulse, and the voltage drop across the capacitor's ESR caused by the current into and out of the capacitor:

$$\begin{split} V_{\text{RIPPLE}} &= V_{\text{RIPPLE}(\text{C})} + V_{\text{RIPPLE}(\text{ESR})} \\ V_{\text{RIPPLE}(\text{ESR})} &\approx I_{\text{PEAK}} \; \text{R}_{\text{ESR}(\text{COUT})} \\ V_{\text{RIPPLE}(\text{C})} &\approx \frac{1}{2} \Bigg(\frac{L}{(V_{\text{OUT}} - V_{\text{BATT}})C_{\text{OUT}}} \Bigg) (I_{\text{PEAK}^2} - I_{\text{OUT}^2}) \end{split}$$

where IPEAK is the peak inductor current (see *Inductor Selection* section). For ceramic capacitors, the output voltage ripple is typically dominated by VRIPPLE(C). For example, a 10µF ceramic capacitor and a 10µH inductor typically provide 75mV of output ripple when stepping up from 3.3V to 5V at 50mA. Low input-to-output voltage differences (i.e. two cells to 3.3V) require higher output capacitor values.

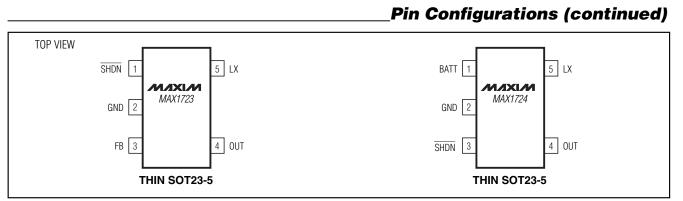
Capacitance and ESR variation of temperature should be considered for best performance in applications with wide operating temperature ranges. Table 2 lists suggested capacitors and suppliers.

PC Board Layout Considerations

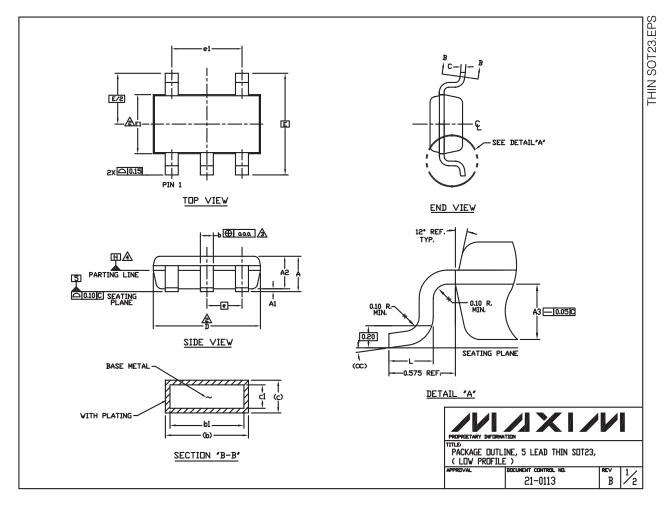
Careful PC board layout is important for minimizing ground bounce and noise. Keep the IC's GND pin and the ground leads of the input and output capacitors less than 0.2in (5mm) apart using a ground plane. In addition, keep all connections to FB (MAX1722/MAX1723 only) and LX as short as possible.

Chip Information

TRANSISTOR COUNT: 863



Package Information



Package Information (continued)

NOTES:

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- D" AND "E1" ARE REFERENCE DATUM AND DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS, AND ARE MEASURED AT THE BOTTOM PARTING LINE. MOLD FLASH OR PROTRUSION SHALL NOT EXCEED 0.15mm ON "D" AND 0.25mm ON "E" PER SIDE.
- THE LEAD WIDTH DIMENSION DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.07mm TOTAL IN EXCESS OF THE LEAD WIDTH DIMENSION AT MAXIMUM MATERIAL CONDITION.
- A DATUM PLANE "H" LOCATED AT MOLD PARTING LINE AND COINCIDENT WITH LEAD, WHERE LEAD EXITS PLASTIC BODY AT THE BOTTOM OF PARTING LINE.
- ✓5 THE LEAD TIPS MUST LINE WITHIN A SPECIFIED TOLERANCE ZONE. THIS TOLERANCE ZONE IS DEFINED BY TWO PARALLEL LINES. ONE PLANE IS THE SEATING PLANE, DATUM [-C-]; AND THE OTHER PLANE IS AT THE SPECIFIED DISTANCE FROM [-C-] IN THE DIRECTION INDICATED. FORMED LEADS SHALL BE PLANAR WITH RESPECT TO ONE ANOTHER WITH 0.10mm AT SEATING PLANE.
- THIS PART IS COMPLIANT WITH JEDEC SPECIFICATION MO-193 EXCEPT FOR THE "e" DIMENSION WHICH IS 0.95Mmm INSTEAD OF 1.00mm. THIS PART IS IN FULL COMPLIANCE TO EIAJ SPECIFICATION SC-74.

SYMBOLS										
A	-	-	1.10							
A1	0.05	0.075	0.10							
A2	0.85	0.88	0.90							
A3		0.50 BSC								
b	0.30	-	0.45							
b1	0.25	0.35	0.40							
с	0.15	-	0.20							
c1	0.12	0.127	0.15							
D	2.80	2.90	3.00							
E		2.75 BSC								
E1	1.55	1.60	1.65							
L	0.30	0.40	0.50							
e1		1.90 BSC								
e		0.95 BSC								
20	0-	4-	8-							
ممم	0.20									

			/
TITLE: PACKAGE DUTLINE, 5 LEAD THIN SOT23, (LOW PROFILE)			
APPROVAL	DOCUMENT CONTROL NO.	REV	21
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