

# MOS FIELD EFFECT TRANSISTOR **2SK3639**

# SWITCHING N-CHANNEL POWER MOS FET

# DESCRIPTION

The 2SK3639 is N-channel MOS FET device that features a low on-state resistance and excellent switching characteristics, and designed for low voltage high current applications such as DC/DC converter with synchronous rectifier.

# ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK3639-ZK	TO-252 (MP-3ZK)

(TO-252)



# **FEATURES**

· Low on-state resistance

 $R_{\text{DS(on)1}}$  = 5.5 m $\Omega$  MAX. (Vgs = 10 V, ID = 32 A)

 $R_{DS(on)2}$  = 8.5 m $\Omega$  MAX. (VGs = 4.5 V, ID = 32 A)

• Low Ciss: Ciss = 2400 pF TYP.

# ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (VGs = 0 V)	Vdss	20	V
Gate to Source Voltage (V <sub>DS</sub> = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	ID(DC)	±64	А
Drain Current (pulse) Note	D(pulse)	±256	А
Total Power Dissipation (Tc = 25°C)	PT1	40	W
Total Power Dissipation	PT2	1.0	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	–55 to +150	°C

**Note** PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%

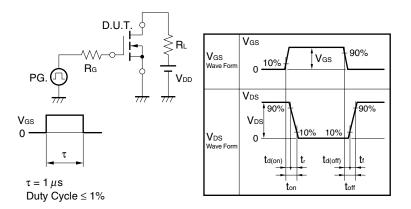
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ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)

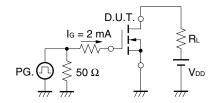
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V			10	μA
Gate Leakage Current	lgss	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V			±100	nA
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.5		2.5	V
Forward Transfer Admittance Note	<b>y</b> fs	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 32 A	19	39		S
Drain to Source On-state Resistance Note	RDS(on)1	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 32 A		4.4	5.5	mΩ
	RDS(on)2	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 32 A		5.8	8.5	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> = 10 V		2400		pF
Output Capacitance	Coss	V <sub>GS</sub> = 0 V		970		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		350		pF
Turn-on Delay Time	td(on)	V <sub>DD</sub> = 10 V, I <sub>D</sub> = 32 A		13		ns
Rise Time	tr	V <sub>GS</sub> = 10 V		14		ns
Turn-off Delay Time	td(off)	R <sub>G</sub> = 10 Ω		71		ns
Fall Time	tr			22		ns
Total Gate Charge	QG	V <sub>DD</sub> = 16 V		45		nC
Gate to Source Charge	QGS	V <sub>GS</sub> = 10 V		7.6		nC
Gate to Drain Charge	Qgd	I <sub>D</sub> = 64 A		11		nC
Body Diode Forward Voltage Note	VF(S-D)	I⊧ = 64 A, V <sub>GS</sub> = 0 V		0.96		V
Reverse Recovery Time	trr	I⊧ = 64 A, V <sub>GS</sub> = 0 V		40		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/ <i>µ</i> s		35		nC

Note Pulsed: PW  $\leq$  350  $\mu$ s, Duty Cycle  $\leq$  2%

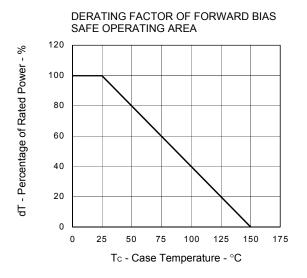
#### **TEST CIRCUIT 1 SWITCHING TIME**

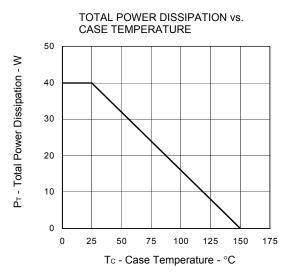


# **TEST CIRCUIT 2 GATE CHARGE**

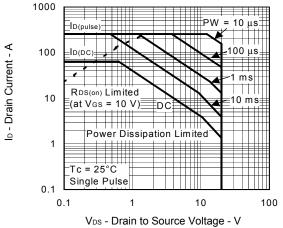


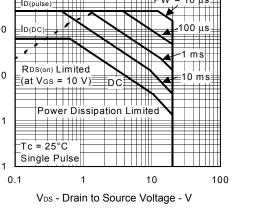
# TYPICAL CHARACTERISTICS ( $T_A = 25^{\circ}C$ )

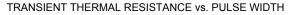


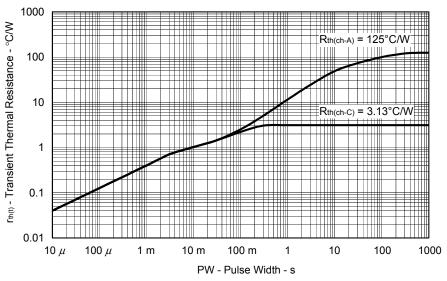


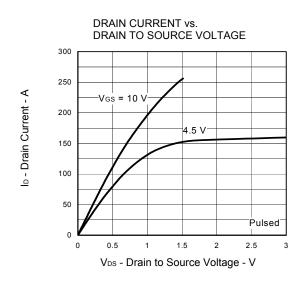
FORWARD BIAS SAFE OPERATING AREA

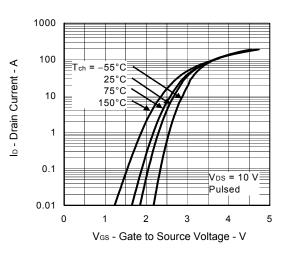






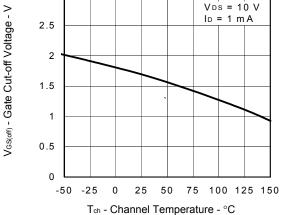




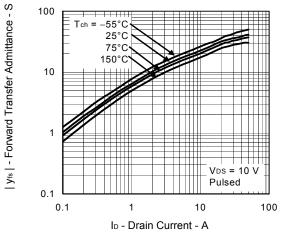


FORWARD TRANSFER CHARACTERISTICS

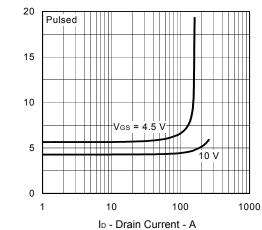
GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE 3 2.5 2 1.5



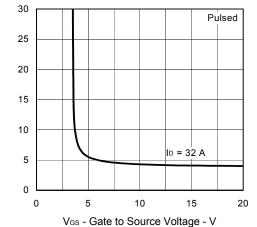
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

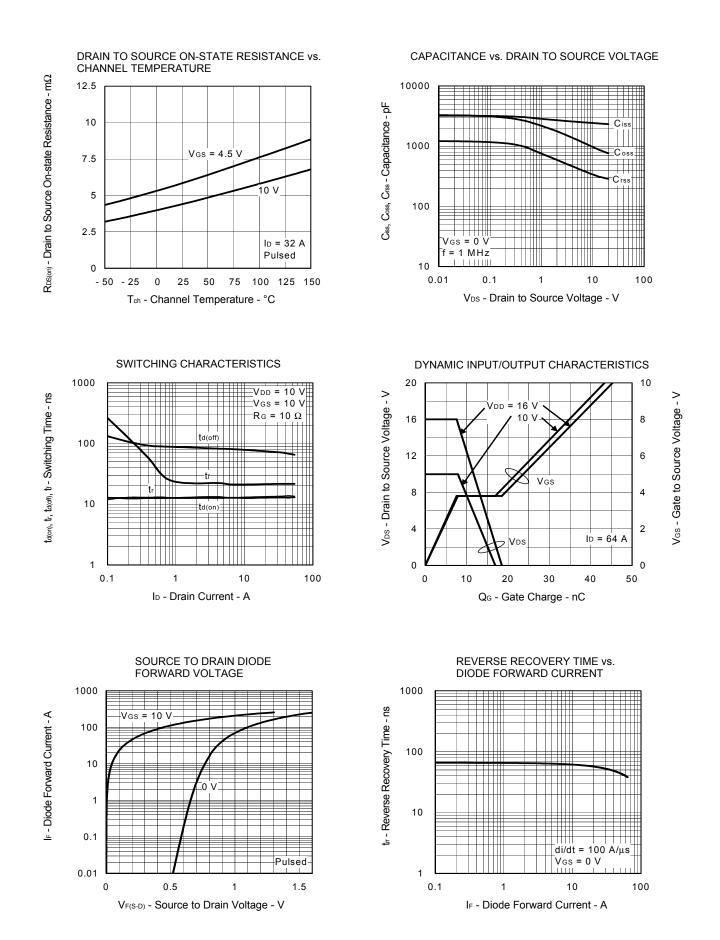


DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



 $R_{\text{DS}(\text{on})}$  - Drain to Source On-state Resistance -  $m\Omega$ 

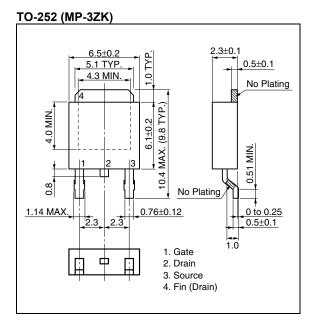
 $R_{DS(on)}$  - Drain to Source On-state Resistance -  $m\Omega$ 



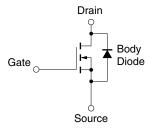
#### Data Sheet D15967EJ3V0DS

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# ★ PACKAGE DRAWING (Unit: mm)



#### **EQUIVALENT CIRCUIT**



**Remark** Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

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