

Order Number 9308DMQB，9308FMQB or DM9308N
See NS Package Number J24A，N24A or W24C

| Pin Names | Description |
| :--- | :--- |
| $D_{0 a}-D_{3 a}$ |  |
| $D_{0 b}-D_{3 b}$ | Parallel Latch Inputs |
| $\bar{E}_{0 a}, \bar{E}_{1 a}, \bar{E}_{0 b}, \bar{E}_{1 b}$ | AND Enable Inputs（Active LOW） |
| $\overline{M R}_{a}, \overline{M R}_{b}$ | Master Reset Inputs（Active LOW） |
| $\bar{Q}_{0 a}-\bar{Q}_{3 a}$ |  |
| $\bar{Q}_{0 b}-\bar{Q}_{3 b}$ |  |$\} \quad$ Parallel Latch Outputs $\quad$.


| Absolute Maximum Ratings (Note) |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications. |  |  |  | Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation. |  |  |  |  |
| Supply Voltage |  | 7V |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |
| Operating Free Air Temperature Range MIL <br> COM |  | $-55^{\circ} \mathrm{C} \text { to }+125^{\circ} \mathrm{C}$ |  |  |  |  |  |  |
| Storage Temperature Range - |  | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |  |  |  |  |  |  |
| Recommended Operating Conditions |  |  |  |  |  |  |  |  |
| Symbol | Parameter | Military |  |  | Commercial |  |  | Units |
|  |  | Min | Nom | Max | Min | Nom | Max |  |
| $\mathrm{V}_{\mathrm{CC}}$ | Supply Voltage | 4.5 | 5 | 5.5 | 4.75 | 5 | 5.25 | V |
| $\mathrm{V}_{\mathrm{IH}}$ | High Level Input Voltage | 2 |  |  | 2 |  |  | V |
| $\mathrm{V}_{\text {IL }}$ | Low Level Input Voltage |  |  | 0.8 |  |  | 0.8 | V |
| IOH | High Level Output Current |  |  | -0.8 |  |  | -0.8 | mA |
| loL | Low Level Output Current |  |  | 16 |  |  | 16 | mA |
| $\mathrm{T}_{\text {A }}$ | Free Air Operating Temperature | -55 |  | 125 | 0 |  | 70 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{t}_{\mathrm{s}}(\mathrm{H})$ | Setup Time HIGH, $\mathrm{D}_{\mathrm{n}}$ to $\bar{E}_{\mathrm{n}}$ | 6 |  |  | 10 |  |  | ns |
| $\mathrm{th}_{\mathrm{h}}(\mathrm{H})$ | Hold Time HIGH, $\mathrm{D}_{\mathrm{n}}$ to $\bar{E}_{n}$ | 4 |  |  | -2.0 |  |  | ns |
| $\mathrm{t}_{\mathrm{s}}(\mathrm{L})$ | Setup Time LOW, $\mathrm{D}_{\mathrm{n}}$ to $\bar{E}_{\mathrm{n}}$ | 10 |  |  | 12 |  |  | ns |
| $t_{\text {h }}(\mathrm{L})$ | Hold Time LOW, $\mathrm{D}_{\mathrm{n}}$ to $\bar{E}_{\mathrm{n}}$ | 4 |  |  | 8 |  |  | ns |
| $\mathrm{t}_{\mathrm{w}}(\mathrm{L})$ | $\bar{E}_{n}$ Pulse Width LOW | 18 |  |  | 18 |  |  | ns |
| $\mathrm{t}_{\mathrm{w}}(\mathrm{L})$ | $\overline{\text { MR }}$ Pulse Width LOW | 18 |  |  | 18 |  |  | ns |
| $\mathrm{t}_{\text {rec }}$ | Recovery Time, $\overline{\mathrm{MR}}$ to $\overline{\mathrm{E}}_{\mathrm{n}}$ | 10 |  |  | 8 |  |  | ns |

Electrical Characteristics over recommended operating free air temperature (unless otherwise noted)

| Symbol | Parameter | Conditions |  | Min | $\begin{gathered} \text { Typ } \\ \text { (Note 1) } \end{gathered}$ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{1}$ | Input Clamp Voltage | $\mathrm{V}_{\mathrm{CC}}=\mathrm{Min}, \mathrm{l}_{\mathrm{I}}=-18 \mathrm{~mA}$ |  |  |  | -1.5 | V |
| $\mathrm{V}_{\mathrm{OH}}$ | High Level Output Voltage | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=\mathrm{Min}, \mathrm{I}_{\mathrm{OH}}=\mathrm{Max}, \\ & \mathrm{~V}_{\mathrm{IL}}=\mathrm{Max}, \mathrm{~V}_{\mathrm{IH}}=\mathrm{Min} \\ & \hline \end{aligned}$ |  | 2.4 |  |  | V |
| $\mathrm{V}_{\mathrm{OL}}$ | Low Level Output Voltage | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=\mathrm{Min}, \mathrm{I}_{\mathrm{OL}}=\operatorname{Max} \\ & \mathrm{V}_{\mathrm{IH}}=\mathrm{Min}, \mathrm{~V}_{\mathrm{IL}}=\mathrm{Max} \end{aligned}$ |  |  |  | 0.4 | V |
| 1 | Input Current @ Max Input Voltage | $\mathrm{V}_{\mathrm{CC}}=\mathrm{Max}, \mathrm{V}_{\mathrm{I}}=5.5 \mathrm{~V}$ |  |  |  | 1 | mA |
| $\mathrm{I}_{\mathrm{H}}$ | High Level Input Current | $\mathrm{V}_{\mathrm{CC}}=\mathrm{Max}, \mathrm{V}_{\mathrm{I}}=2.4 \mathrm{~V}$ |  |  |  | 40 | $\mu \mathrm{A}$ |
| IIL | Low Level Input Current | $\mathrm{V}_{\mathrm{CC}}=\mathrm{Max}, \mathrm{V}_{\mathrm{I}}=0.4 \mathrm{~V}$ |  |  |  | -1.6 | mA |
| los | Short Circuit Output Current | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=\mathrm{Max} \\ & (\text { Note 2) } \end{aligned}$ | MIL | -20 |  | -70 | mA |
|  |  |  | COM | -20 |  | -57 |  |
| ICC | Supply Current | $\mathrm{V}_{\text {CC }}=\mathrm{Max}$ (Note 3) |  |  |  | 100 | mA |

Note 1: All typicals are at $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$.
Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.
Note 3: $I_{C C}$ is measured with all outputs open and all inputs grounded.

## Functional Description

Data can be entered into the latch when both of the enable inputs are LOW. As long as this logic condition exists, the output of the latch will follow the input. If either of the enable inputs goes HIGH, the data present in the latch at that time is held in the latch and is no longer affected by data input. The master reset overrides all other input conditions and forces the outputs of all the latches LOW when a LOW signal is applied to the Master Reset input.

Truth Table

| $\overline{\mathrm{MR}}$ | E0 | E1 | D | Qn | Operation |
| :---: | :---: | :---: | :---: | :---: | :---: |
| H | L | L | L | L | Data Entry |
| H | L | L | H | H | Data Entry |
| H | L | H | X | Qn-1 | Hold |
| H | H | L | X | Qn-1 | Hold |
| H | H | H | X | Qn-1 | Hold |
| L | X | X | X | L | Reset |

$Q_{n-1}=$ Previous Output State
$\mathrm{Q}_{\mathrm{n}}=$ Present Output State
$\mathrm{H}=\mathrm{HIGH}$ Voltage Level
L = LOW Voltage Level
$X=$ Immaterial

## Logic Diagram



TL/F/10208-3

Switching Characteristics $\mathrm{V}_{\mathrm{CC}}=+5.0 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ (See Section 5 for test waveforms and output load.)

| Symbol | Parameter | 9308 |  | Units |
| :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \mathrm{C}_{\mathrm{L}}=15 \mathrm{pF} \\ & \mathrm{R}_{\mathrm{L}}=400 \Omega \end{aligned}$ |  |  |
|  |  | Min | Max |  |
| $\begin{aligned} & \text { tpLH } \\ & \mathrm{t}_{\mathrm{PHLL}} \\ & \hline \end{aligned}$ | Propagation Delay En to Qn |  | $\begin{aligned} & 30 \\ & 22 \\ & \hline \end{aligned}$ | ns |
| $t_{\text {PLH }}$ <br> $\mathrm{t}_{\mathrm{PHL}}$ | Propagation Delay Dn to Qn |  | $\begin{aligned} & 15 \\ & 18 \\ & \hline \end{aligned}$ | ns |
| $\mathrm{t}_{\text {PHL }}$ | Propagation Delay MR to Qn |  | 22 | ns |




Physical Dimensions inches (millimeters) (Continued)


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| National Semiconductor Corporation <br> 1111 West Bardin Road <br> Arlington, TX 76017 <br> Tel: 1(800) 272-9959 <br> Fax: 1(800) 737-7018 | National Semiconductor <br> Europe <br> Fax: (+49) 0-180-530 8586 <br> Email: cnjwge@tevm2.nsc.com <br> Deutsch Tel: (+49) 0-180-530 8585 <br> English Tel: $(+49)$ 0-180-532 7832 <br> Français Tel: (+49) 0-180-532 9358 <br> Italiano Tel: $(+49)$ 0-180-534 1680 | National Semiconductor Hong Kong Ltd. <br> 13th Floor, Straight Block, Ocean Centre, 5 Canton Rd. Tsimshatsui, Kowloon Hong Kong <br> Tel: (852) 2737-1600 <br> Fax: (852) 2736-9960 | National Semiconductor Japan Ltd. <br> Tel: 81-043-299-2309 <br> Fax: 81-043-299-2408 |
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